Board Dimensions and Board Origin

Board Origin (x=0, y=0)
Components centroid data (x,y) reference point

Notes:
1. All dimensions are in mm.
2. All holes are \( \Phi \) 2.70 +/− 0.10 and shall be surrounded by a \( \Phi \) 6.00 keep-out area.
3. There is a 2.00 mm keep-out area along front edges which shall be kept.
4. The origin of the card (0,0) is the centre of voltage keying hole.