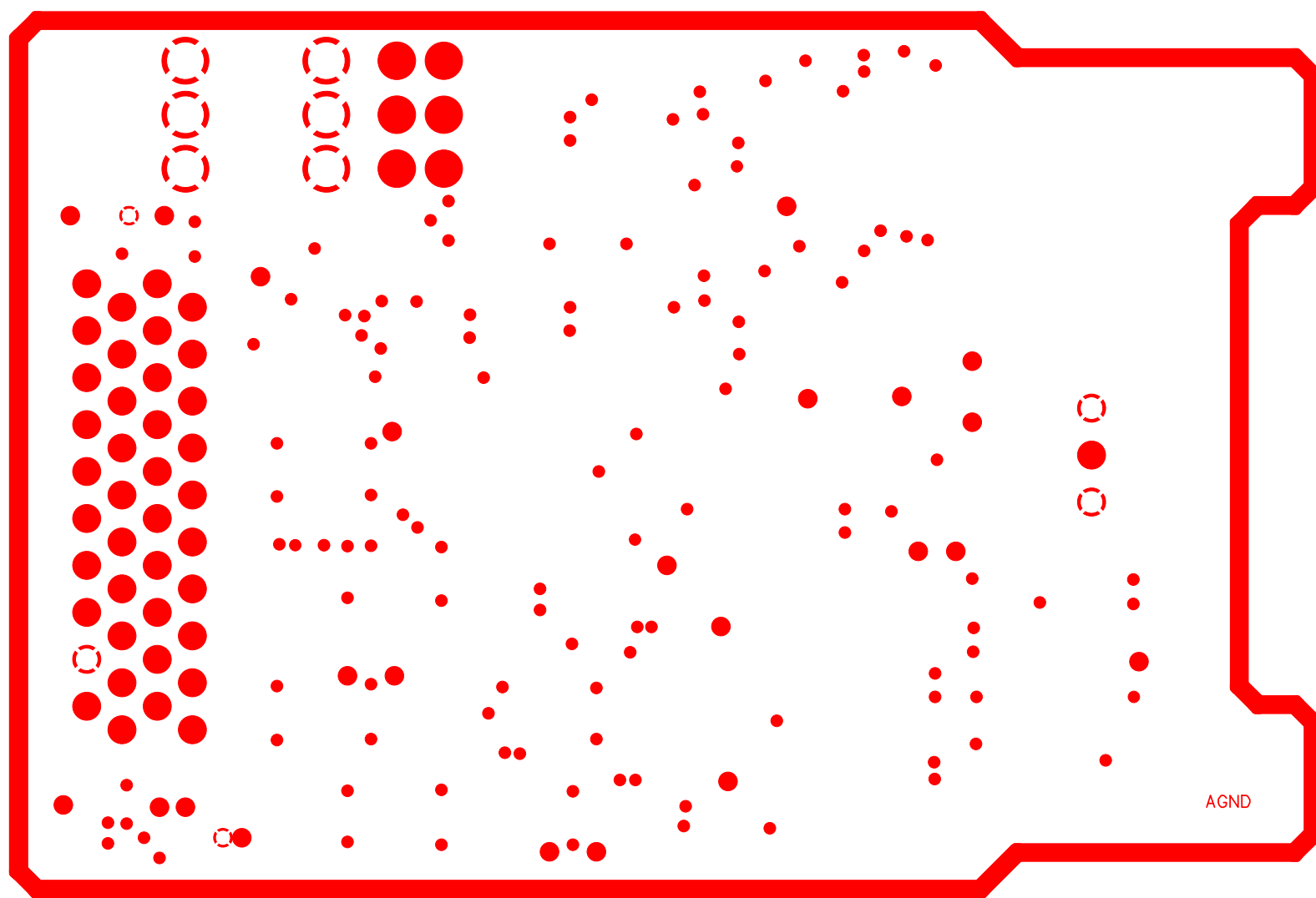
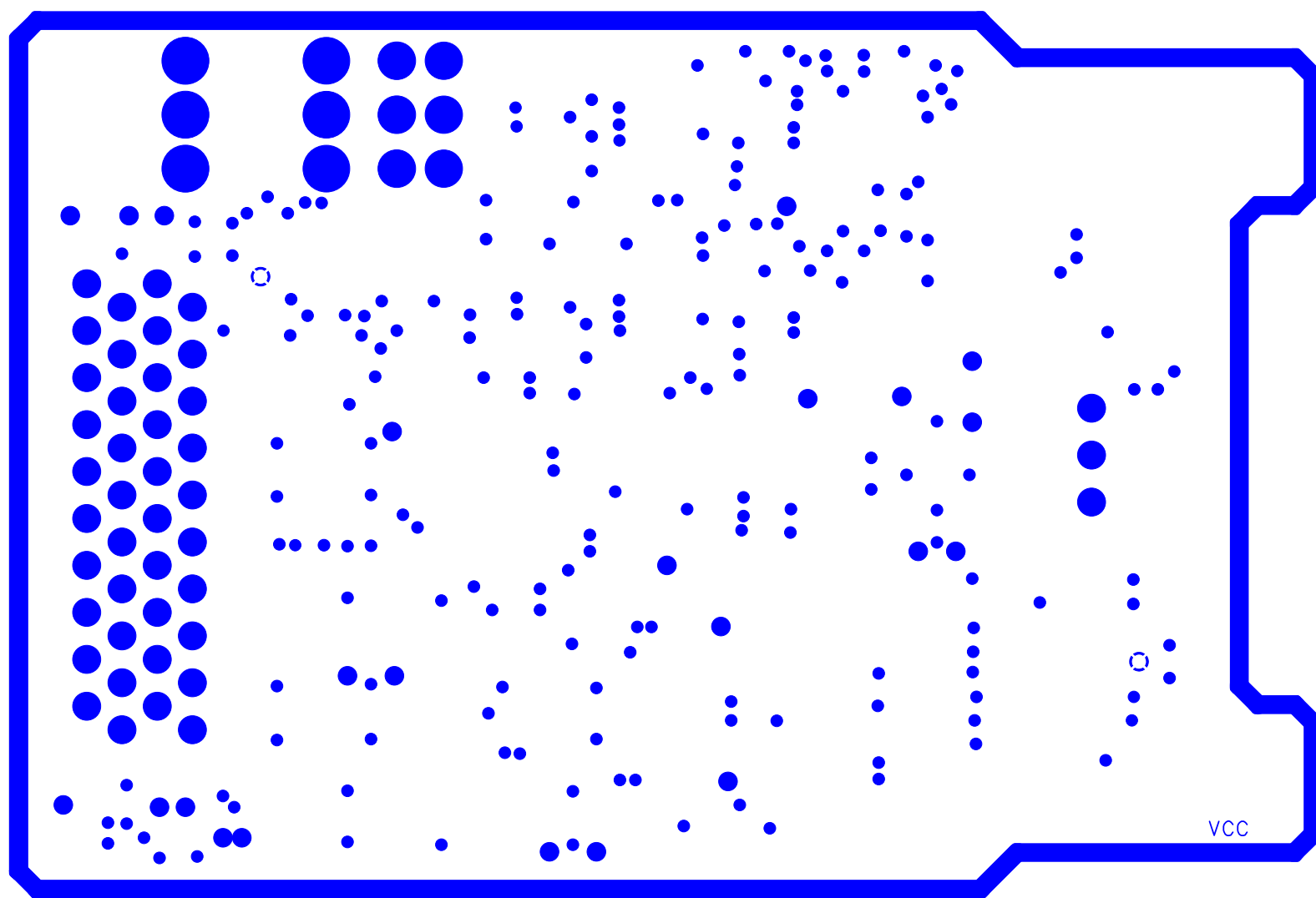


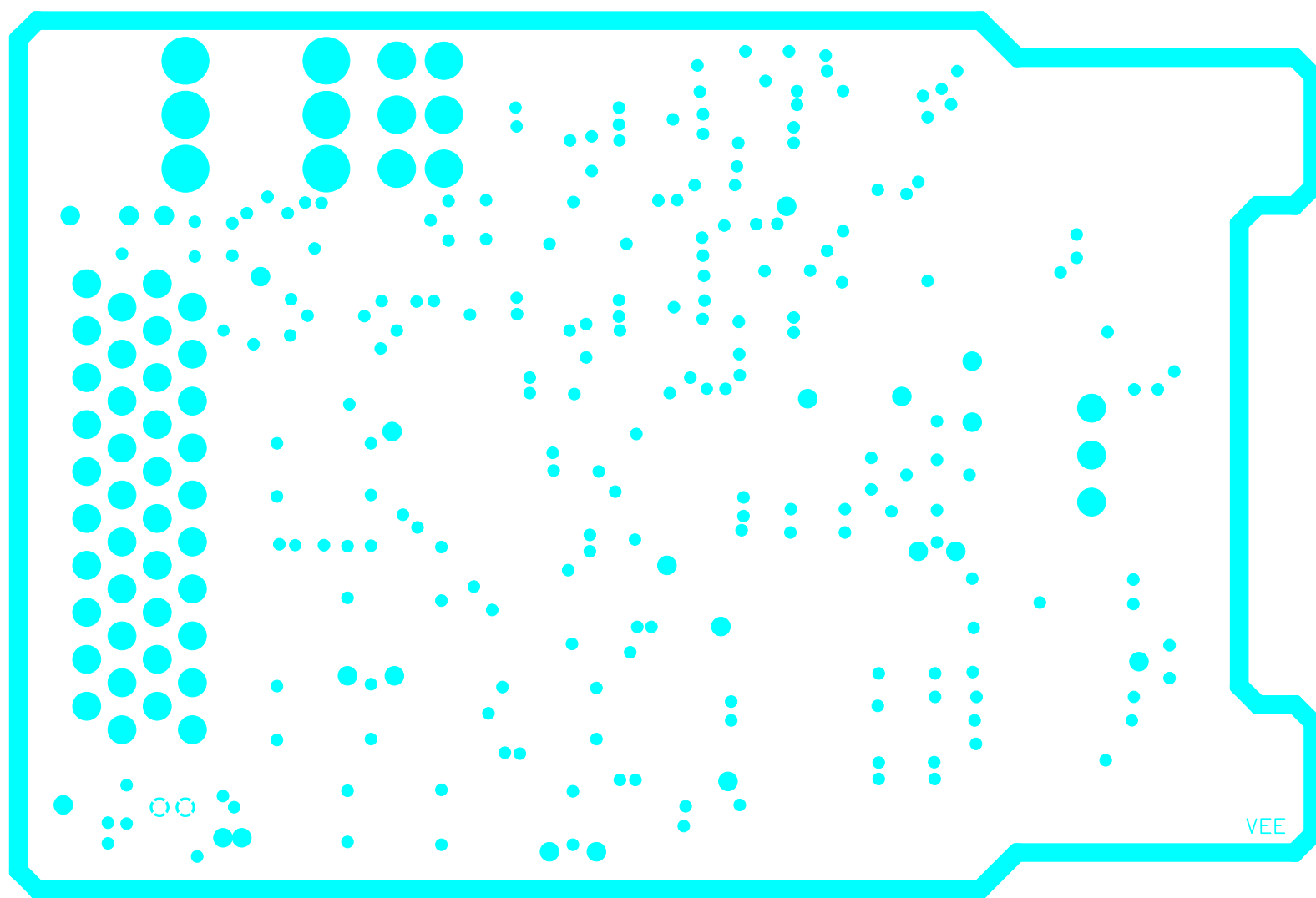
ARTWORK_1: LAYER1 (Top, SIGNAL_1)



ARTWORK_2: LAYER2 (AGND)



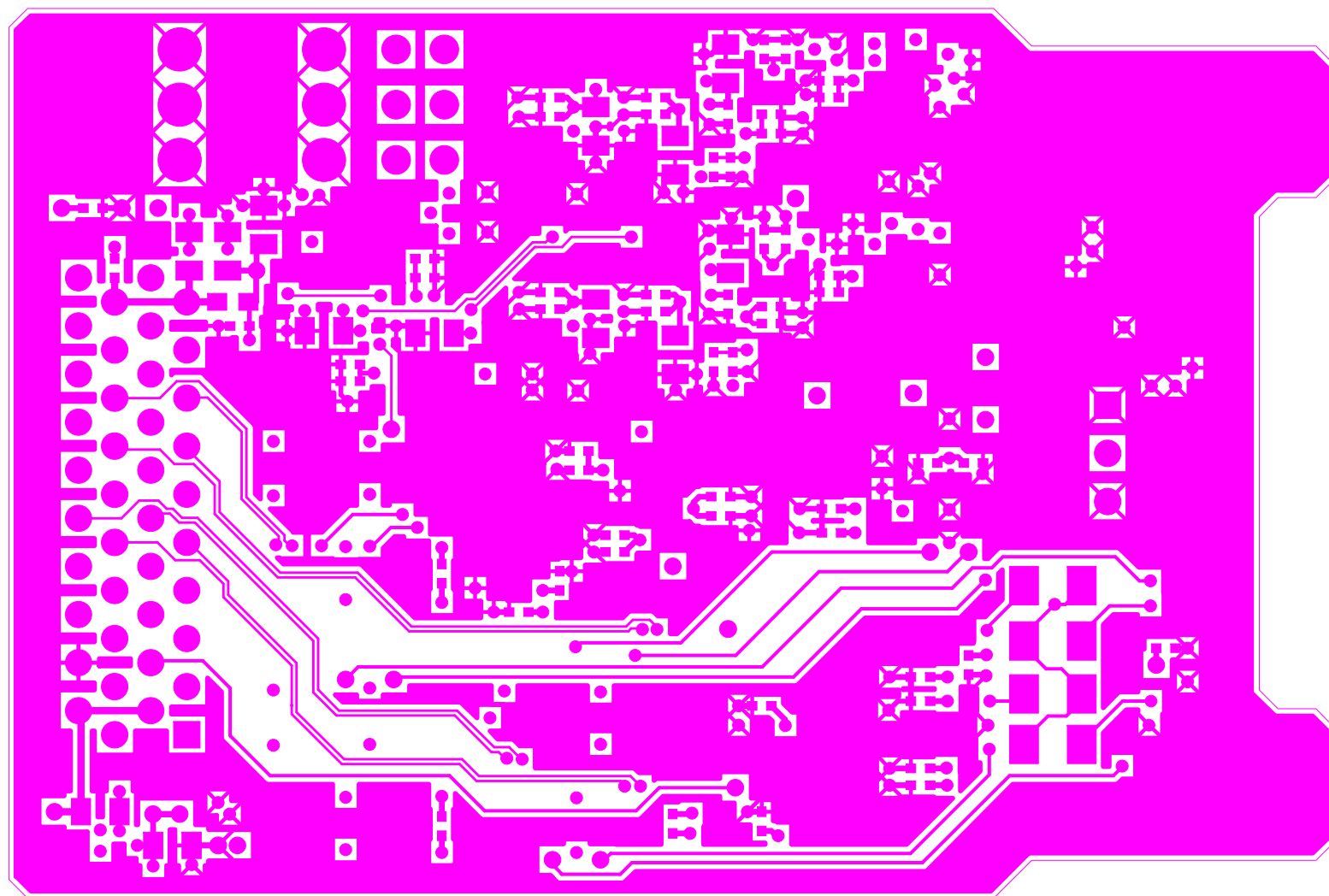
ARTWORK_3: LAYER3 (VCC)



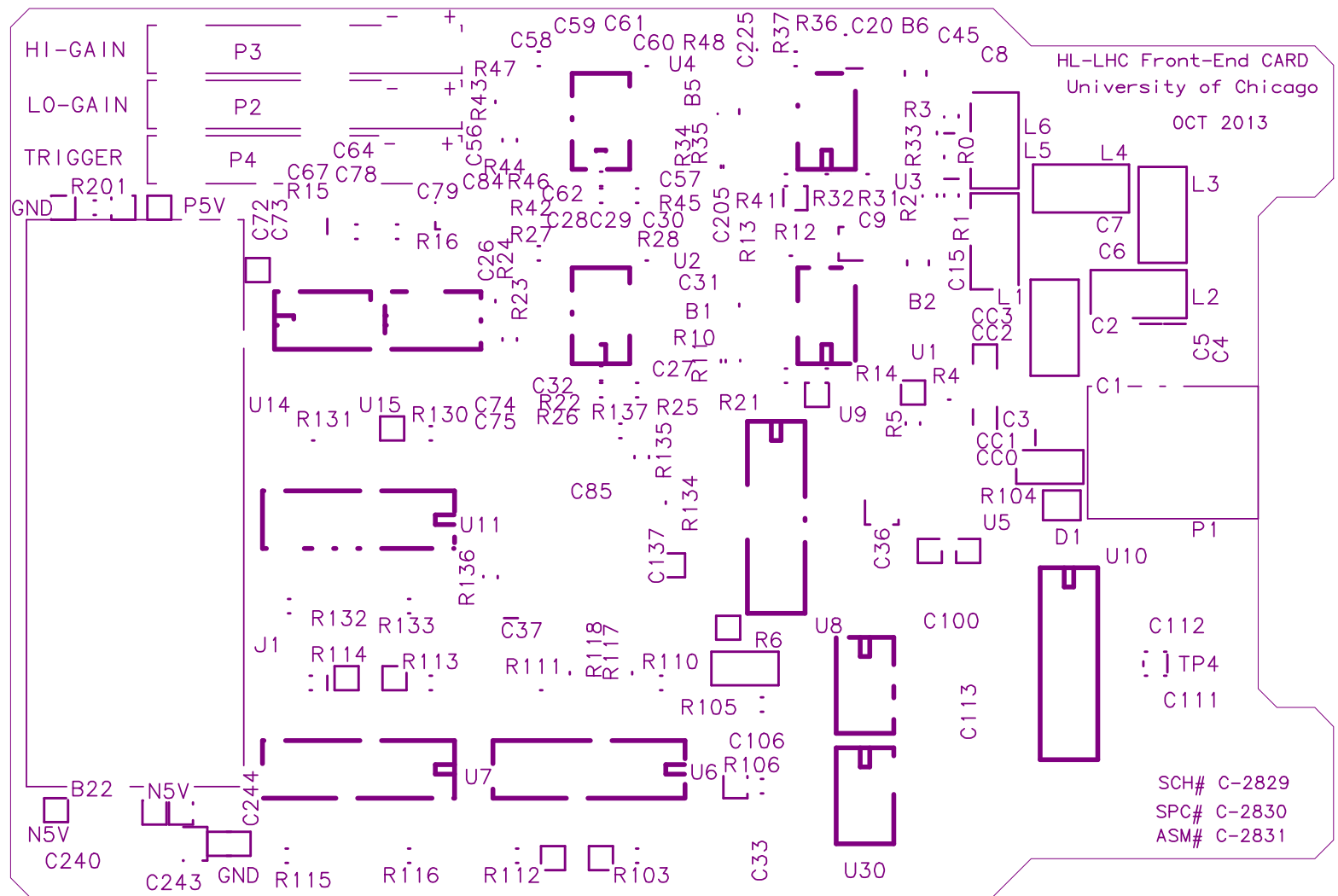
ARTWORK_4: LAYER4 (VEE)



ARTWORK_5: LAYER5 (GROUND)

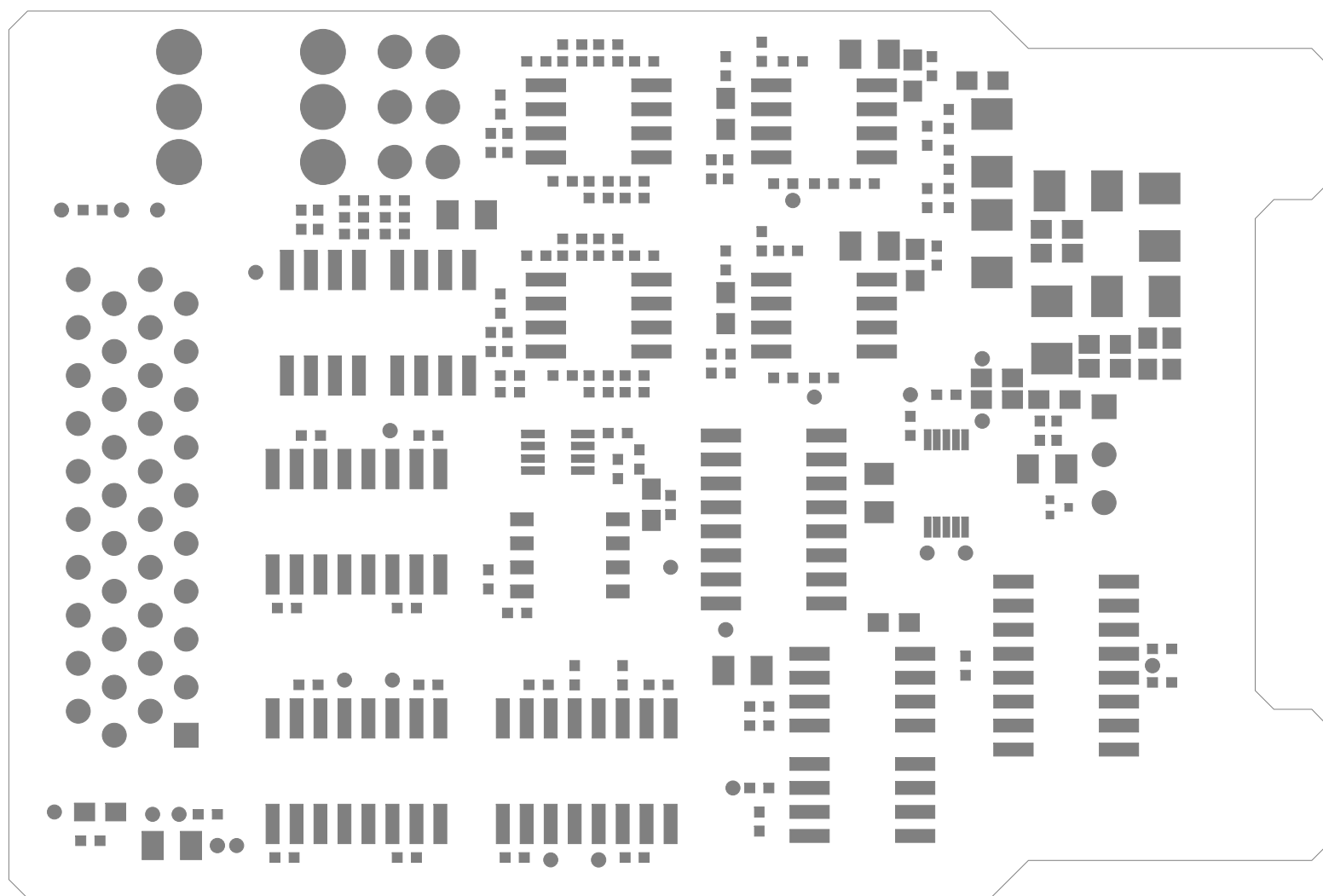


ARTWORK_6: LAYER6 (Bot, Signal_2)

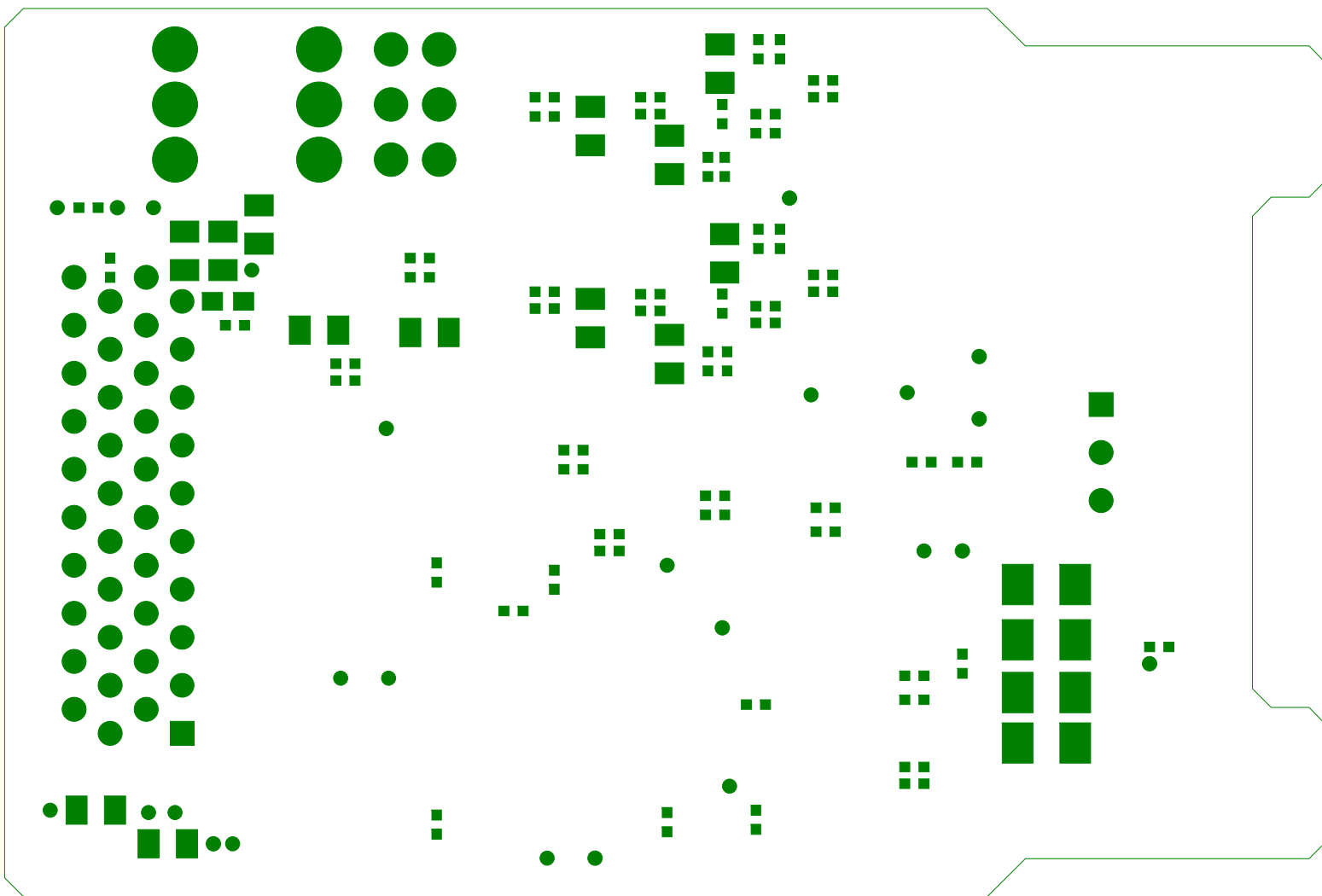


TP18
TP6
TP10
TP8
TP13
TP7
TP2
TP9
TP15
TP1
TP12
TP5
TP21
TP3
TP30
TP12
TP16
TP17
TP19
TP4
TP11
TP151

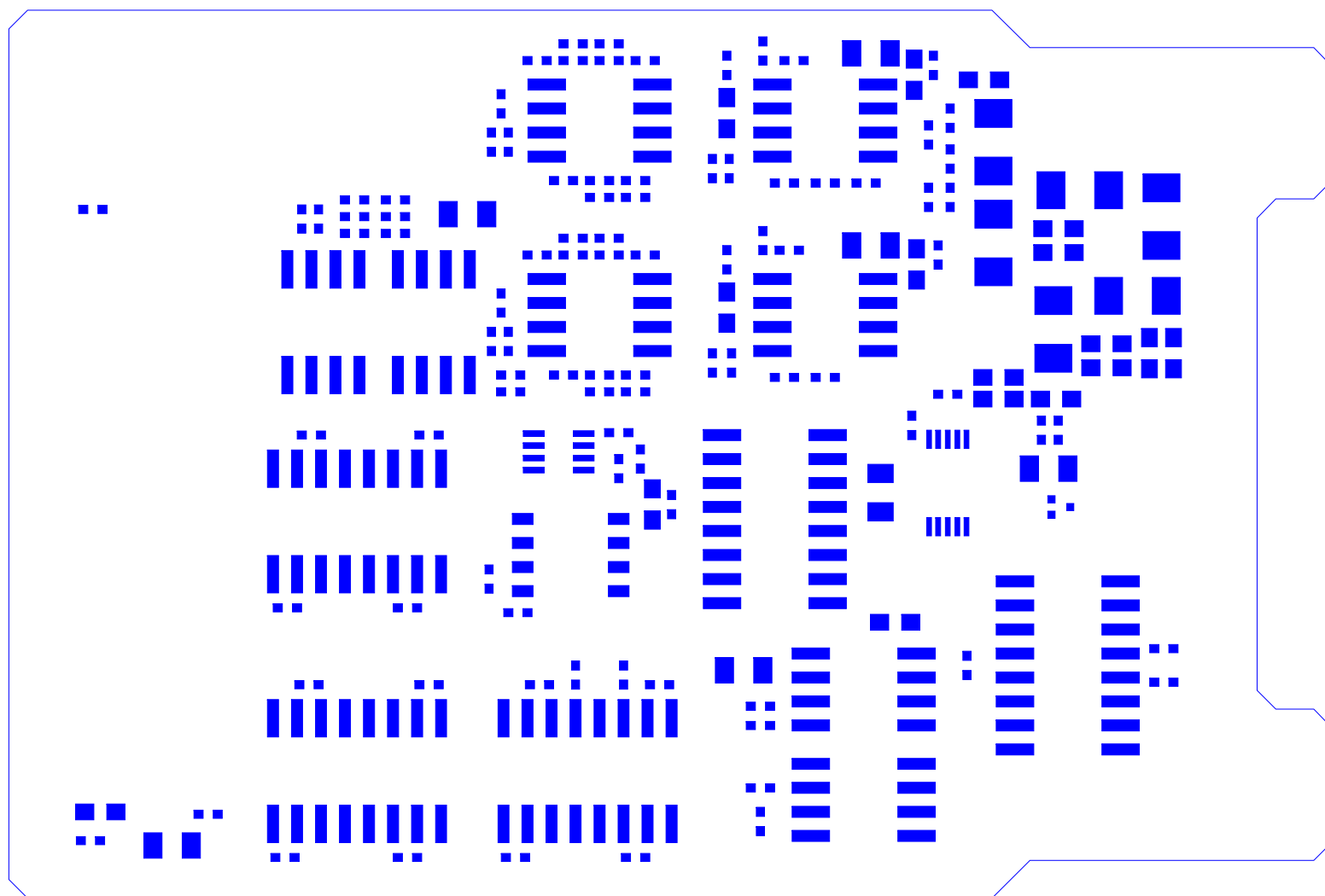
ARTWORK_7: Top Silkscreen (SILK_SCREEN_1)



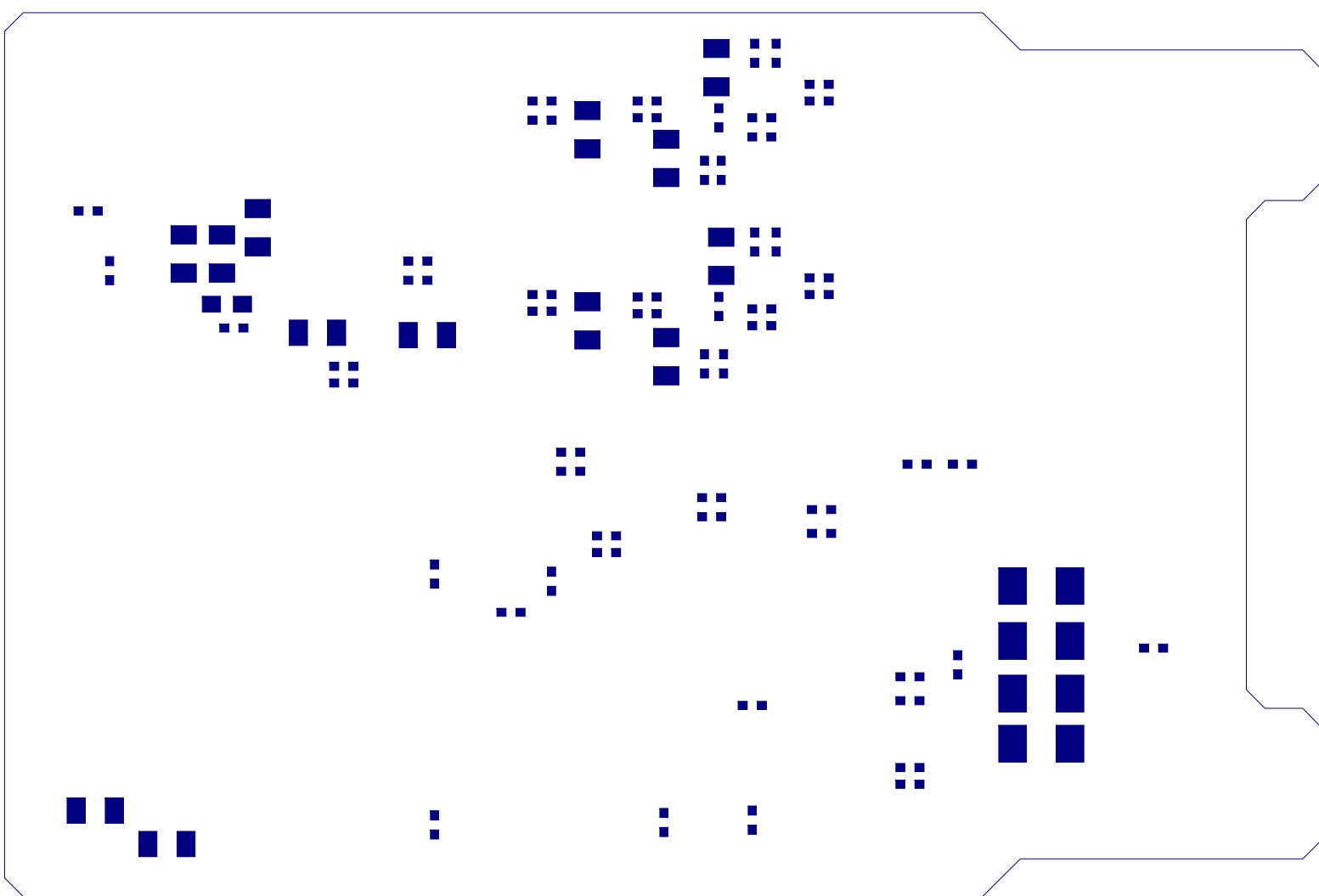
ARTWORK_9: Top Solder Mask (SOLDER_MASK_1)



ARTWORK_10: Bottom Solder Mask (SOLDER_MASK_2)



ARTWORK_11: Top Paste Mask (PASTE_MASK_1)



ARTWORK_12: Bottom Paste Mask (PASTE_MASK_2)

