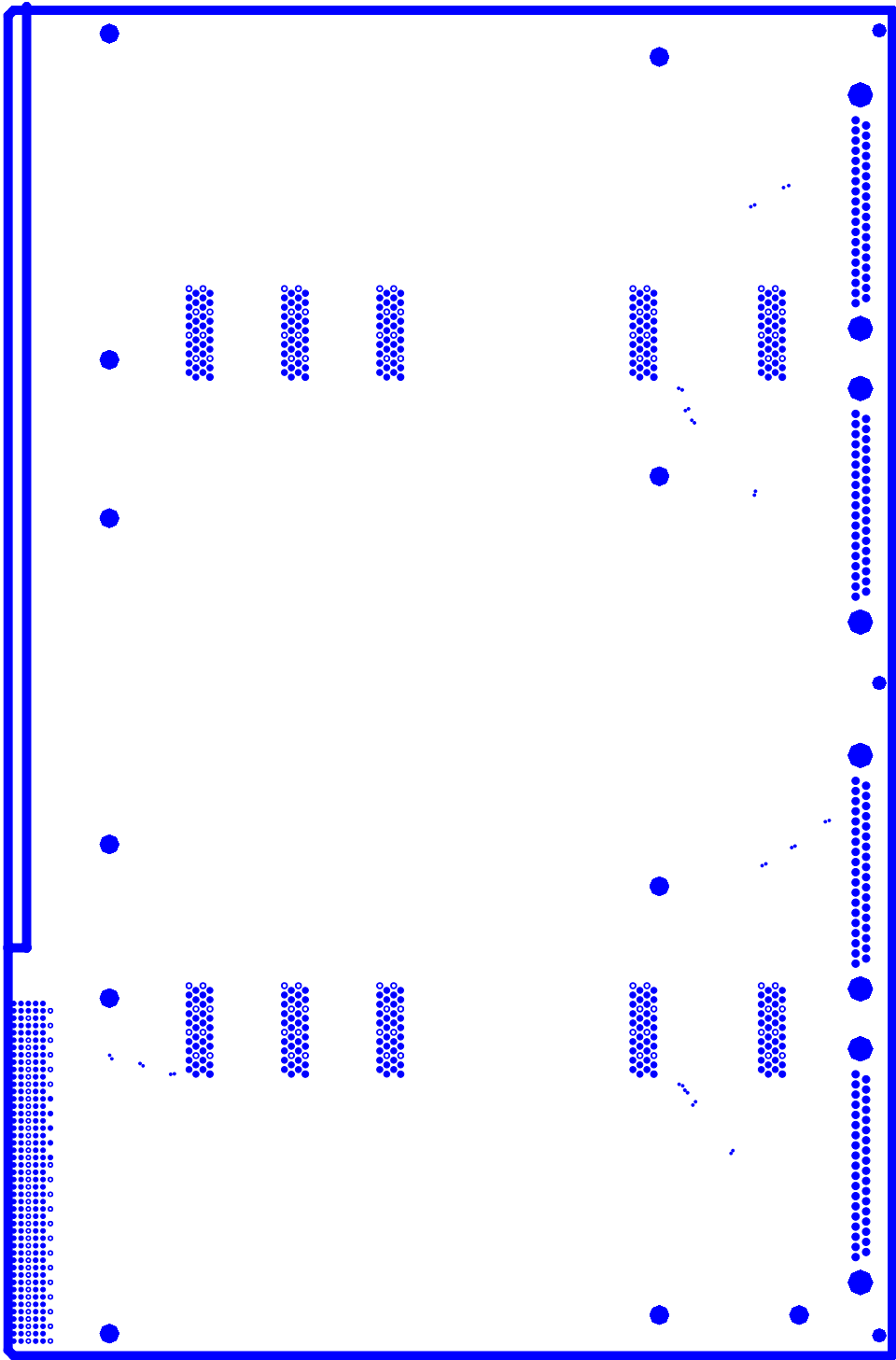
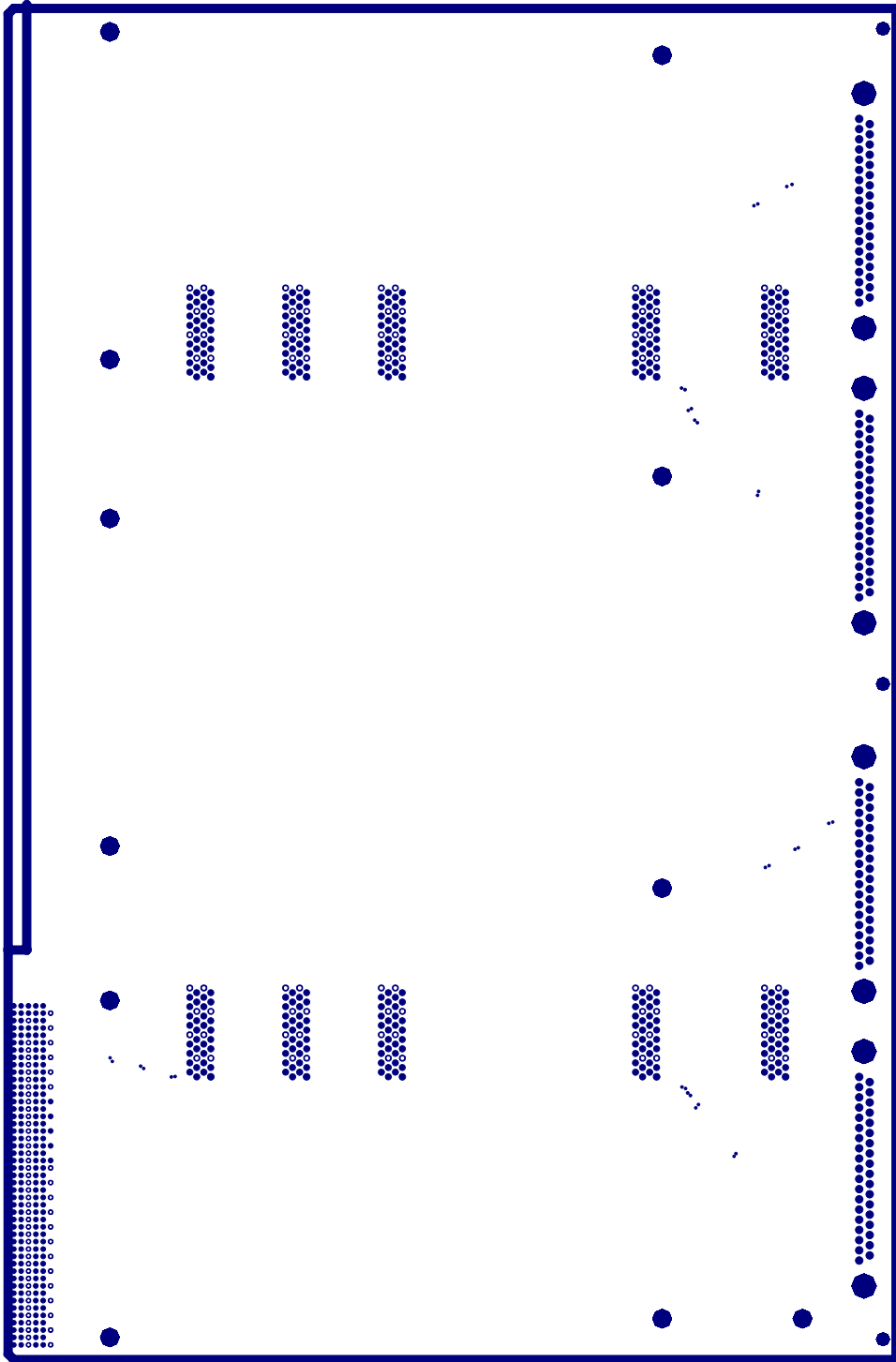


ARTWORK_1: Top Component Layer (Signal_1, PAD_1)



ARTWORK_2: POWER PLANE VCC (POWER_1)
ARTWORK_3: POWER PLANE VCC (POWER_1)

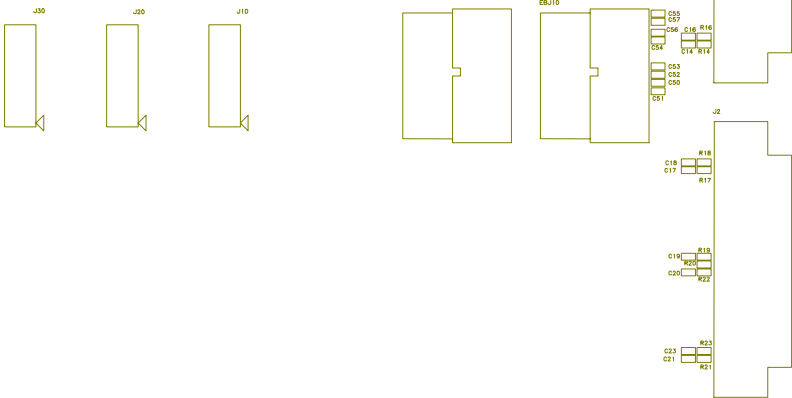


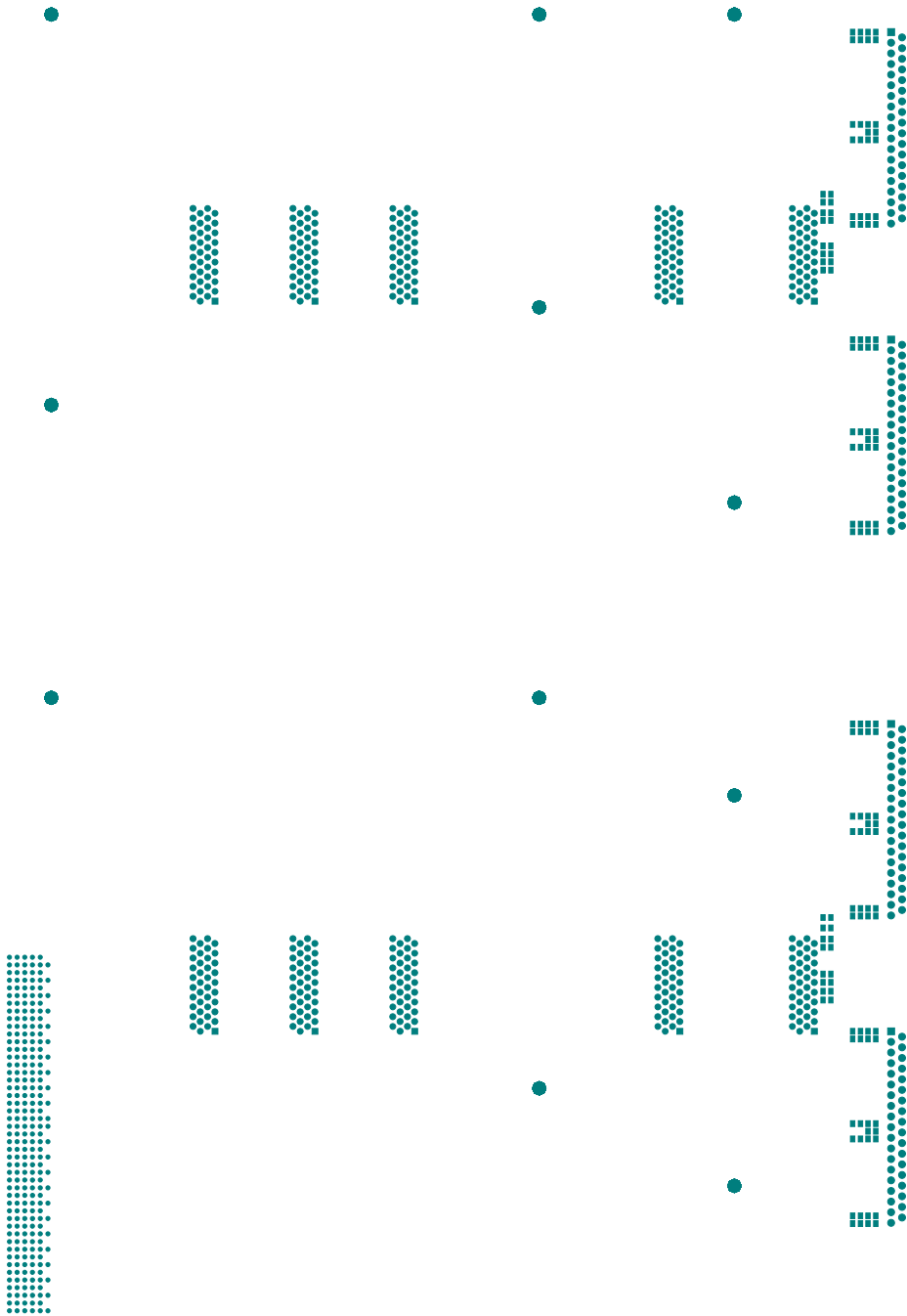
ARTWORK_2: POWER PLANE VCC (POWER_1)

ARTWORK_3: POWER PLANE VCC (POWER_1)

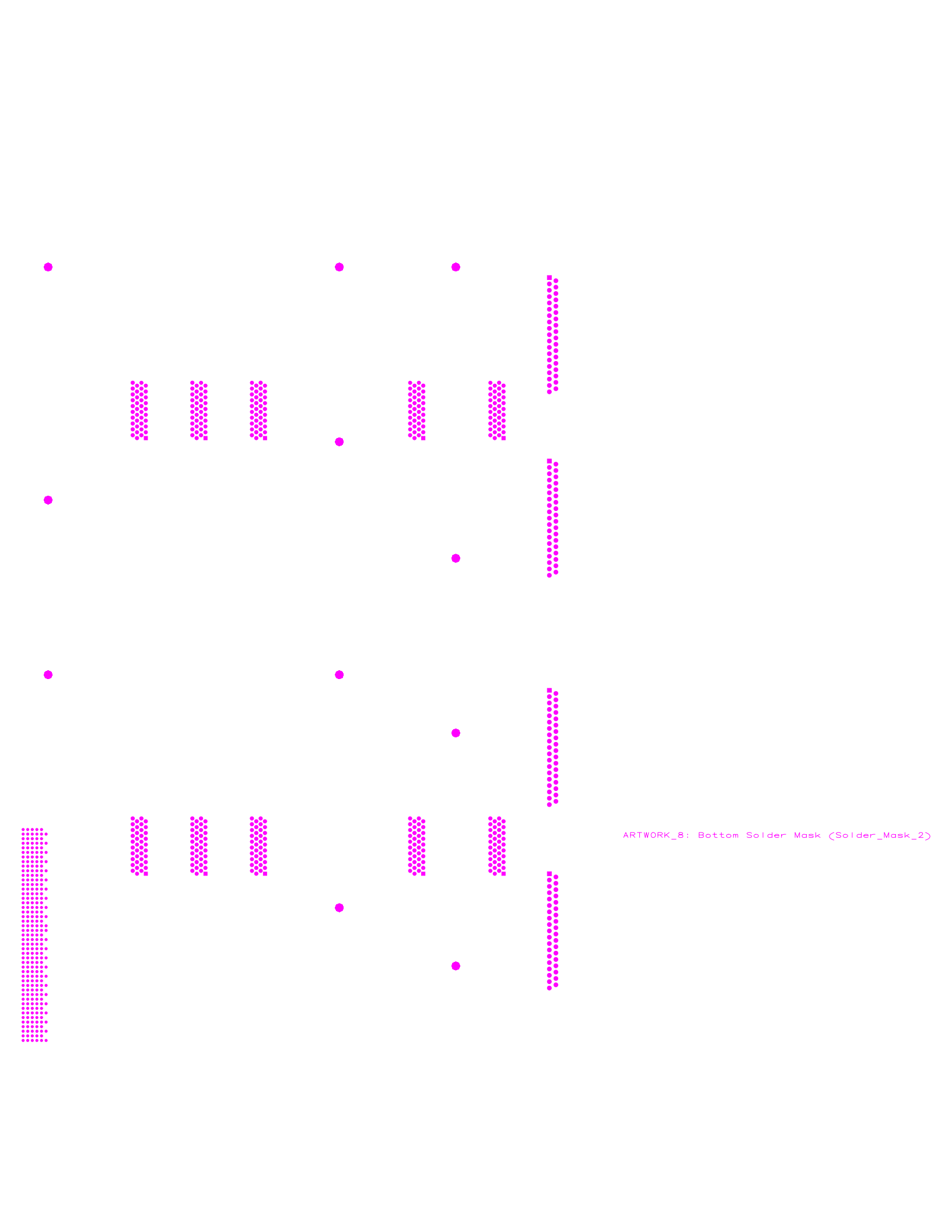
SCH# B-2544V2
SPC# B-2545V2
ASM# B-2546V2

AUG. 2005





ARTWORK_7: Top Solder Mask (Solder_Mask_1)





ARTWORK_9: Top Paste Mask (Paste_Mask_1)

ARTWORK_10: Bottom Paste Mask (Paste_Mask_2

