Board Dimensions and Board Origin

Board Origin (x=0, y=0)
Components centroid data (x, y) reference point
Top Side Component Placement Drawing

[Diagram of a top side component placement drawing with various components labeled.]

The drawing includes labels for components such as ATLAS SCT S-LINK INTERFACE, U1, U2, U4, and others. It also features a note indicating the drawing was created by CERN.

Additional labels include POWER, TEST MODE, LINK UP, DATA_ERROR, XOFF, and ACTIVITY.

A small note at the bottom left indicates that the drawing is for 3 JY Supply Only.