Rows Z and D are in accordance with the VME64 Extensions standard. Note: the positioning of rows A, B and C should still be in accordance with the VME Standard.

All user-defined signals on P2 are 1-to-1 TTL excluding:

a. Geographical Addresses are pulled up/grounded wires
b. 132ns Clock is differential PECL: CLK - DCLK

c. ERROR* is open collector TTL