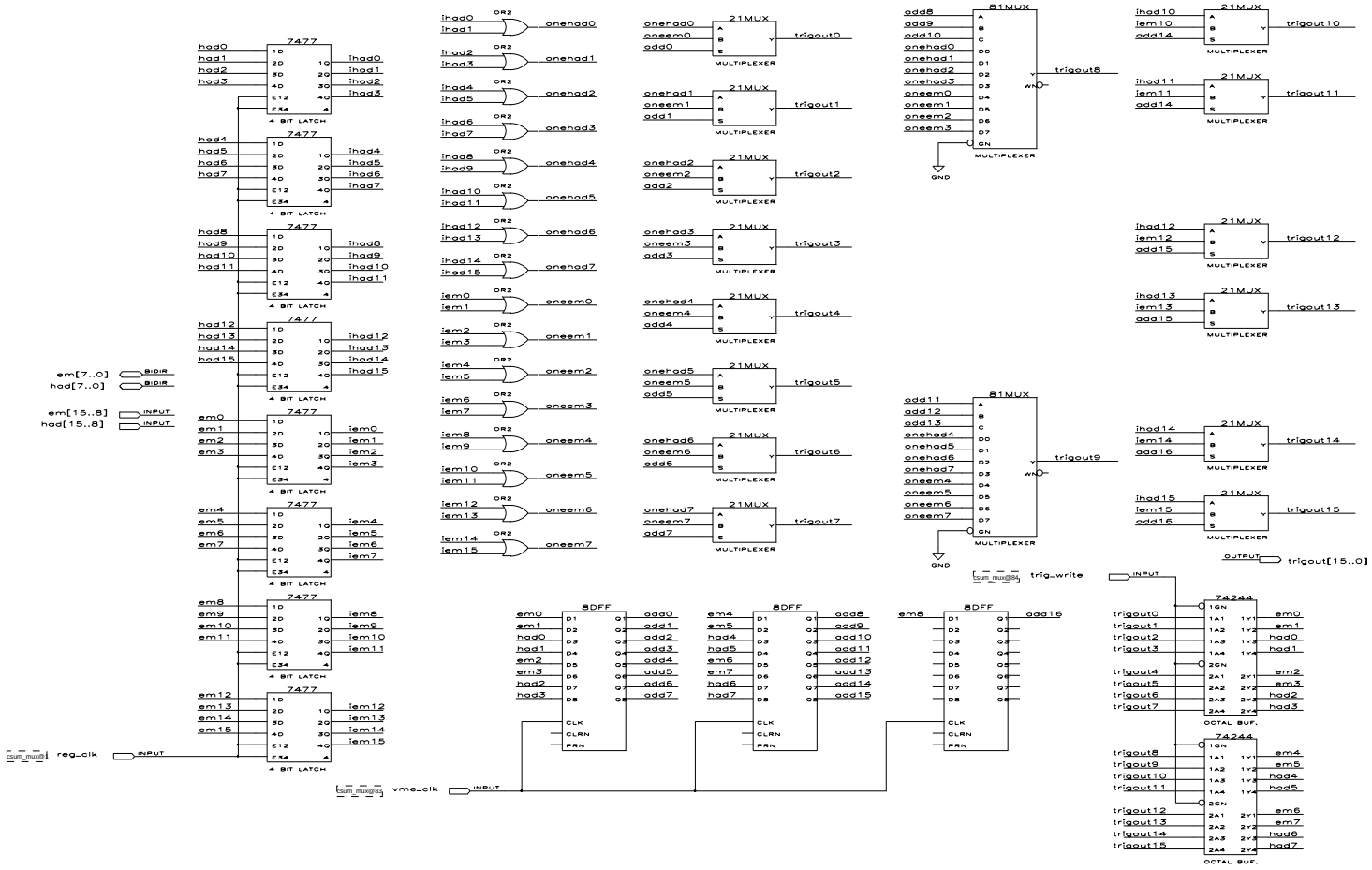
7477
4 BIT LATCH
OR2
21MUX
MULTIPLEXER
81MUX
8DFF
74244
OCTAL BUF.
BIDIR
INPUT
OUTPUT
GND
em[7..0]
had[7..0]
em[15..8]
had[15..8]
reg_clk
vme_clk
trig_write
trigout[15..0]