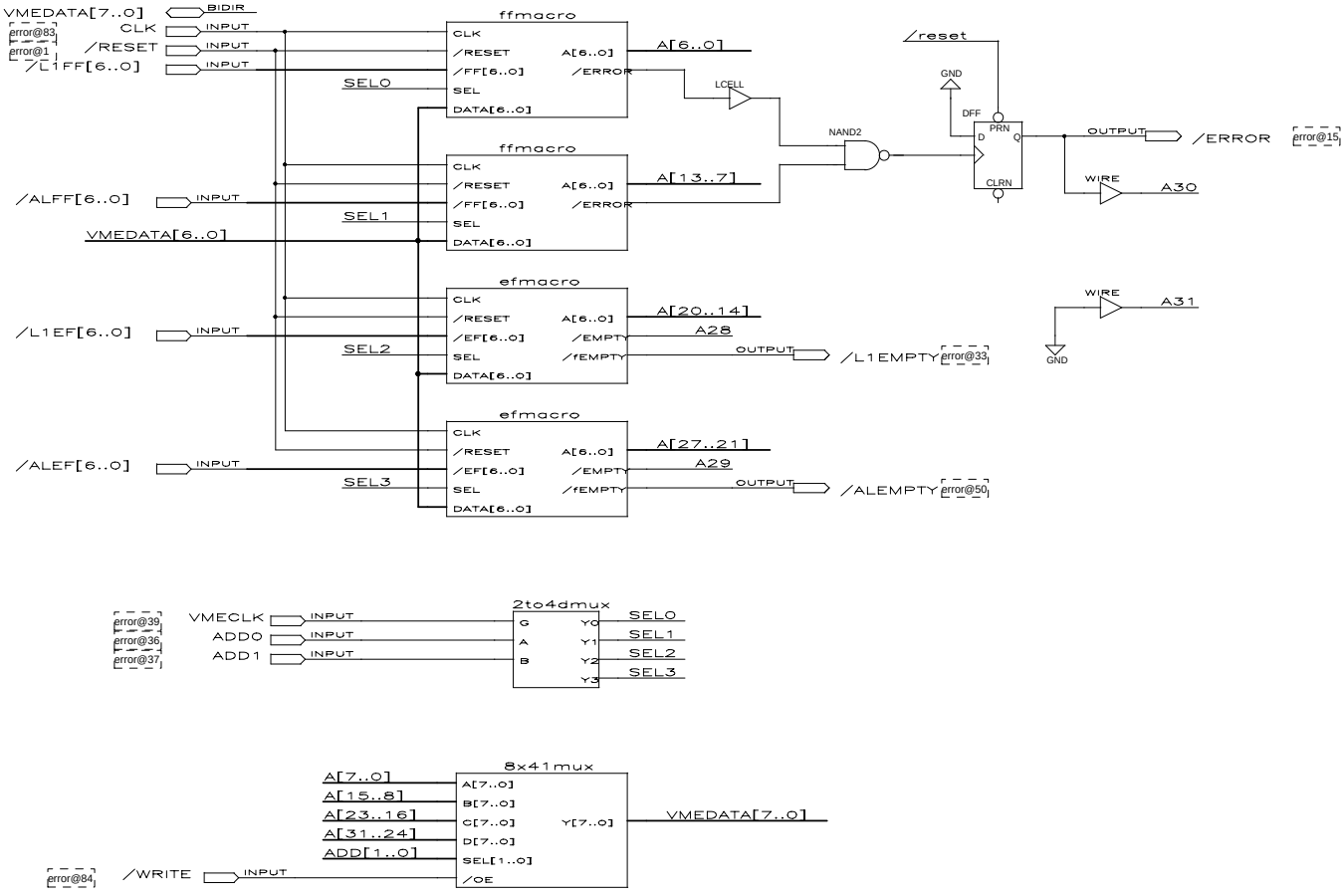
VMEDATA[7..0]
BIDIR
error@83
CLK
INPUT
error@1
/RESET
INPUT
/L1FF[6..0]
INPUT
ffmacro
CLK
/RESET
/FF[6..0]
SEL
DATA[6..0]
A[6..0]
/ERROR
SEL0
LCELL
/reset
GND
DFF
PRN
D
Q
CLRN
NAND2
OUTPUT
/ERROR
error@15
WIRE
A30
ffmacro
A[13..7]
/ALFF[6..0]
INPUT
SEL1
VMEDATA[6..0]
efmacro
A[20..14]
A28
/L1EF[6..0]
INPUT
SEL2
/EMPTY
/fEMPTY
OUTPUT
/L1EMPTY
error@33
WIRE
A31
GND
efmacro
A[27..21]
A29
/ALEF[6..0]
INPUT
SEL3
OUTPUT
/ALEMPTY
error@50
error@39
error@36
error@37
VMECLK
ADD0
ADD1
INPUT
2to4dmux
G
A
B
Y0
Y1
Y2
Y3
SEL0
SEL1
SEL2
SEL3
A[7..0]
A[15..8]
A[23..16]
A[31..24]
ADD[1..0]
8x41mux
A[7..0]
B[7..0]
C[7..0]
D[7..0]
SEL[1..0]
/OE
Y[7..0]
VMEDATA[7..0]
error@84
/WRITE
INPUT