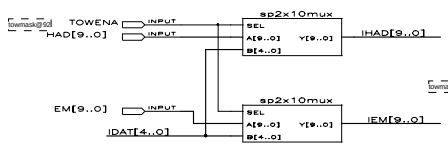
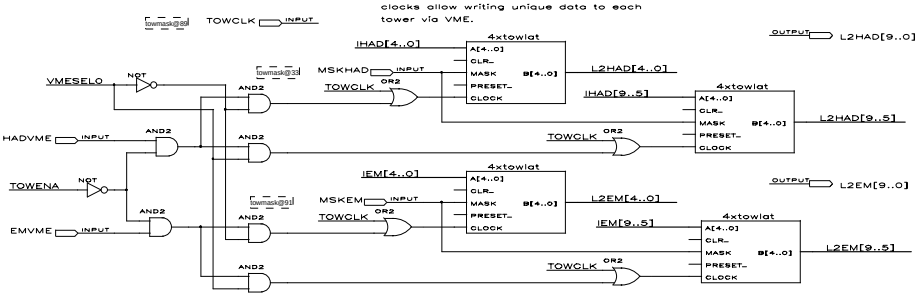
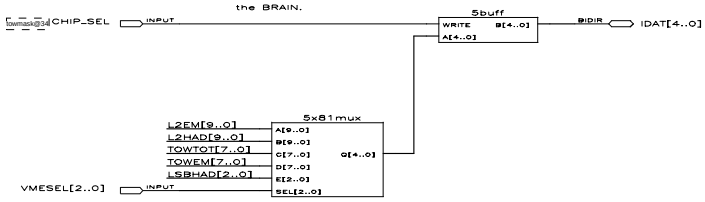
Special MUX to select real data or VME data based on the state of TOWENA.



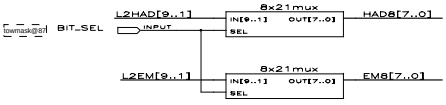
Actual Tower Latches. MSK* provides the ability to keep a tower off during data taking. TOWCLK is the clock that comes during data taking(thus it clocks both towers). The other two clocks allow writing unique data to each tower via VME.



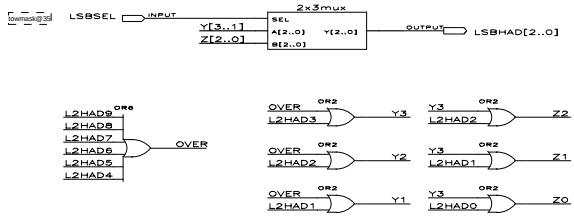
MUX for putting all output data back onto the VMEbus data lines. Requires CHIP_SEL and 3 selector lines from the BRAIN.

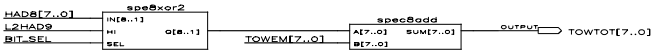


THIS SECTION SELECTS THE 8 BITS TO USE AS THE L1 TOWER ENERGIES. tHERE ARE 2 OPTIONS, EITHER THE 8 msbS OR BITS 1..8. tHE ABILITY TO USE BITS 7..0 IS NOT IMPLEMENTED.



This section selects the 3 bits of HAD energy to be passed to the MLU for had/em. The set of OR gates make sure that if the energy is above either set of 3 bits that all 3 bits are turned on. LSBSEL is provided by a register in the BRAIN.



L2EM bit 9 mIGHT be ORed with the other bits to form the tower energy. (overflow)

Produces HAD + EM (TOT) for this tower. This is really only an 8 bit sum, THIS SUM IS PRODUCED AFTER THE PROPER BITS ARE CREATED BY ORING THE msbs. tHE CARRY OF THE SUM IS THEN ORED WITH THE OTHER 8 BITS TO PRODUCE THE PROPER OUTPUT.