



Rows Z and D are in accordance with the VME64 Extensions standard. Note: the positioning of rows A, B and C should be in accordance with the VME Standard.

All user-defined signals on P2 are totem-pole TTL excluding:  
 a. Geographical Addresses are pulled-up/grounded wires  
 b. 132ns Clock is differential PECL: CLK - CLK\*  
 c. ERROR\* is open collector TTL

DATE	REVISION DESCRIPTION
16 Dec 93	Dr-0: final design -- YF
31 Aug 95	Add row Z and D to conform to the VME64 Ext. Spec. Corrections to conform to new CDF 72 Specs.
18 Nov 95	3rd Prototypes - Bus Files

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		TITLE
VMEbus_P2		
SHEET	12 OF 13	C-2374
DATE	18 Nov 1995	REV B
DRWN	JM	