

Sheet 6,
Lights(6:0)

Sheet 8,
Lights(6:0)

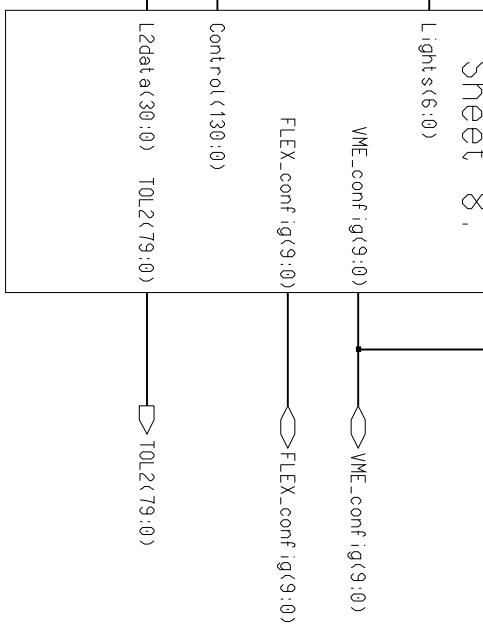
(7:0)
Data(31:0)
Control(130:0)

VME_conf ig(9:0)

FLEX_conf ig(9:0)

Control(130:0)

L2data(30:0) TOL2(79:0)

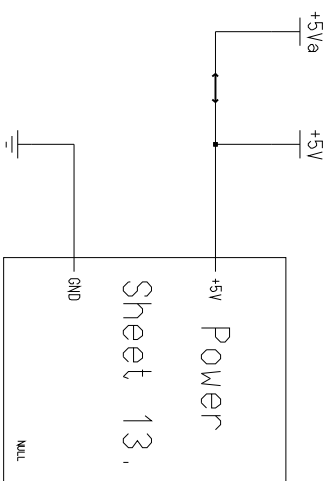


(26:2) Control(130:0)
VME_Data(31:0)

VME_Data(31:0) L2data(30:0)
Control(130:0)

Sheet 10,
FRED_logic

Sheet 12,
DAQ_Interface



DATE REVISION DESCRIPTION

10 Oct 97 Add 3 MSBs to VME_Address bus

TITLE
PrøFRED

UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

SHEET 1 OF 13
DATE 12 May 1999
DRWN alexis / mince

C-2392
REV B

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