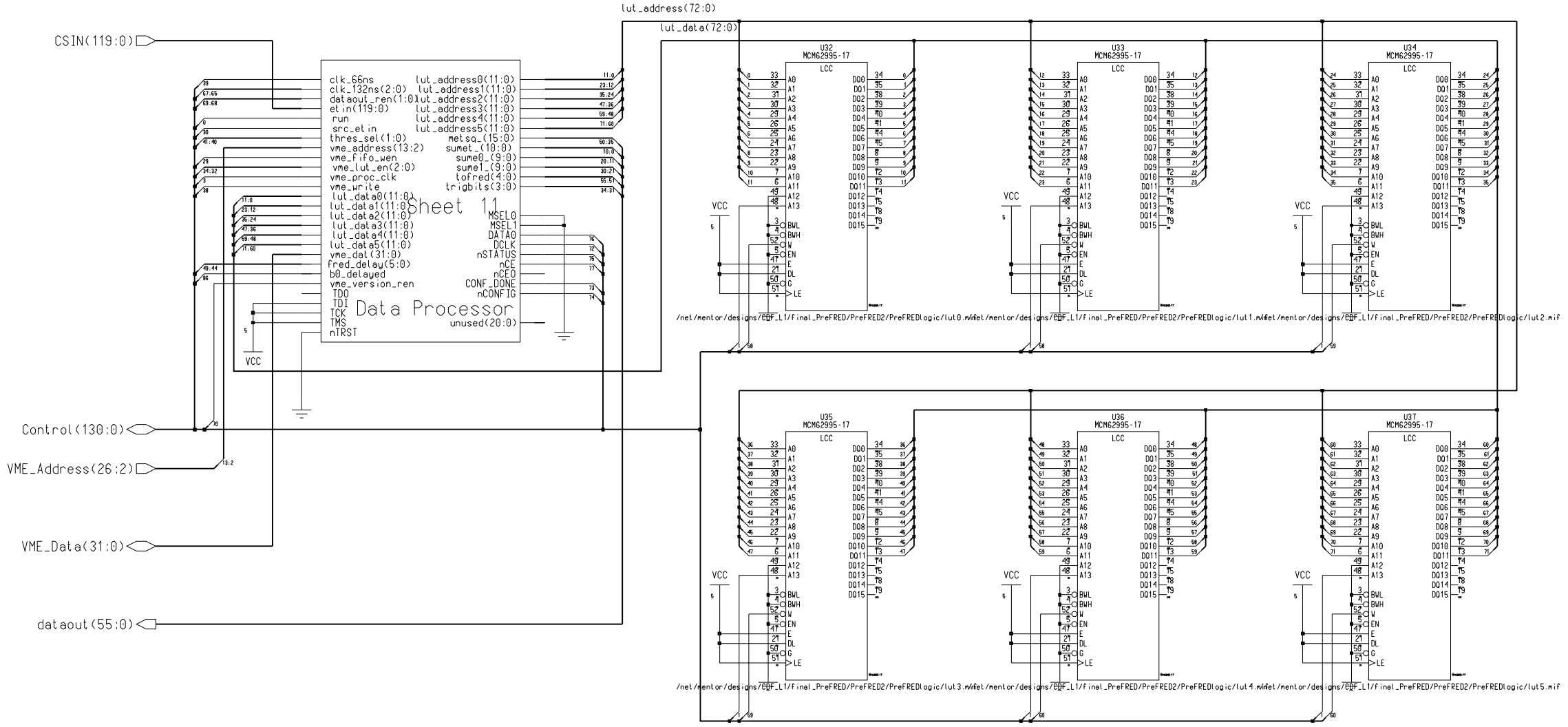

DATE	REVISION DESCRIPTION
15 May 1997	First Edition: after group review
17 Sep 1997	New design (parallel processing)
1 Oct 1997	Remove EPC1 after new configuration scheme

UNIVERSITY OF CHICAGO

ELECTRONICS DEVELOPMENT GROUP

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DATE 17 Sep 1997

DRWN Alexis / Mircea

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