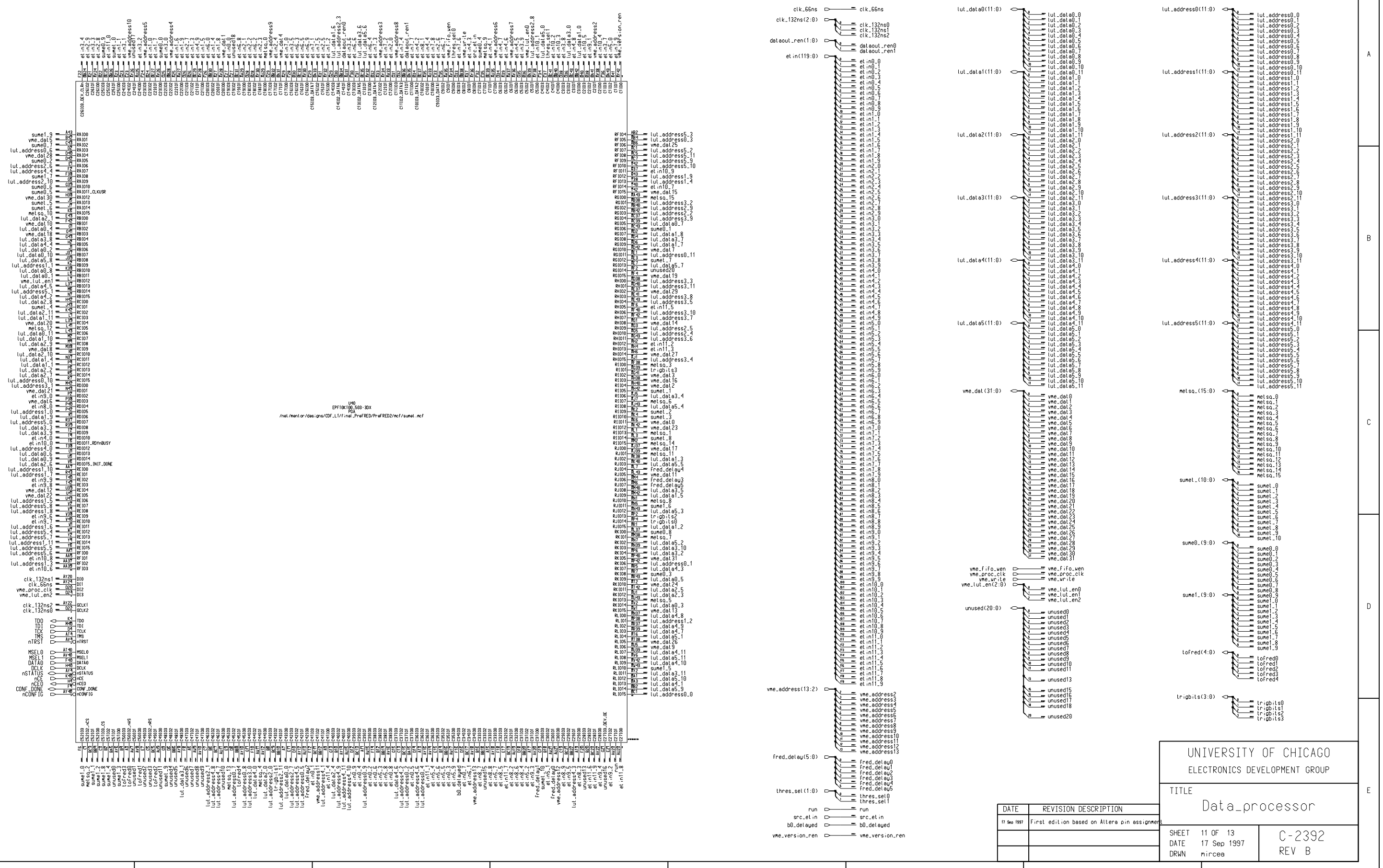
U40
EPF10K10_503-3DX
PQ5
/net/mentor/designs/CDF_L1/final_PreFRED/PreFRED2/mcf/sumet.mcf
clk_66ns
clk_132ns(2:0)
clk_132ns0
clk_132ns1
clk_132ns2
dataout_ren(1:0)
dataout_ren0
dataout_ren1
etin(119:0)
vme_address(13:2)
fred_delay(5:0)
thres_sel(1:0)
thres_sel0
thres_sel1
run
src_etin
b0_delayed
vme_version_ren
lut_data0(11:0)
lut_data1(11:0)
lut_data2(11:0)
lut_data3(11:0)
lut_data4(11:0)
lut_data5(11:0)
vme_dat(31:0)
vme_fifo_wen
vme_proc_clk
vme_write
vme_lut_en(2:0)
lut_en0
lut_en1
lut_en2
unused(20:0)
unused13
unused15
unused16
unused17
unused18
unused20
lut_address0(11:0)
lut_address1(11:0)
lut_address2(11:0)
lut_address3(11:0)
lut_address4(11:0)
lut_address5(11:0)
melsq_(15:0)
sumet_(10:0)
sume0_(9:0)
sume1_(9:0)
tofred(4:0)
trigbits(3:0)
UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP
TITLE
Data_processor
SHEET 11 OF 13
DATE 17 Sep 1997
DRWN mircea
C-2392
REV B
DATE
REVISION DESCRIPTION
17 Sep 1997
First edition based on Altera pin assignment