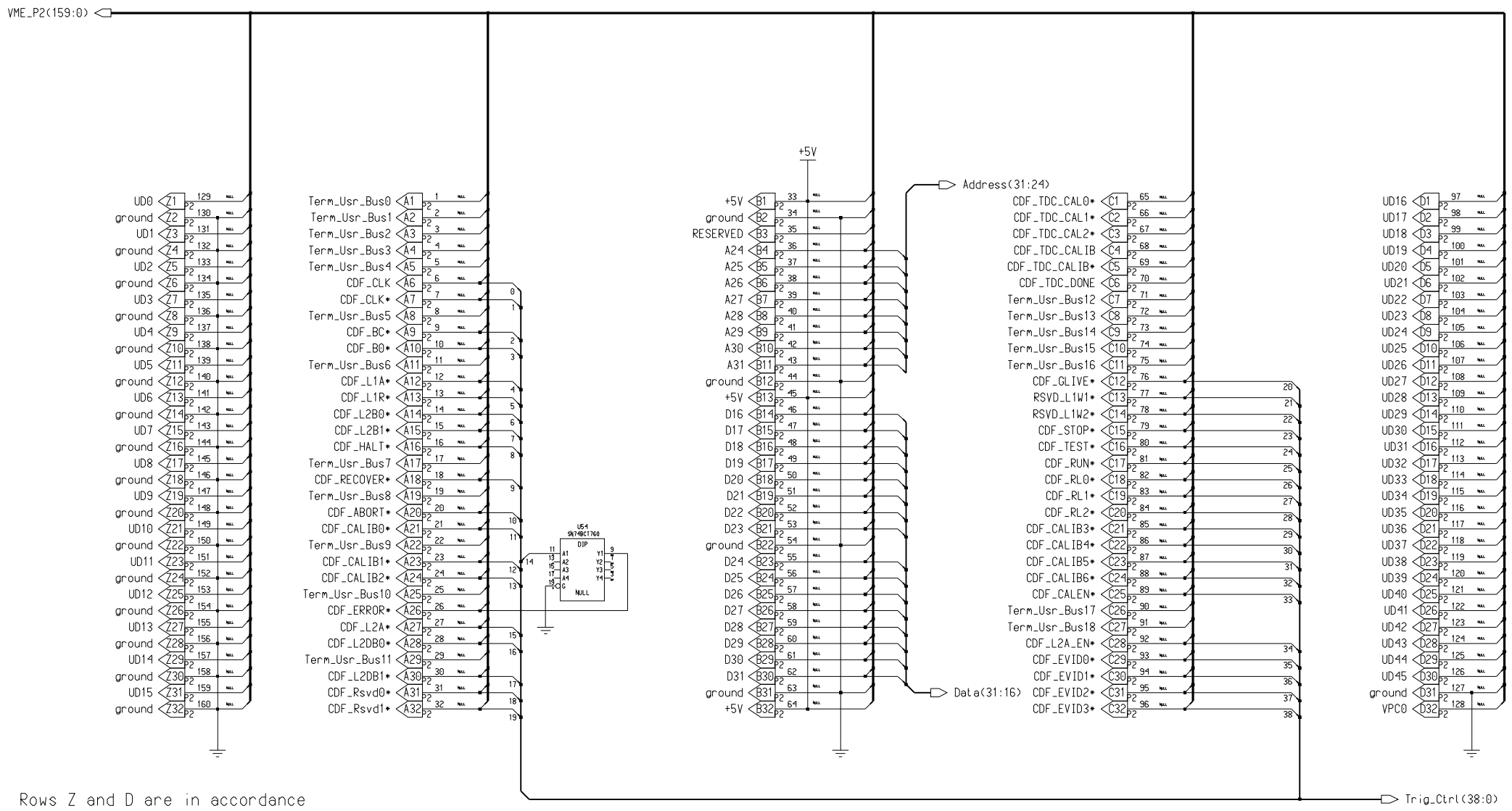
P2



Rows Z and D are in accordance with the VME64 Extensions standard. Note: the positioning of rows A, B and C should is still in accordance with the VME Standard.

All user-defined signals on P2 are totem-pole TTL excluding:
a. Geographical Addresses are pulled-up/grounded wires
b. 132ns Clock is differential PECL: CLK - CLK*
c. ERROR* is open collector TTL

DATE	REVISION DESCRIPTION
16 Dec 93	Original design -- YF
31 Aug 95	Add rows Z and D to conform to the VME64 Ext. Spec. Corrections to conform to new CDF P2 Spec.
18 Nov 96	3rd Prototype - Bug Fixes

UNIVERSITY OF CHICAGO
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TITLE
VMEbus_P2

SHEET 5 OF 13
DATE 18 Nov 1996
DRWN JW

C-2392
REV B