P2

P2_Signals(10:0)

UD0	Z1	129	Term_Usr_Bus0	A1	1	+5V	B1	33
ground	Z2	130	Term_Usr_Bus1	A2	2	ground	B2	34
UD1	Z3	131	Term_Usr_Bus2	A3	3	RESERVED	B3	35
ground	Z4	132	Term_Usr_Bus3	A4	4	A24	B4	36
UD2	Z5	133	Term_Usr_Bus4	A5	5	A25	B5	37
ground	Z6	134	CDF_CLK	A6	6	A26	B6	38
UD3	Z7	135	CDF_CLK*	A7	7	A27	B7	39
ground	Z8	136	Term_Usr_Bus5	A8	8	A28	B8	40
UD4	Z9	137	CDF_BC*	A9	9	A29	B9	41
ground	Z10	138	CDF_B0*	A10	10	A30	B10	42
UD5	Z11	139	Term_Usr_Bus6	A11	11	A31	B11	43
ground	Z12	140	CDF_L1A*	A12	12	ground	B12	44
UD6	Z13	141	CDF_L1R*	A13	13	+5V	B13	45
ground	Z14	142	CDF_L2B0*	A14	14	D16	B14	46
UD7	Z15	143	CDF_L2B1*	A15	15	D17	B15	47
ground	Z16	144	CDF_HALT*	A16	16	D18	B16	48
UD8	Z17	145	Term_Usr_Bus7	A17	17	D19	B17	49
ground	Z18	146	CDF_RECOVER*	A18	18	D20	B18	50
UD9	Z19	147	Term_Usr_Bus8	A19	19	D21	B19	51
ground	Z20	148	CDF_ABORT*	A20	20	D22	B20	52
UD10	Z21	149	CDF_CALIB0*	A21	21	D23	B21	53
ground	Z22	150	Term_Usr_Bus9	A22	22	ground	B22	54
UD11	Z23	151	CDF_CALIB1*	A23	23	D24	B23	55
ground	Z24	152	CDF_CALIB2*	A24	24	D25	B24	56
UD12	Z25	153	Term_Usr_Bus10	A25	25	D26	B25	57
ground	Z26	154	CDF_ERROR*	A26	26	D27	B26	58
UD13	Z27	155	CDF_L2A*	A27	27	D28	B27	59
ground	Z28	156	CDF_L2DB0*	A28	28	D29	B28	60
UD14	Z29	157	Term_Usr_Bus11	A29	29	D30	B29	61
ground	Z30	158	CDF_L2DB1*	A30	30	D31	B30	62
UD15	Z31	159	CDF_Rsvd0*	A31	31	ground	B31	63
ground	Z32	160	CDF_Rsvd1*	A32	32	+5V	B32	64

CDF_TDC_CAL0*	C1	65	UD16	D1	97
CDF_TDC_CAL1*	C2	66	UD17	D2	98
CDF_TDC_CAL2*	C3	67	UD18	D3	99
CDF_TDC_CALIB	C4	68	UD19	D4	100
CDF_TDC_CALIB*	C5	69	UD20	D5	101
CDF_TDC_DONE	C6	70	UD21	D6	102
Term_Usr_Bus12	C7	71	UD22	D7	103
Term_Usr_Bus13	C8	72	UD23	D8	104
Term_Usr_Bus14	C9	73	UD24	D9	105
Term_Usr_Bus15	C10	74	UD25	D10	106
Term_Usr_Bus16	C11	75	UD26	D11	107
CDF_GLIVE*	C12	76	UD27	D12	108
RSVD_L1W1*	C13	77	UD28	D13	109
RSVD_L1W2*	C14	78	UD29	D14	110
CDF_STOP*	C15	79	UD30	D15	111
CDF_TEST*	C16	80	UD31	D16	112
CDF_RUN*	C17	81	UD32	D17	113
CDF_RL0*	C18	82	UD33	D18	114
CDF_RL1*	C19	83	UD34	D19	115
CDF_RL2*	C20	84	UD35	D20	116
CDF_CALIB3*	C21	85	UD36	D21	117
CDF_CALIB4*	C22	86	UD37	D22	118
CDF_CALIB5*	C23	87	UD38	D23	119
CDF_CALIB6*	C24	88	UD39	D24	120
CDF_CALEN*	C25	89	UD40	D25	121
Term_Usr_Bus17	C26	90	UD41	D26	122
Term_Usr_Bus18	C27	91	UD42	D27	123
CDF_L2A_EN*	C28	92	UD43	D28	124
CDF_EVID0*	C29	93	UD44	D29	125
CDF_EVID1*	C30	94	UD45	D30	126
CDF_EVID2*	C31	95	ground	D31	127
CDF_EVID3*	C32	96	VPC0	D32	128

Rows Z and D are in accordance with the VME64 Extensions standard. Note: the positioning of rows A, B and C should is still in accordance with the VME Standard.

All user-defined signals on P2 are totem-pole TTL excluding:
a. Geographical Addresses are pulled-up/grounded wires
b. 132ns Clock is differential PECL: CLK - CLK*
c. ERROR* is open collector TTL

DATE	REVISION DESCRIPTION
16 Dec 93	Original design -- YF
31 Aug 95	Add rows Z and D to conform to the VME64 Ext. Spec. Corrections to conform to new CDF P2 Spec.
18 Nov 96	3rd Prototype - Bug Fixes

UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE
VMEbus_P2

SHEET 3 OF 10
DATE 18 Nov 1996
DRWN JW

C-2374
REV B