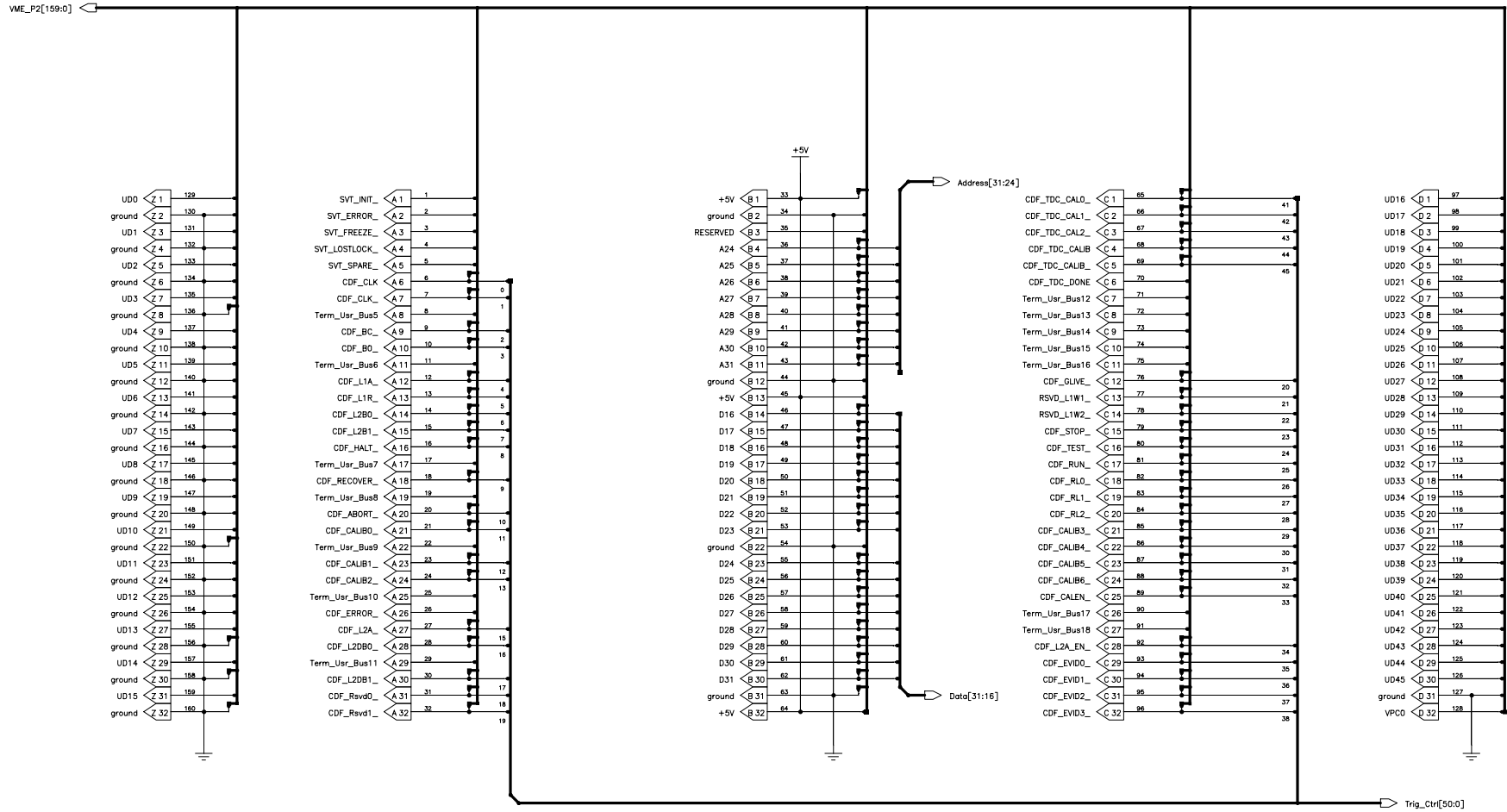


P2



Engineer: M. Bogdan
 Drawn by: M. Bogdan
 DATE: 2/8/22

The University of Chicago
 VME-P2
 500MHz 16Ch ADC Module
 REV. A | DRW. 3003 | Sheet 10