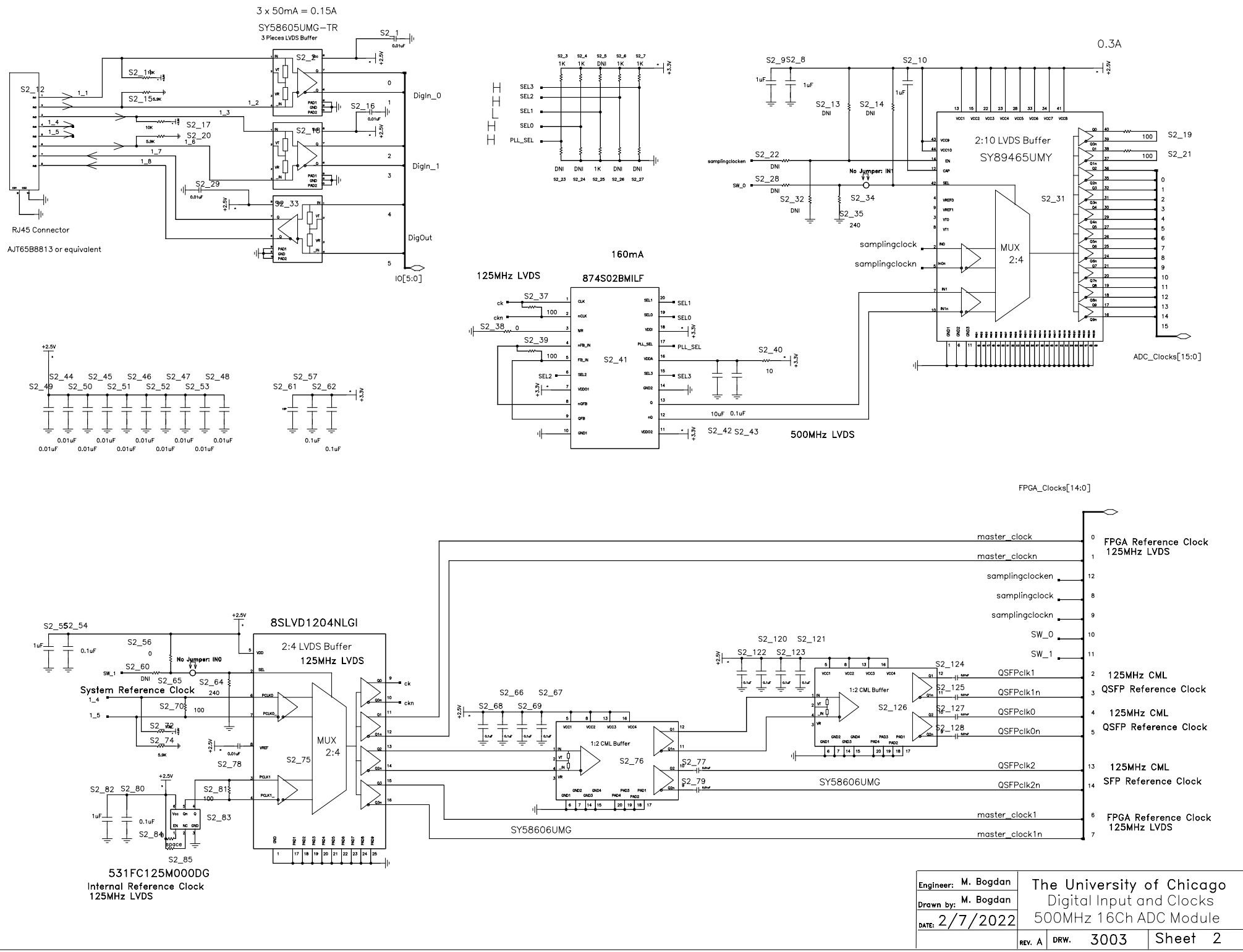


| | | |
|---|---|---|
| Engineer: M. Bogdan | The University of Chicago | |
| Drawn by: M. Bogdan | Digital Input and Clocks | |
| DATE: 2/7/2022 | 500MHz 16Ch ADC Module | |
| | REV. A | DRW. 3003 | Sheet 2 |