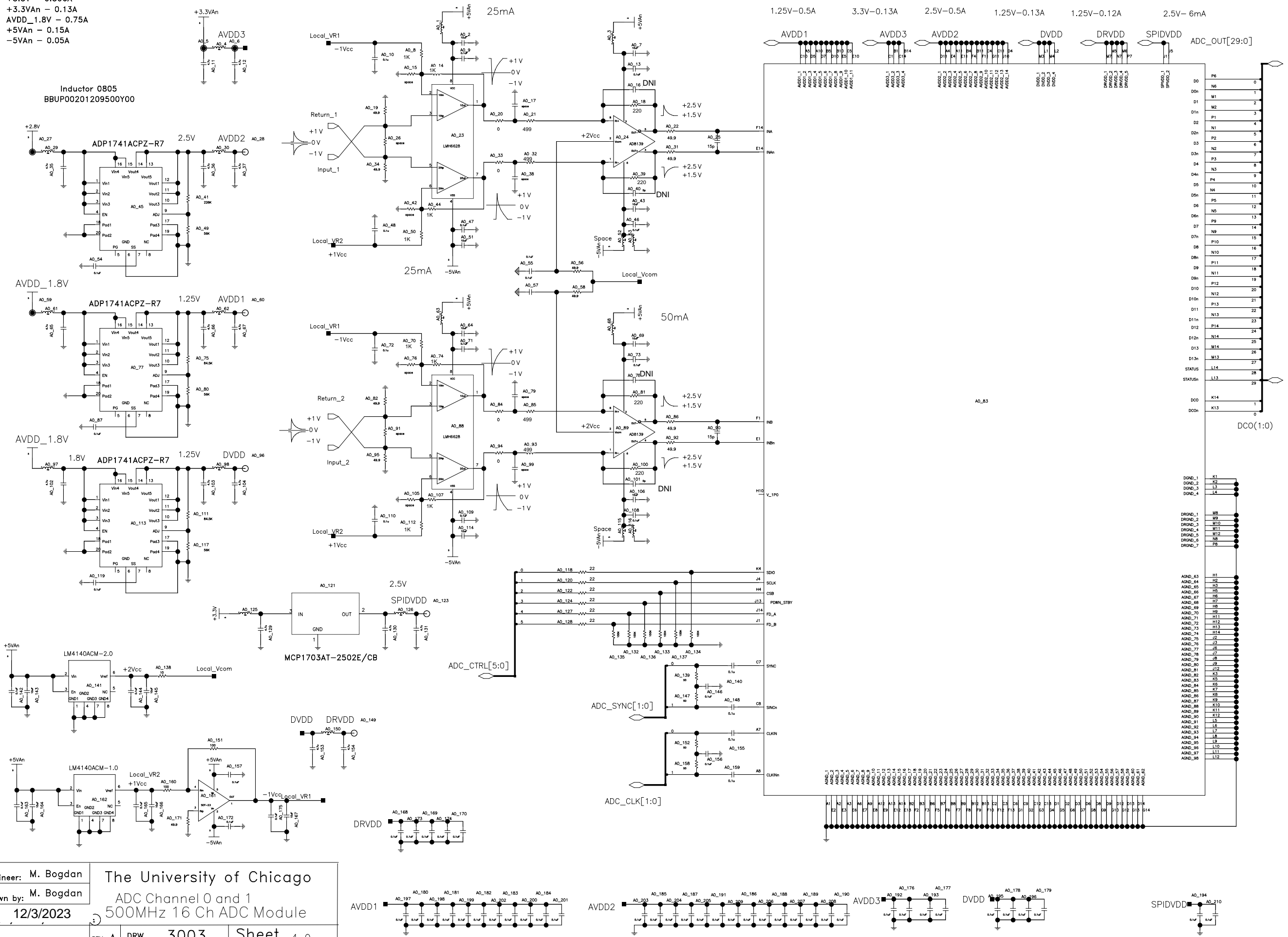


Two ADC Channels Power Requirements:  
 +3.3V - 0.506A  
 +3.3VAn - 0.13A  
 AVDD\_1.8V - 0.75A  
 +5VAn - 0.15A  
 -5VAn - 0.05A



Engineer: M. Bogdan  
 Drawn by: M. Bogdan  
 DATE: 12/3/2023  
 REV. A  
 The University of Chicago  
 ADC Channel 0 and 1  
 500MHz 16 Ch ADC Module  
 DRW. 3003  
 Sheet 4\_0