



16 ADC Channels Power Requirements:
 +3.3V - 4A to make 2.5V
 5V (Vcc) - 3A to make 1.8V, 3.3VAn
 +5VAn - 1.2A
 -5VAn - 0.4A

FPGA Max Power:
 5V (Vcc) - 1.25A to make 1.1V, 1.15V
 +3.3V - 1A to make 2.5V, 1.5V

Clocks Power:
 +3.3V - 0.7A max including the 2.5V
 2 x QSFP+
 +3.3V - 2 A max

Power Estimate per Board Max Power from VME Slot
 3.3V - 7.7A 3.3V - 9 pins - 13.5A
 5V - 4.25A 5V - 6 pins - 9A
 +/-12V - 1 pin - 1.5A
 +/-7V - 0.4A User Defined +/-7V - 2 pins - 3A

Engineer: M. Bogdan	The University of Chicago Power 500MHz 16Ch ADC Module		
Drawn by: M. Bogdan			
DATE: 5/26/23	REV. A	DRW. 3003	Sheet 5