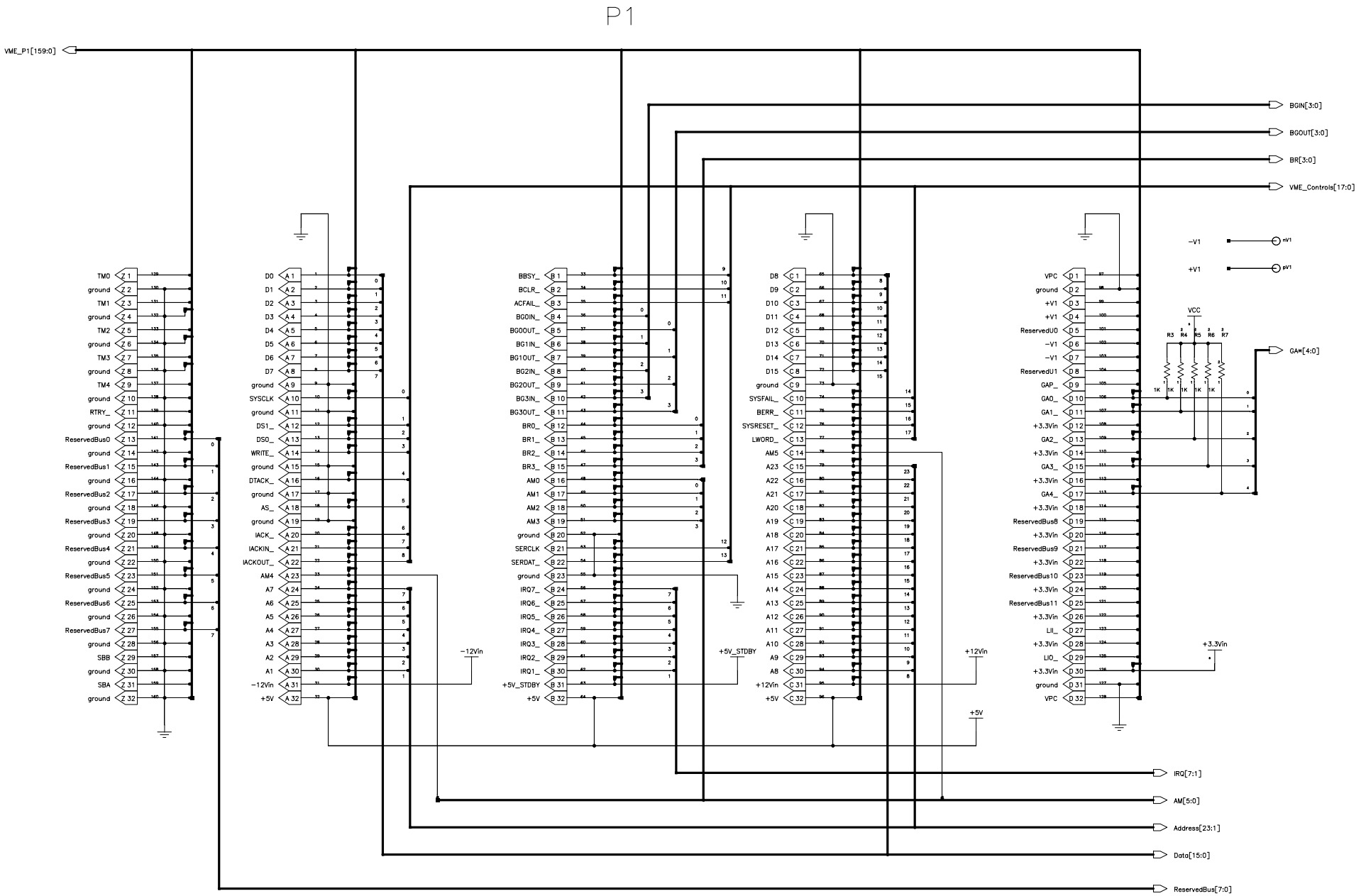

Engineer:	M. Bogdan	The University of Chicago	
Drawn by:	M. Bogdan	VME−P1	
DATE:	2/8/22	500MHz 16Ch ADC Module	
		REV. A	DRW. 3003	Sheet 9