



BOARD'S DRILL SCHEDULE

DRILL SYMBOL	DRILL SIZE	COUNT	PLATED	Tolerance	COMMENT
○	.014	4841	YES	---	
⊞	.015748031	144	YES	---	
Φ	.018	5	YES	---	
⊞	.035	42	YES	---	
⊞	.037	18	YES	---	
⊞	.041	475	YES	---	
⊞	.042	20	YES	---	
□	.05	5	YES	---	
	.057	18	YES	---	
	.062	4	YES	---	
	.062992126	8	YES	---	
	.086614173	2	NO	---	
	.106	6	NO	---	
	.125	2	YES	---	
	.12795276	6	YES	---	3
	.15	5	NO	---	

THIS SHEET IS COMPUTER GENERATED

PVC NO. 2606

Top - Comp.Side		Layer Order	
0.005	---	1.Signal_1	Microstrip
0.005	---	2.Power	
0.010	==	3.Signal_7	Stripline
0.005	---	4.Signal_2	
0.005	---	5.Power	
0.005	---	6.Signal_8	
0.010	==	7.Signal_3	
0.005	---	8.Power	
0.005 +/- 0.008	---	9.Power	
0.005	---	10.Signal_9	
0.010	==	11.Signal_4	
0.005	---	12.Power	
0.005	---	13.Signal_10	
0.010	==	14.Signal_5	
0.005	---	15.Power	
0.005	---	16.Signal_6	

## Board Characteristics

0. All dimensions are given in inches unless specified otherwise.

1. Material FR4 with Tg&gt;170C, E.g. FR406

2. Minimum trace width: 0.006" and clearance: 0.005" on Signal\_1,6 (Top and Bottom);

3. Minimum trace width and clearance: 0.005" on Signal\_2,3,4,5,7,8,9,10 (all stripline);

4. 1 oz copper for all power layers and for Signal\_1,2 (Top and Bottom)  
1/2 oz copper for Stripline trace layers (Signal\_2,3,4,5,7,8,9,10).

5. Immersion Gold over copper, with min. Ni: 2.5-5 um; Au: 0.05-0.2 um.

6. Apply Solder Mask over bare copper.

7. Board Thickness: 0.093 +/- 0.008

8. Mill the Top and Bottom of board on the solder side to a thickness of 0.063" +/- 0.008

9. Silkscreen on Component and Solder Sides.

10. 45 degree chamfer.

11. FHS tolerances: +/- 0.002 unless specified otherwise.

12. Interlayer spacing as specified

13. Zc=55 Ohm +/- 5 Ohm for 0.005" stripline and 0.006" microstrip traces on all layers.

Perform TDR test for all signal layers.

Present TDR test results for all signal layers.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES XX XXX DO NOT SCALE DRAWING		CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
APPROVALS		DATE		TITLE	
DRAWN M.Bogdan		4/25/2017		14-BIT ADC Board Specification Drawing	
CHECKED M.Bogdan		4/25/2017		SIZE B	
ISSUED				PCLM NO.	
SYNTER TO		MCT. VI		PVC NO. 2606	
				SCALE 1/2	
				SHEET	