

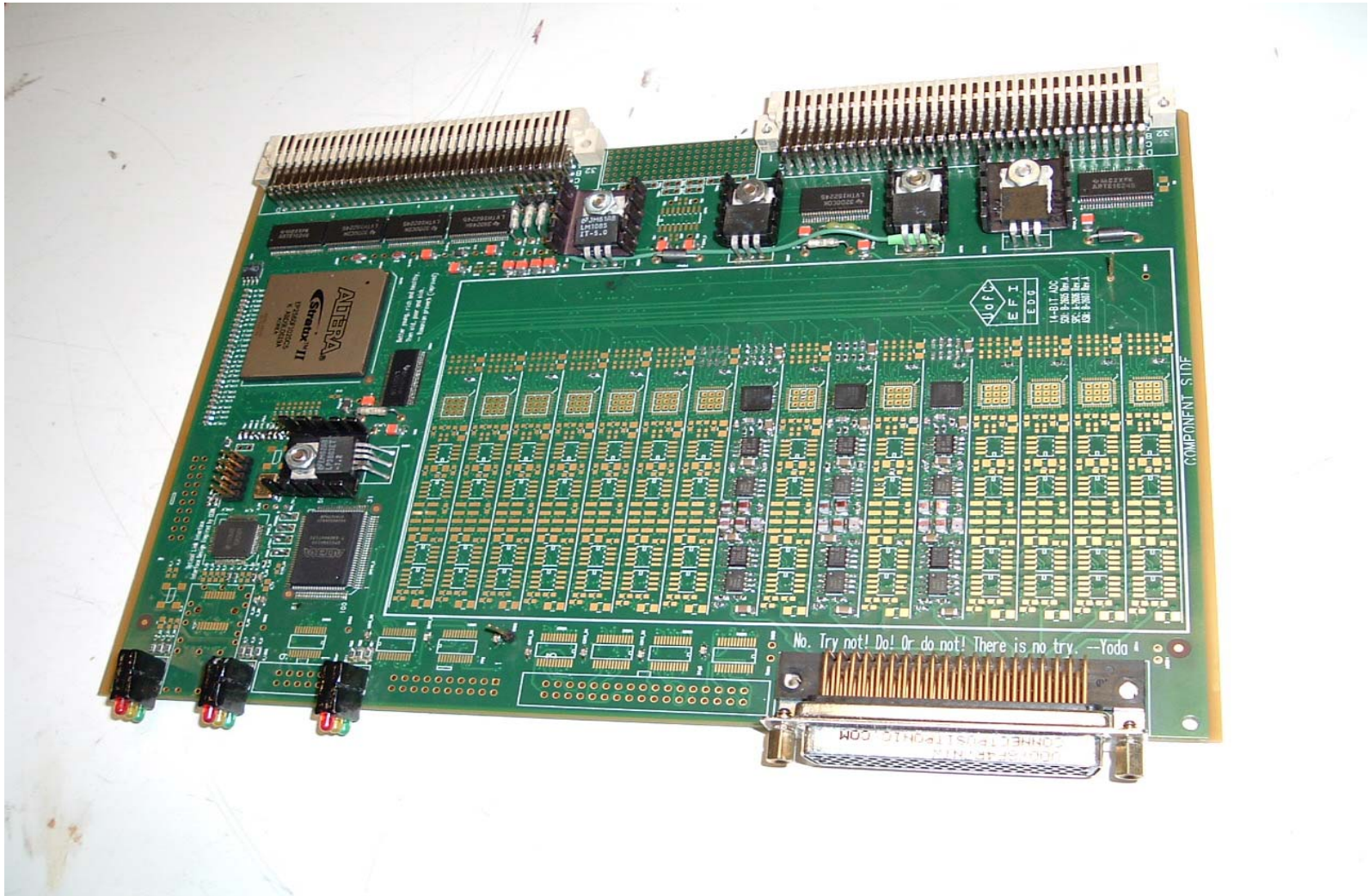
14BIT 125MHz ADC Board for JPARC-K

Status Report

Mircea Bogdan
August 9, 2007

The University of Chicago

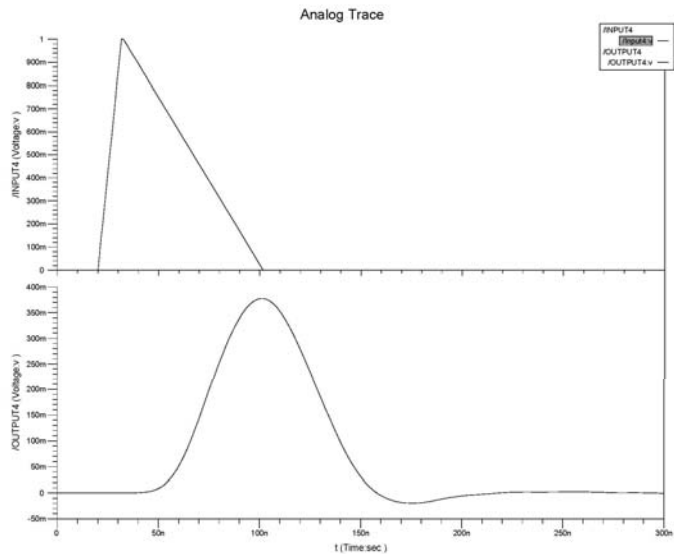
14-Bit, 125MHz ADC Board



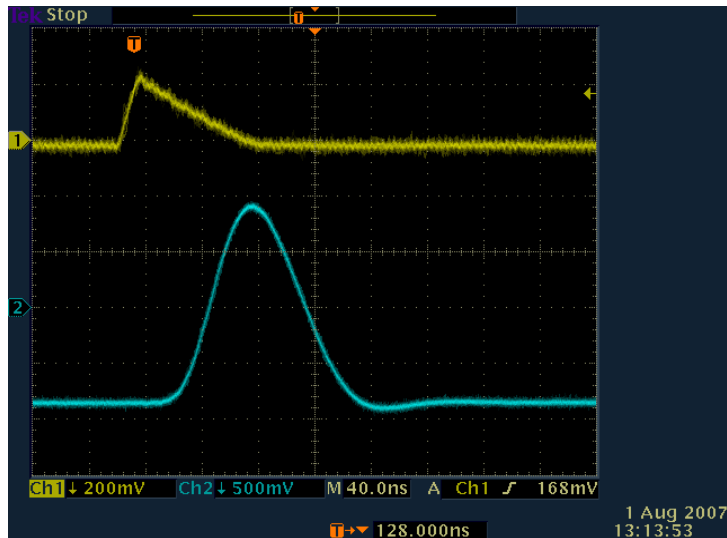
First Prototype with 3 channels stuffed.

14-Bit, 125MHz ADC Board - Testing





Simulation - Shaper In and Out



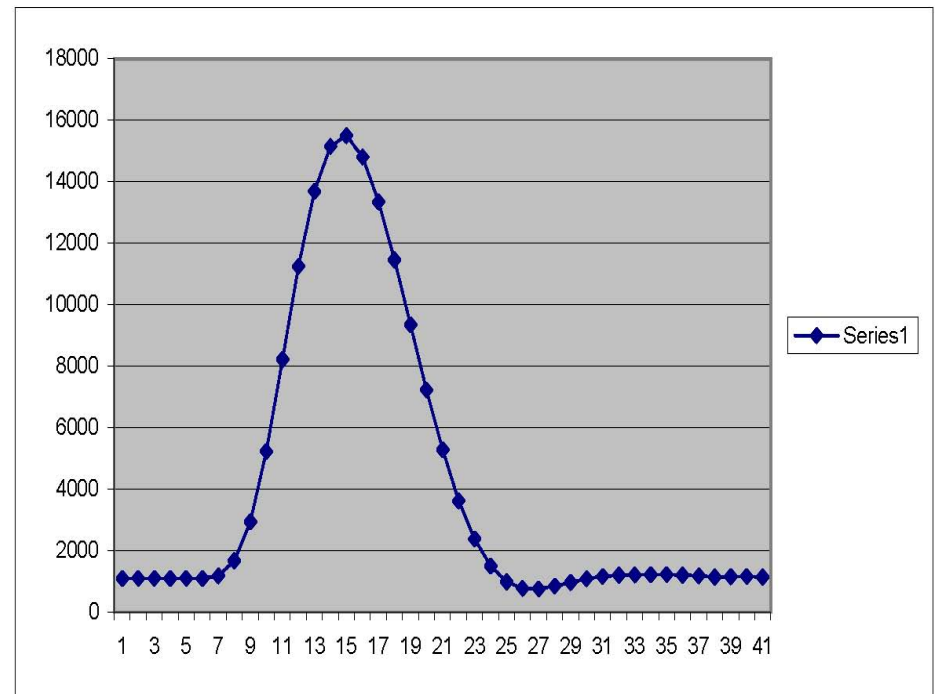
Scope Plots - Shaper In and Out

14-Bit, 125MHz ADC Board

Preliminary Testing Results:

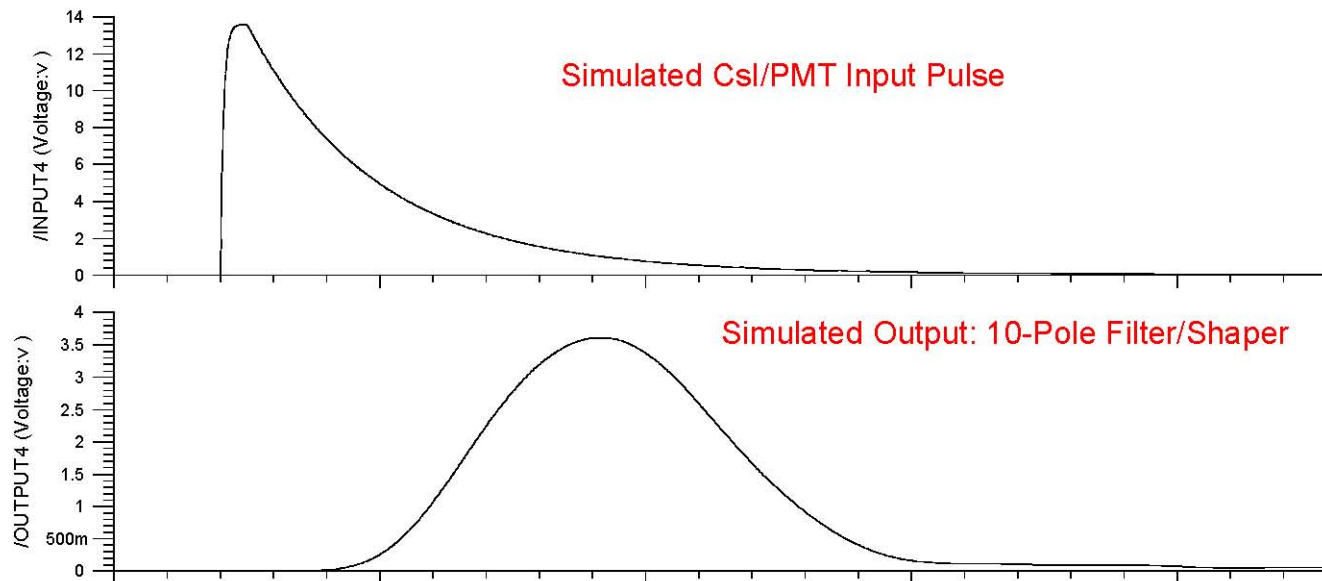
10-Pole Shaper

Linear Input Pulse



VME Acquisitions with new ADC Board

14-Bit, 125 MHz ADC Board



10-Pole CsI/PMT Simulation Results

Conclusions

- Will continue work, and stuff two full boards;
- So far, so good.