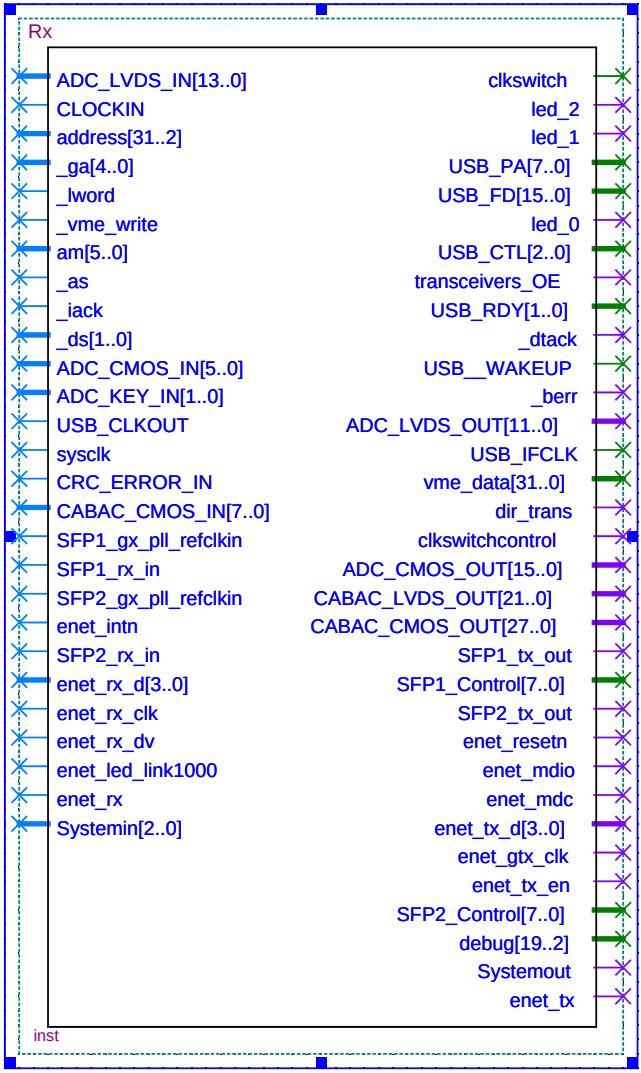
Rx
ADC_LVDS_IN[13..0]
CLOCKIN
address[31..2]
_ga[4..0]
_lword
_vme_write
am[5..0]
_as
_iack
_ds[1..0]
ADC_CMOS_IN[5..0]
ADC_KEY_IN[1..0]
USB_CLKOUT
sysclk
CRC_ERROR_IN
CABAC_CMOS_IN[7..0]
SFP1_gx_pll_refclkin
SFP1_rx_in
SFP2_gx_pll_refclkin
enet_intn
SFP2_rx_in
enet_rx_d[3..0]
enet_rx_clk
enet_rx_dv
enet_led_link1000
enet_rx
Systemin[2..0]
clkswitch
led_2
led_1
USB_PA[7..0]
USB_FD[15..0]
led_0
USB_CTL[2..0]
transceivers_OE
USB_RDY[1..0]
_dtack
USB__WAKEUP
_berr
ADC_LVDS_OUT[11..0]
USB_IFCLK
vme_data[31..0]
dir_trans
clkswitchcontrol
ADC_CMOS_OUT[15..0]
CABAC_LVDS_OUT[21..0]
CABAC_CMOS_OUT[27..0]
SFP1_tx_out
SFP1_Control[7..0]
SFP2_tx_out
enet_resetn
enet_mdio
enet_mdc
enet_tx_d[3..0]
enet_gtx_clk
enet_tx_en
SFP2_Control[7..0]
debug[19..2]
Systemout
enet_tx
inst