

Sheet 3

LVDS_IN[27:0] ADC_LVDS_IN[27:0]

LVDS_OUT[23:0] ADC_LVDS_OUT[23:0]

CMOS_IN[5:0] ADC_CMOS_IN[5:0]

CMOS_OUT[15:0] ADC_CMOS_OUT[15:0]

KEY_IN[1:0] ADC_KEY_IN[1:0]

ADC

ASP-122953-01

VCC_1.1V VIO_2.5V T_GXB_1.15V H_GXB_1.5V

VCCP_1.1V VIO_3.0V LR_GXB_1.15V VAUX_2.5V

VCC_1.1V VIO_2.5V VAUX_2.5V

VCCP_1.1V VIO_3.0V LR_GXB_1.15V H_GXB_1.5V

VIO_3.0V T_GXB_1.15V

ADC_LVDS_IN[27:0] ADC_LVDS_OUT[23:0] ADC_CMOS_IN[5:0] ADC_CMOS_OUT[15:0] ADC_KEY_IN[1:0]

DigIO[15:0]

VME_etc[30:0] VME_Addr[31:2] transceivers_OE dir_trans VME_Data[31:0]

3 Ch In (2 + clk)

1 Ch Out

Sheet 2

Digital I/O

VME_etc[30:0] VME_etc(30:0)

VME_Addr[31:2] VME_Addr(31:2)

VME_P1(159:0) P1[159:0]

VME_OE VME_OE **VME**

VME_DIR_TRANS VME_DIR_TRANS

VME_P2(159:0) P2[159:0]

Local_VME_Data(31:0)

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Sheet 13

CMOS_IN[7:0] CABAC_CMOS_IN[7:0]

CMOS_OUT[27:0] CABAC_CMOS_OUT[27:0]

LVDS_OUT[43:0] CABAC_LVDS_OUT[43:0]

CABAC

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5AGXFB5H4F35C4

Sheet 11

GMI[24:0] GMI(24:0)

SGMI[5:0] SGMI(5:0)

Mngmnt[5:0] Mngmnt(5:0)

10/100/100

PHY

USB_FD[15:0] USB_FD[15:0]

USB_PA[7:0] USB_PA[7:0]

USB_AUX[8:0] USB_AUX[8:0]

Sheet 4

USB Interface

FPGA

SFP1_Rx[1:0] Rx[1:0] **SFP**

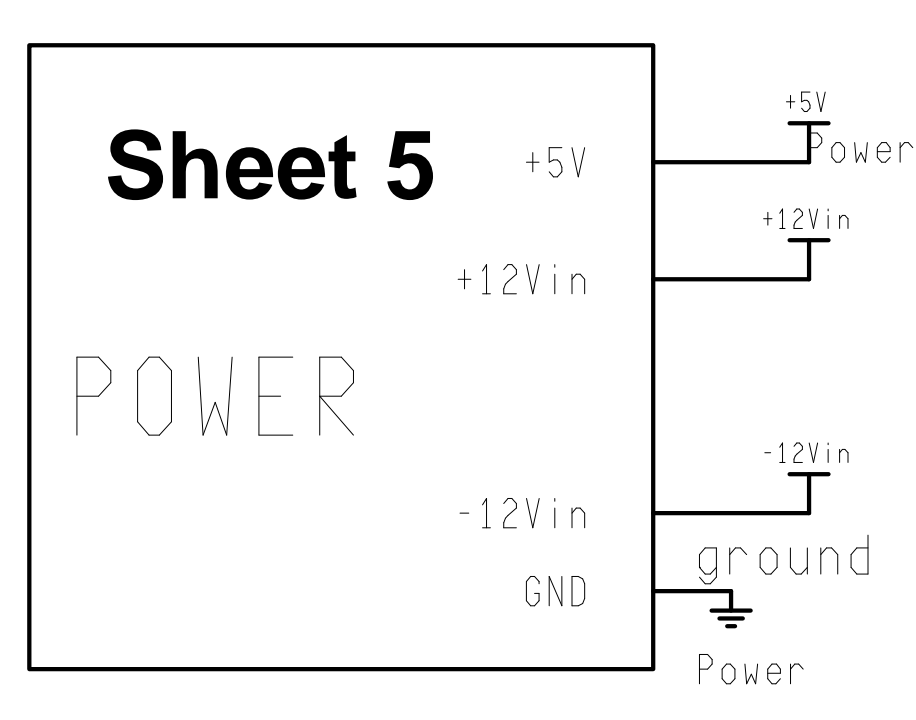
SFP1_Tx[1:0] Tx[1:0] **Sheet 12_1**

SFP1_CTRL[7:0] Control[7:0]

SFP2_Rx[1:0] Rx[1:0] **SFP**

SFP2_Tx[1:0] Tx[1:0] **Sheet 12_2**

SFP2_CTRL[7:0] Control[7:0]



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	The University of Chicago		
SPC#2938 ASM#2939	REV. A	DRW. 2937	Sheet 1 of 13