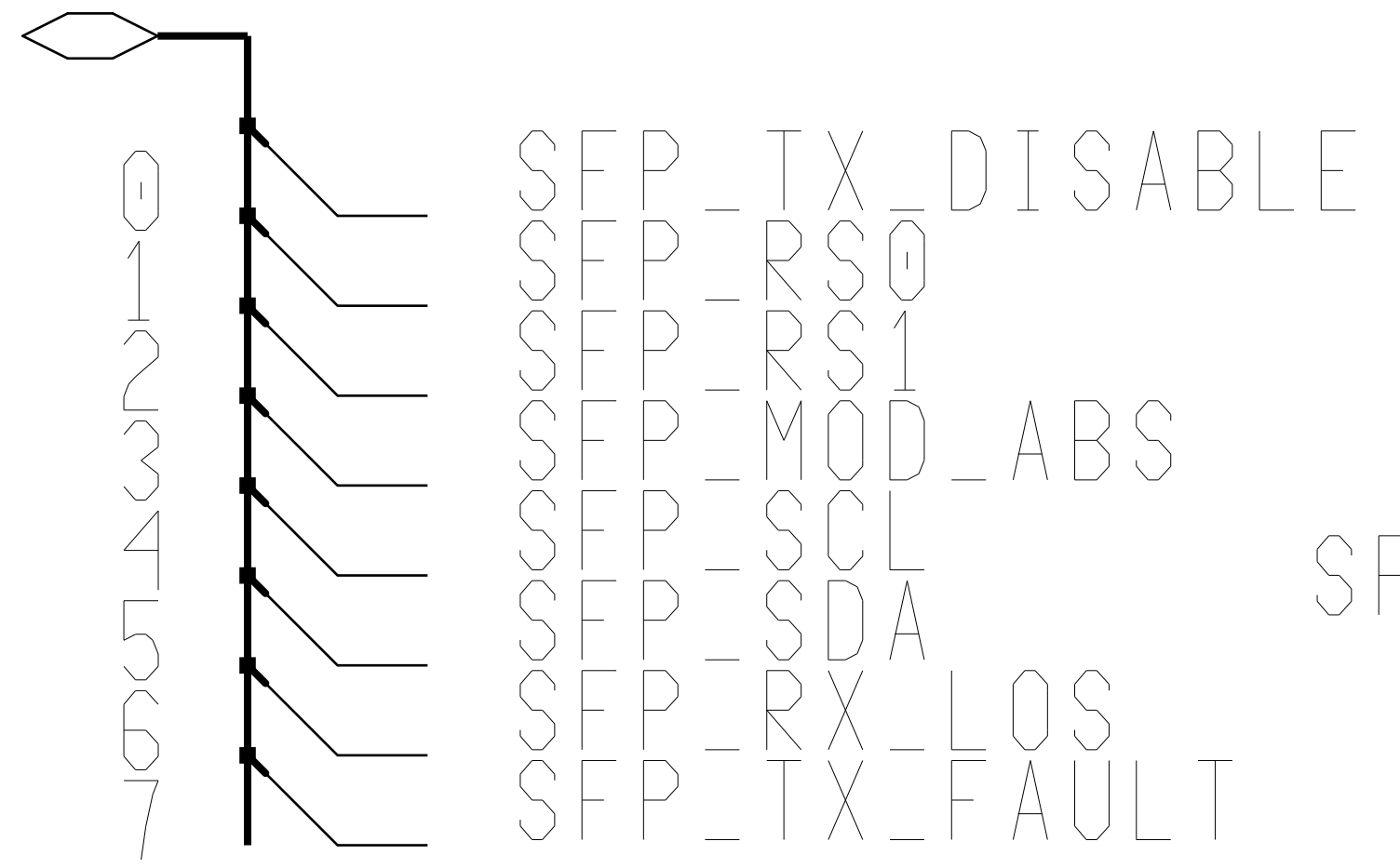
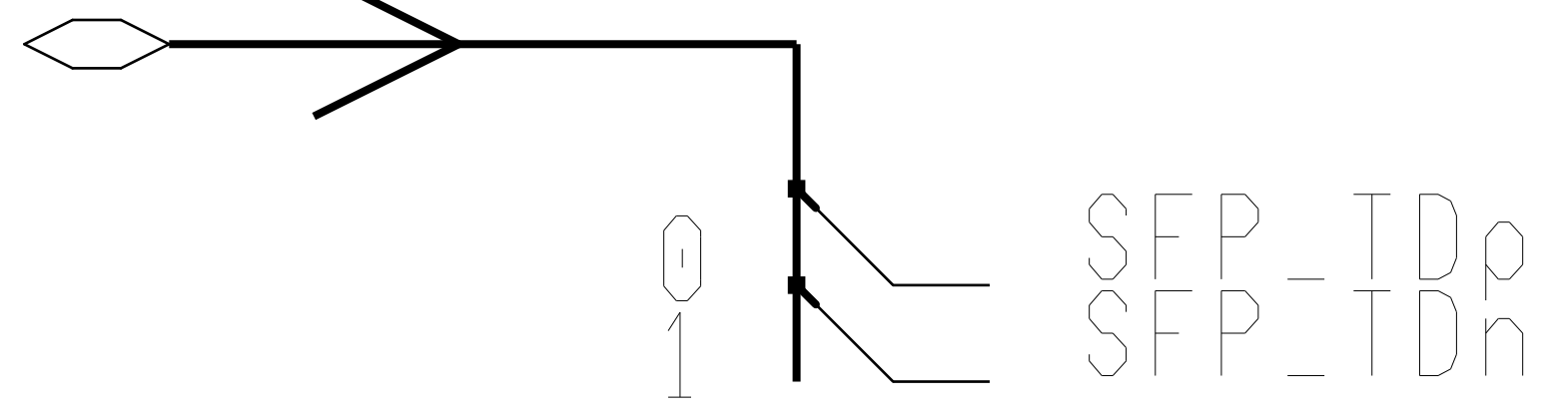


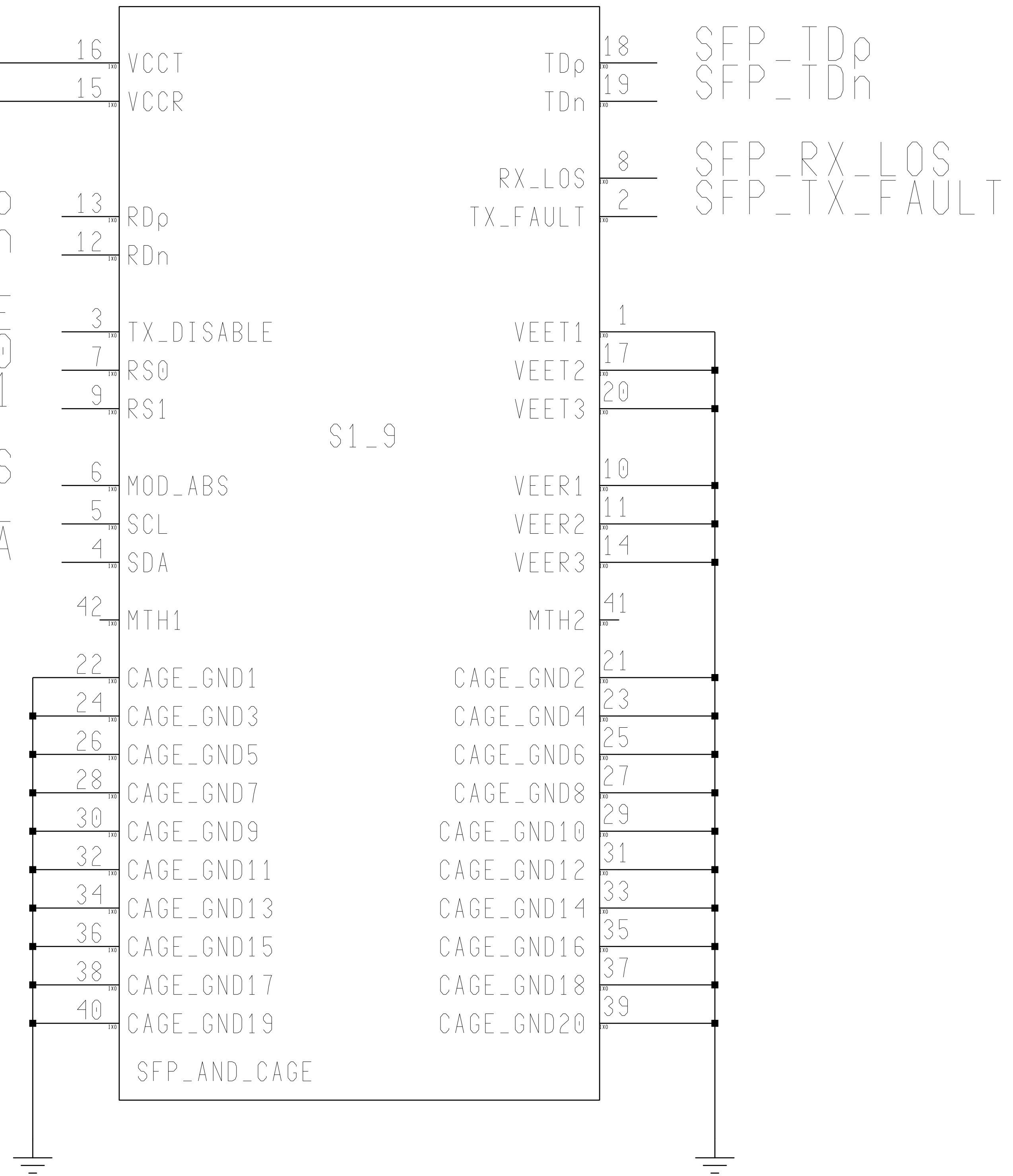
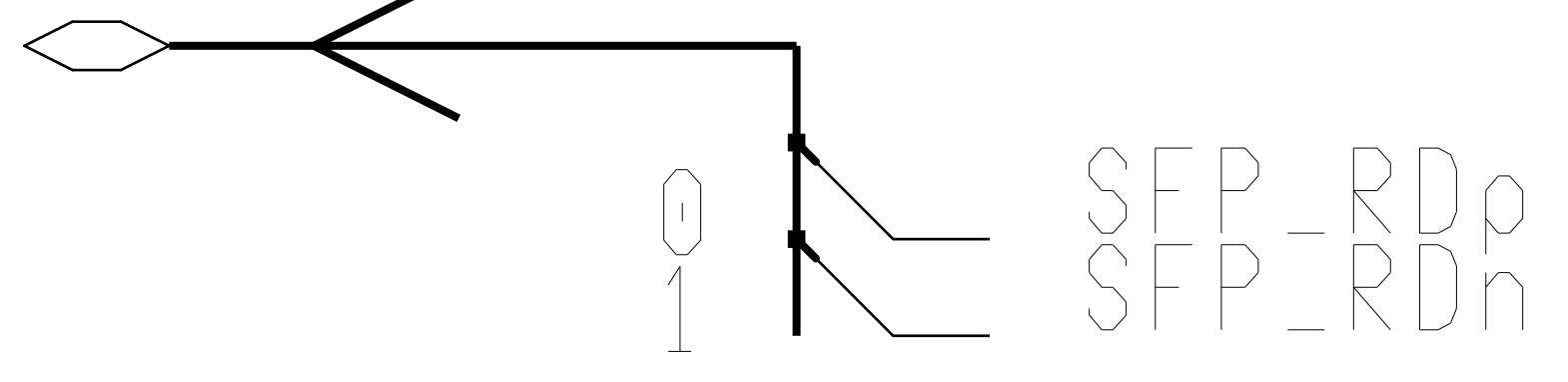
Control[7:0]



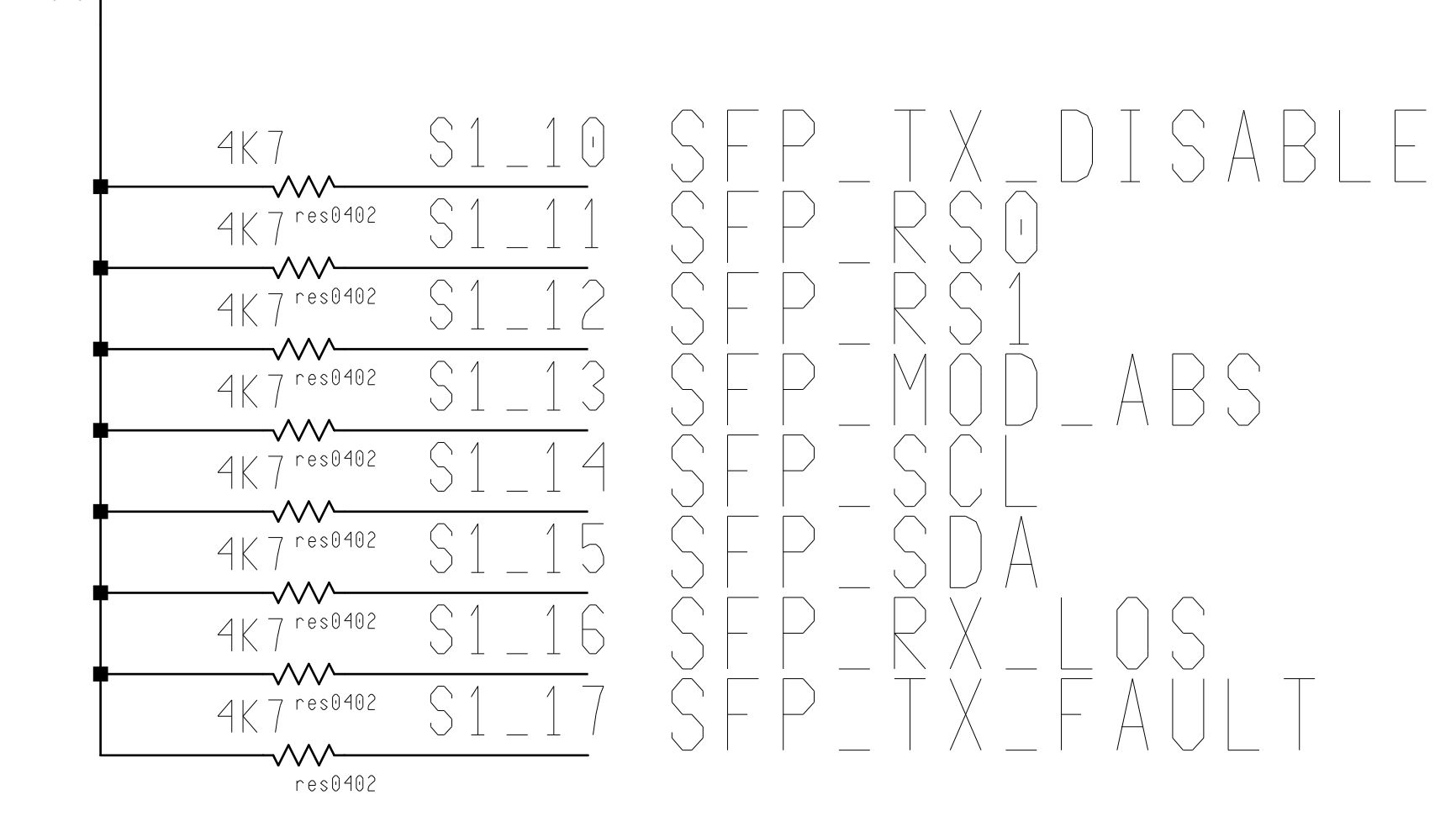
Tx[1:0]



Rx[1:0]



+3.3V



**SFP+ Module**  
**SFP Cage**  
**SFP+ Host Connector, 20-pin**

Engineer: <b>M. Bogdan</b>	<i>DAMIC - ODILE Module</i> SFP Block_1		
Drawn by: <b>M. Bogdan</b>			
DATE: <b>2/7/2019</b>	<b>The University of Chicago</b>		
SPC#2938 ASM#2939	REV. A	DRW. <b>2937</b>	Sheet <b>12_1</b>