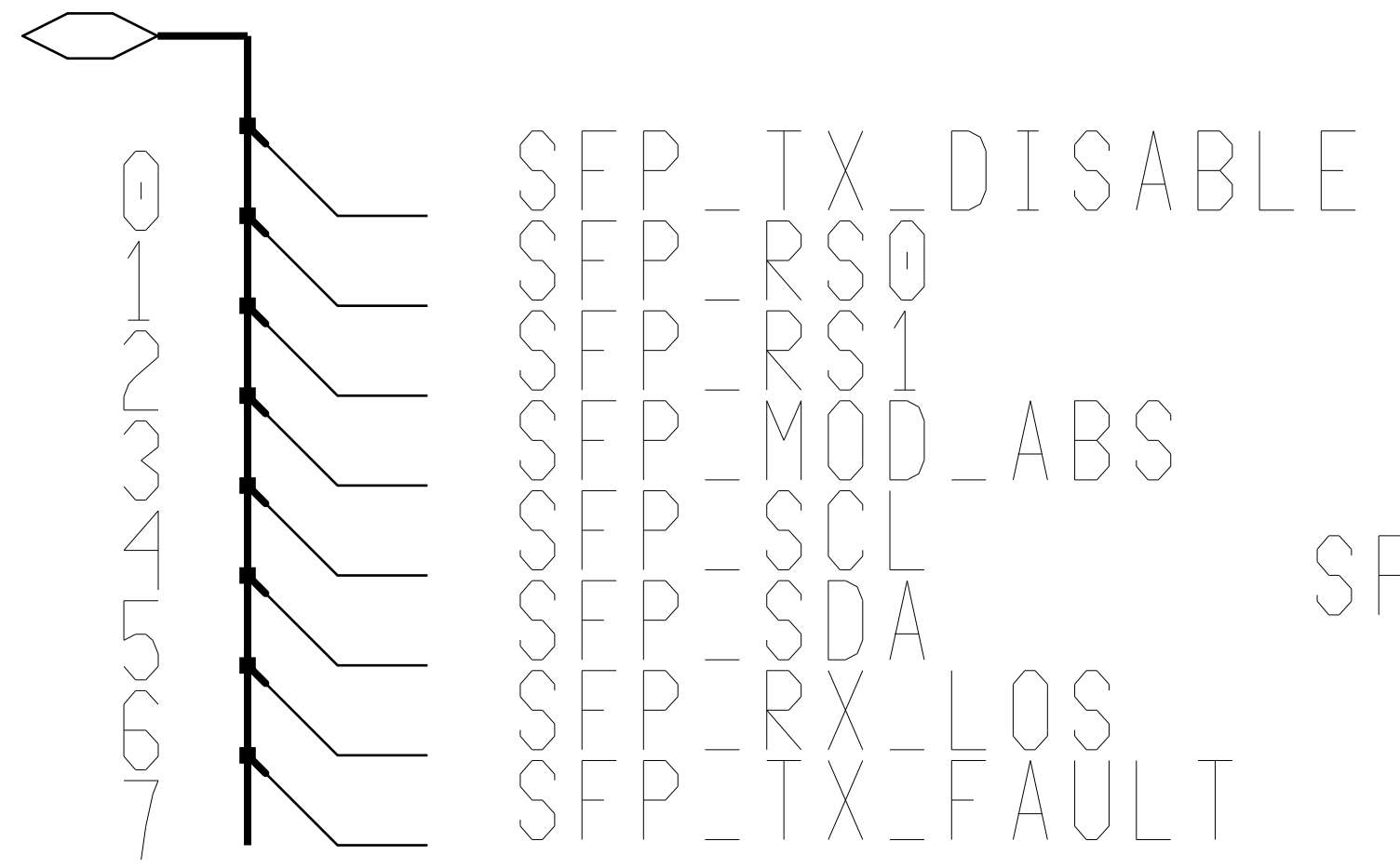
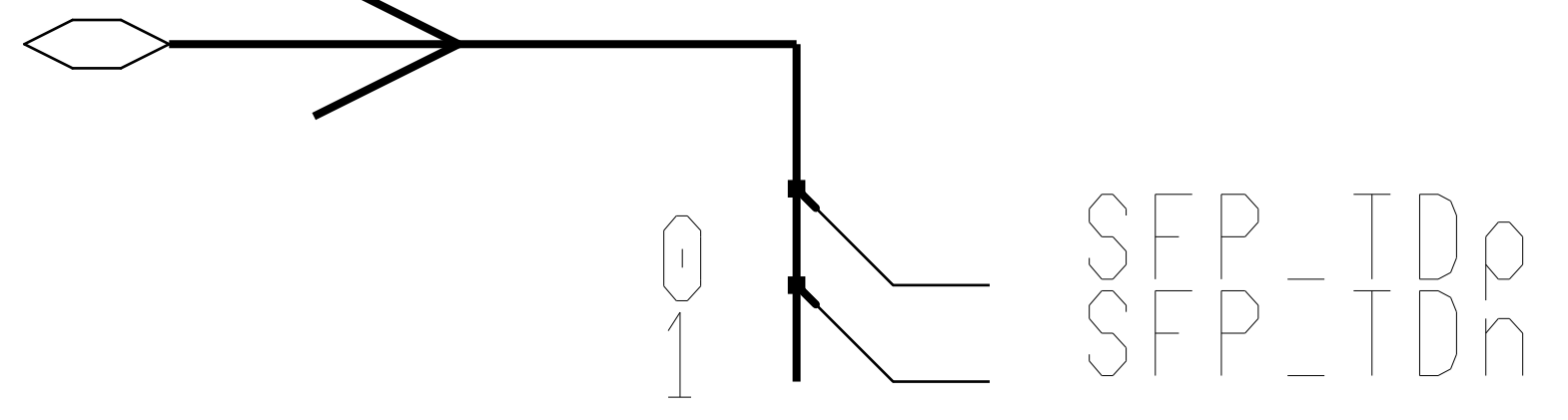


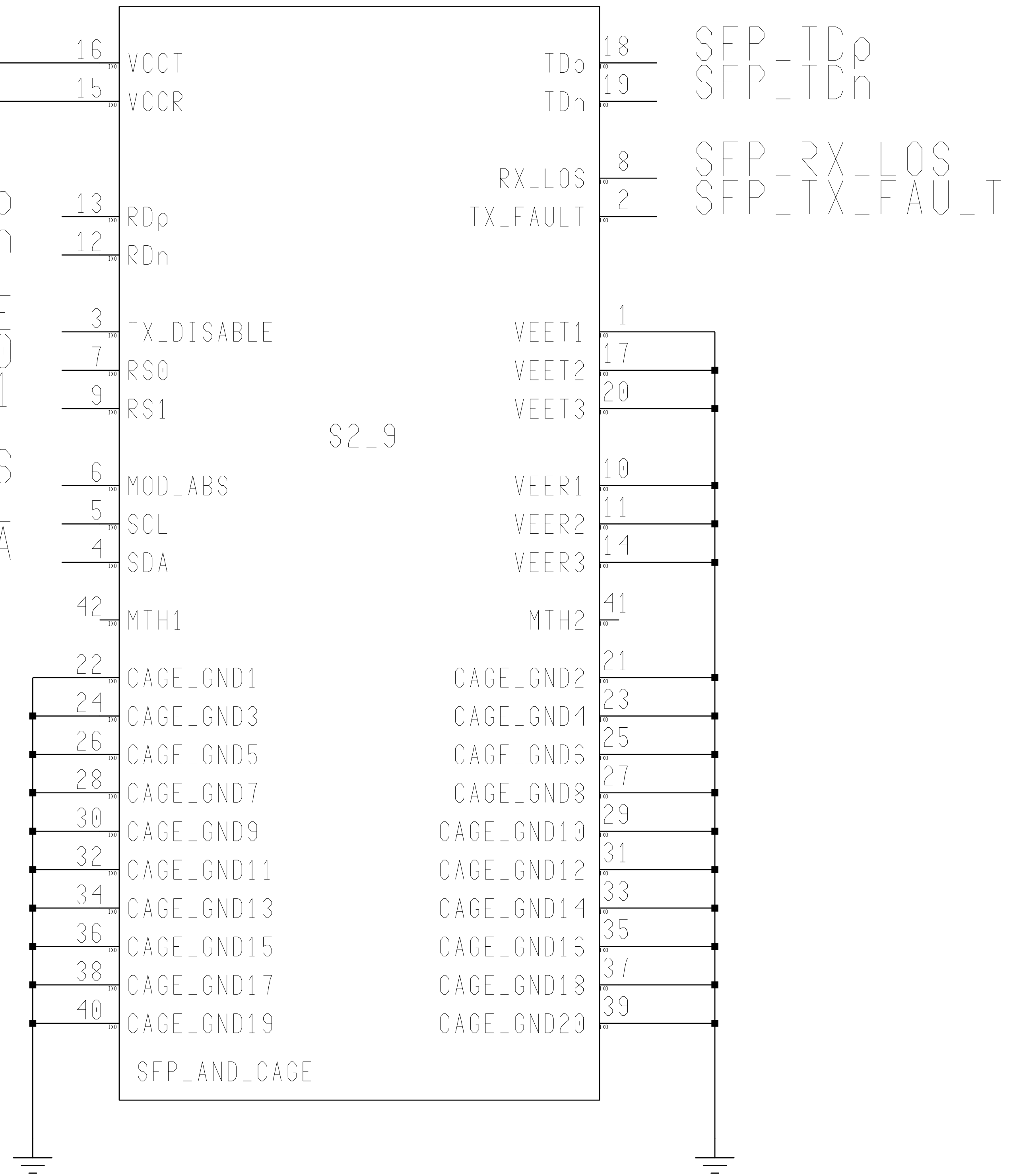
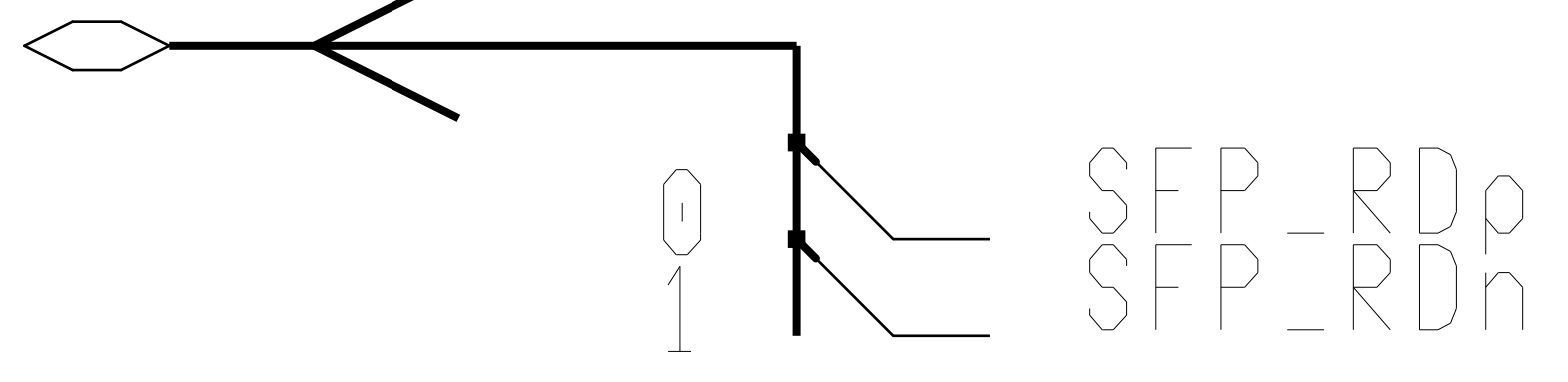
Control[7:0]



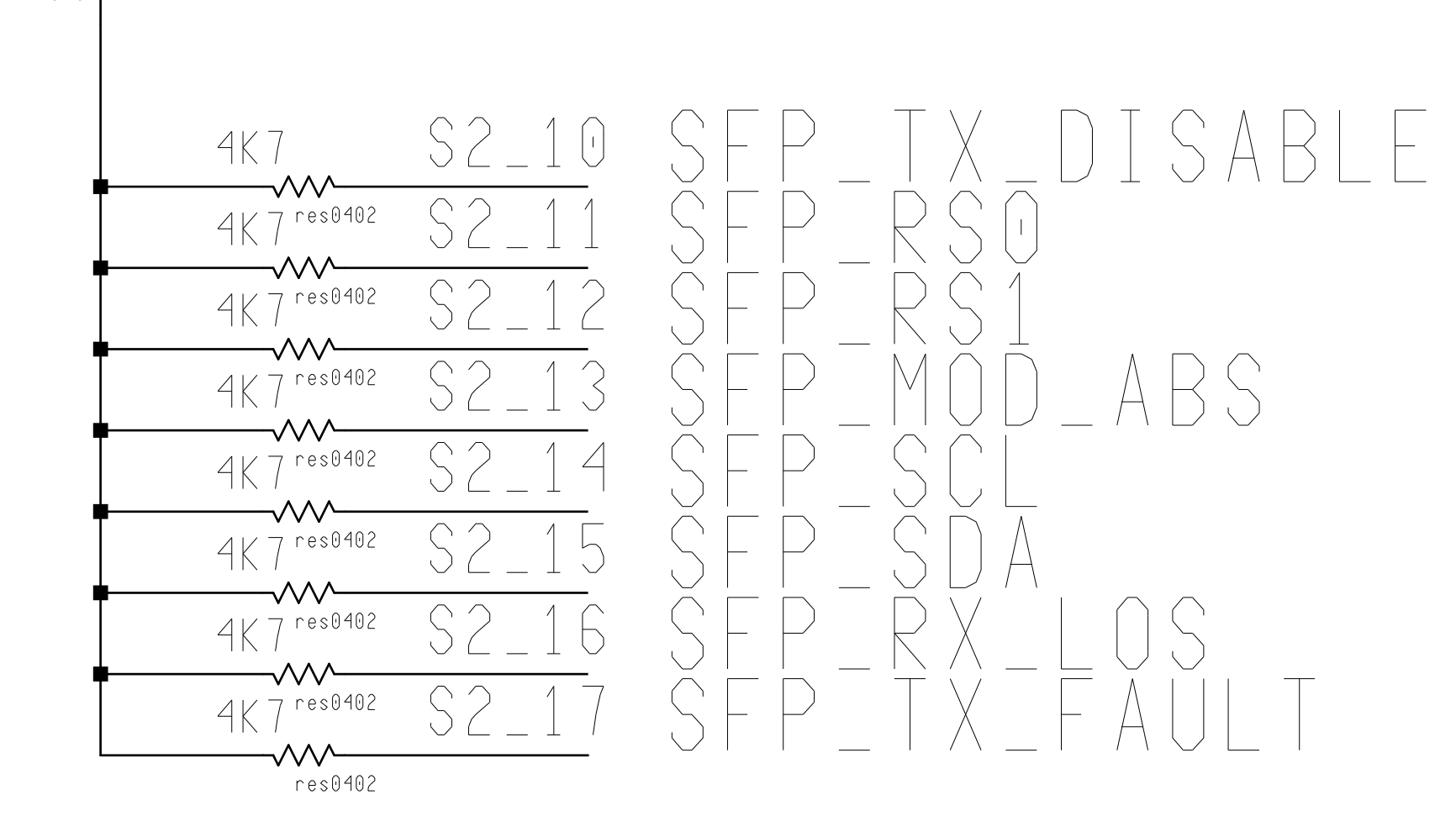
Tx[1:0]



Rx[1:0]



+3.3V



**SFP+ Module**  
**SFP Cage**  
**SFP+ Host Connector, 20-pin**

Engineer: **M. Bogdan**  
 Drawn by: **M. Bogdan**  
 DATE: **2/7/2019**  
 SPC#2938  
 ASM#2939

*DAMIC - ODILE Module*  
 SFP Block\_2  
**The University of Chicago**  
 REV. A | DRW. **2937** | Sheet **12\_2**