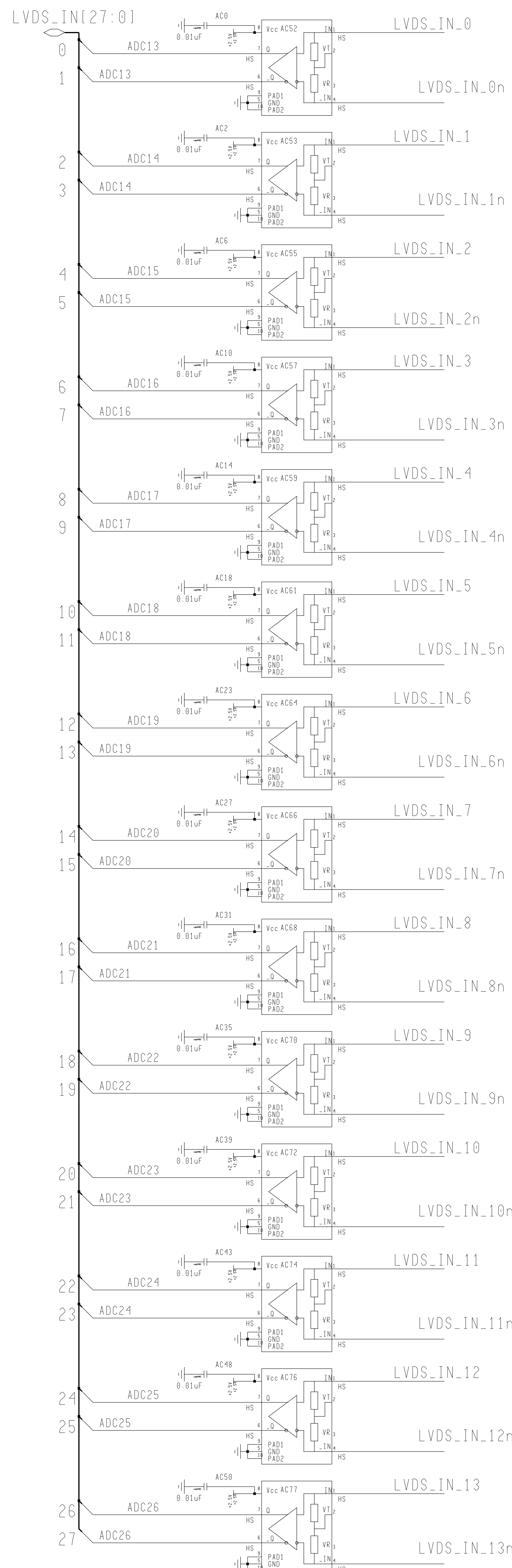
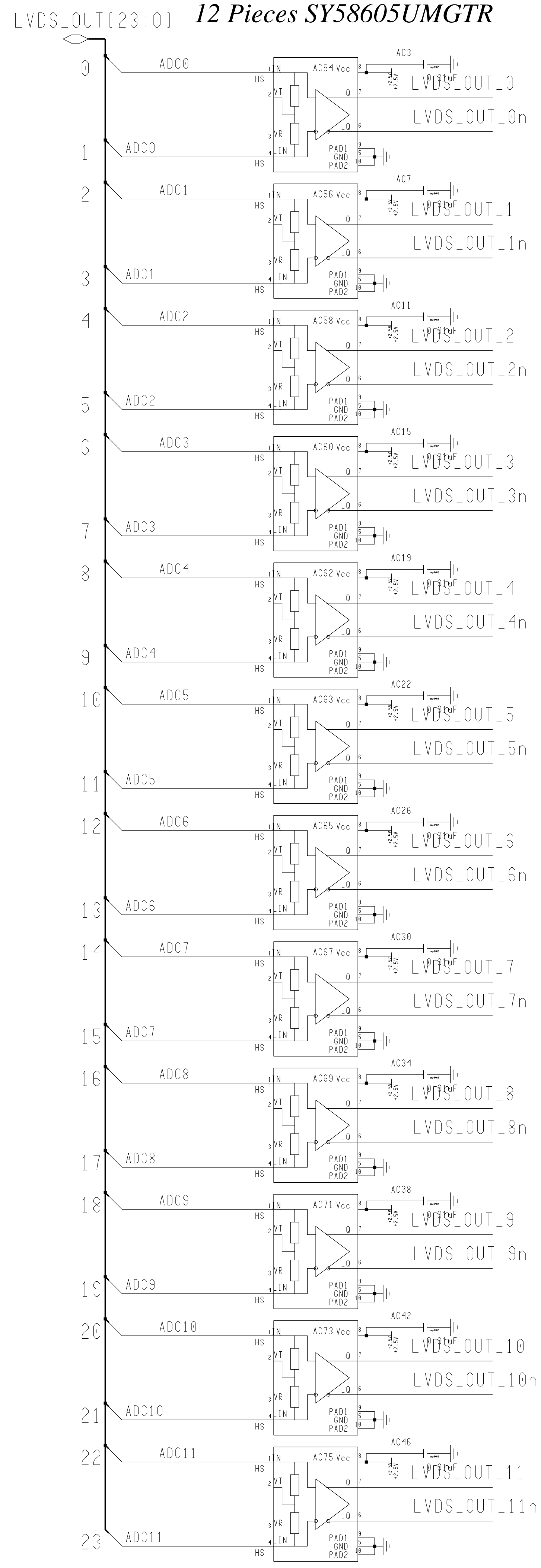


14 Pieces SY58605UMGTR



- AO_0 AC201 50pF
- AO_1 AC202 50pF
- AO_2 AC203 50pF
- AO_3 AC204 50pF
- AO_4 AC205 50pF
- AO_5 AC206 50pF
- AO_6 AC207 50pF
- AO_7 AC208 50pF
- AO_8 AC209 50pF
- AO_9 AC210 50pF
- AO_10 AC211 50pF
- AO_11 AC212 50pF
- AO_12 AC214 50pF
- AO_13 AC215 50pF
- AO_14 AC216 50pF

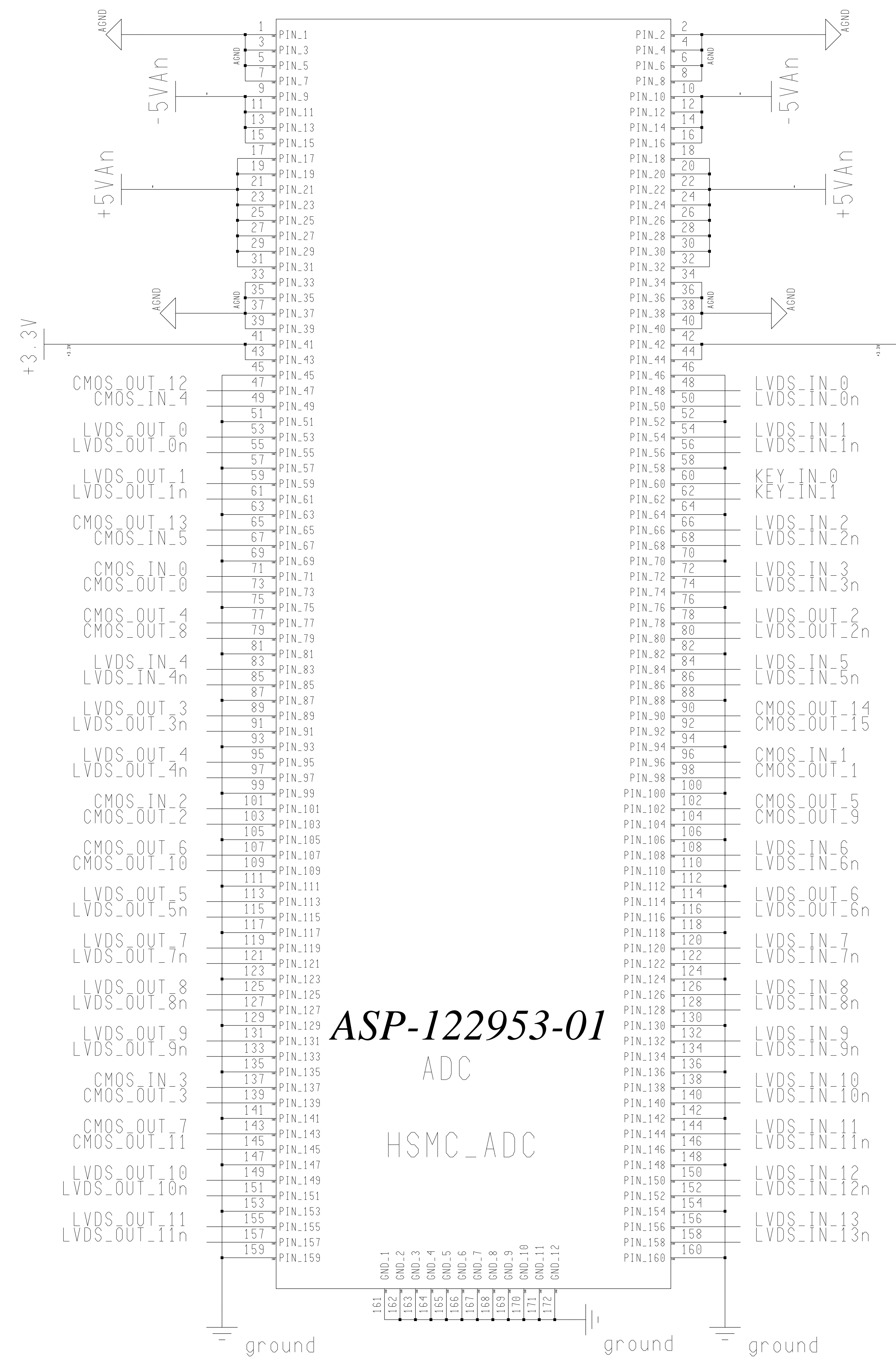
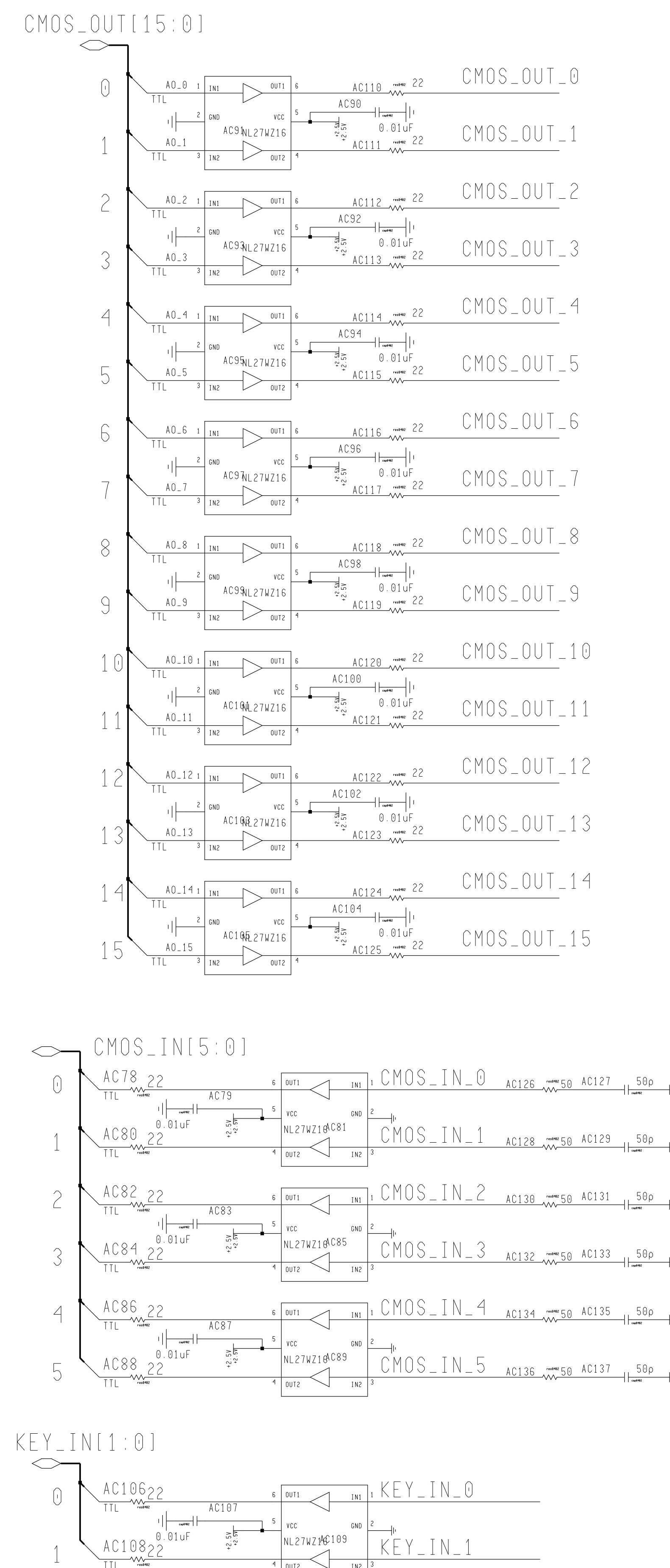
12 Pieces SY58605UMGTR



NOTES:

1. All LVDS_IN, CMOS_IN, KEY_IN lines are driven by the Mezzanine Card.
2. All LVDS_OUT, CMOS_OUT lines are driven by the Motherboard.
3. All unused single ended lines must be terminated with 10 KOhm to GND and a 50 Ohm series with 50 pF to GND on the Mezzanine Card.
4. All unused LVDS_OUT lines must be terminated differentially with 100 Ohm on the Mezzanine Card.
6. The 3.3V Power is Referenced to Digital Ground "ground".
7. The +5Van, -5Van Powers can be referenced to the Analog Ground "AGND" or to the Digital Ground.
8. For 20-BIT ADC with Single Ended Interface, use Connection List below.

12 Pieces NL27WZ16DFT2G



20-BIT ADC with Single Ended Interface:
 DOUT[3:0] -> CMOS_IN[3:0]
 CNVST[3:0] -> CMOS_OUT[3:0]
 SCLK[3:0] -> CMOS_OUT[7:4]
 DIN[3:0] -> CMOS_OUT[11:8]

Engineer: M. Bogdan	DAMIC - ODILE Module
Drawn by: M. Bogdan	ADC Connector
DATE: 4/22/2019	The University of Chicago
SPC#2938 ASM#2939	REV. A DRW. 2937 Sheet 3 of 12