

1	2	3	4
ground <A1	CLRp <A1	SCKp <B1	DINo <C1
ground <A2	CLRn <A2	SCKn <B2	DINn <C2
ground <A3	A0p <A3	ground <B3	A1p <C3
ground <A4	A0n <A4	ground <B4	A1n <C4
ground <A5	A2p <A5	A3p <B5	A4p <C5
+3.3V <A6	A2n <A6	A3n <B6	A4n <C6
+3.3V <A7	+3.3V <A7	+3.3V <B7	+3.3V <C7
+3.3V <A8	A5p <A8	A6p <B8	A7p <C8
ground <A9	A5n <A9	A6n <B9	A7n <C9
ground <A10	PCLK0Ap <A10	ground <B10	PCLK0Bp <C10
ground <A11	PCLK0An <A11	ground <B11	PCLK0Bn <C11
ground <A12	PCLK1Ap <A12	CSp <B12	PCLK1Bp <C12
ground <A13	PCLK1An <A13	CSn <B13	PCLK1Bn <C13
ground <A14	ground <A14	ground <B14	ground <C14
NoConnect 1 <A15	NoConnect 2 <A15	NoConnect 3 <B15	NoConnect 4 <C15
FI Power 1 <A16	FI Power 1 <A16	NoConnect 6 <B16	NoConnect 7 <C16
FI Power 1r <A17	FI Power 1r <A17	NoConnect 9 <B17	NoConnect 10 <C17
FI Power 2 <A18	FI Power 2 <A18	NoConnect 12 <B18	NoConnect 13 <C18
FI Power 2r <A19	FI Power 2r <A19	NoConnect 15 <B19	NoConnect 16 <C19
FI Power 3 <A20	FI Power 3 <A20	NoConnect 18 <B20	NoConnect 19 <C20
FI Power 3r <A21	FI Power 3r <A21	NoConnect 21 <B21	NoConnect 22 <C21
FI Power 4 <A22	FI Power 4 <A22	NoConnect 24 <B22	NoConnect 25 <C22
FI Power 4r <A23	FI Power 4r <A23	NoConnect 27 <B23	NoConnect 28 <C23
FI Power 5 <A24	FI Power 5 <A24	NoConnect 30 <B24	NoConnect 31 <C24
FI Power 5r <A25	FI Power 5r <A25	NoConnect 33 <B25	NoConnect 34 <C25
FI Power 6 <A26	FI Power 6 <A26	NoConnect 36 <B26	NoConnect 37 <C26
FI Power 6r <A27	FI Power 6r <A27	NoConnect 39 <B27	NoConnect 40 <C27
NoConnect 42 <A28	NoConnect 43 <A28	NoConnect 44 <B28	NoConnect 45 <C28
NoConnect 47 <A29	NoConnect 48 <A29	NoConnect 49 <B29	NoConnect 50 <C29
NoConnect 52 <A30	+5Vopto <A30	GNDopto <B30	NoConnect 53 <C30
NoConnect 55 <A31	AnalogOut <A31	GNDopto <B31	NoConnect 56 <C31
NoConnect 58 <A32	-5Vopto <A32	GNDopto <B32	NoConnect 59 <C32
ground <A1			ground <C1
			ground <C2
			ground <C3
			ground <C4
			ground <C5
			ground <C6
			ground <C7
			ground <C8
			ground <C9
			ground <C10
			ground <C11
			ground <C12
			ground <C13
			ground <C14
			NoConnect 5 <C15
			NoConnect 8 <C16
			NoConnect 11 <C17
			NoConnect 14 <C18
			NoConnect 17 <C19
			NoConnect 20 <C20
			NoConnect 23 <C21
			NoConnect 26 <C22
			NoConnect 29 <C23
			NoConnect 32 <C24
			NoConnect 35 <C25
			NoConnect 38 <C26
			NoConnect 41 <C27
			NoConnect 46 <C28
			NoConnect 51 <C29
			NoConnect 54 <C30
			NoConnect 57 <C31
			NoConnect 60 <C32

**Notes:**

1. This is the P1 pin list common for the MMIC and PhSw Modules in QEB.
2. The "FI Power" pins feed through and do not connect to the Backplane.
3. "+3.3V" and "ground" are digital power and ground planes, common for all Modules in Backplane.
4. "+5Vopto", "-5Vopto" and "GNDopto" are local power and ground planes common for MMIC, PhSw and HK Modules.
5. All differential signals, with the exception of "CS", are BLVDS, driven each by an DS92001 (B/LVDS-BLVDS Buffer) and are terminated on Backplane. They shall not be terminated on Modules.
6. "CS" is an LVDS signal (point-to-point) and it has to be terminated on Modules.

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Drawn by	M. Bogdan		
R&D CHK		TITLE	Size B
DATE:	6/4/05	P1-MMIC & PhSw - Pin List <i>QEB-Backplane</i>	
TIME:	2:00 pm		
QA CHK		REV A	DRW. B-xxxx   Sheet x of n