



DigOut1(63:0)
 FP digital output
 DigOut2(17:0)

Engineer	M. Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637		
Drawn by	M. Bogdan			
R&D CHK		QEBB - LVDS I/O - SAB SAB for J1 Slot 8 of QEBB		
DATE:	1/09/05			
TIME:	1:30 pm			
QA CHK		REV A	DRW. B-2572	Sheet 1 of 2