



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES . XX . XXX DO NOT SCALE DRAWING			CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP			
TREATMENT			APPROVALS	DATE	TITLE HELIX - Ideal Diode Board Assembly drawing - Bottom			
FINISH			DRAWN M. Bogdan	11/5/2020				
SIMILAR TO			CHECKED M. Bogdan	11/5/2020	SIZE	FSCM NO.	DWG. NO. B-2972-Bot	REV. A
ACT. WT		CALC WT	ISSUED		SCALE		SHEET 1 of 1	