

**I2C Address**

0



1



2



3



4



5



6



7



PM1\_ADR0  
Connects to a PV OR+Shunt Board  
**PV Array Monitor**  
PM1\_ADR1  
PM2\_ADR0 I2C[2:0]  
Sheet\_2\_1 PVA  
PM2\_ADR1

PM1\_ADR0  
Connects to a PV OR+Shunt Board  
**PV Array Monitor**  
PM1\_ADR1  
PM2\_ADR0 I2C[2:0]  
Sheet\_2\_2 PVA  
PM2\_ADR1

PM1\_ADR0  
Connects to a PV OR+Shunt Board  
**PV Array Monitor**  
PM1\_ADR1  
PM2\_ADR0 I2C[2:0]  
Sheet\_2\_3 PVA  
PM2\_ADR1

PM1\_ADR0  
Connects to a PV OR+Shunt Board  
**PV Array Monitor**  
PM1\_ADR1  
PM2\_ADR0 I2C[2:0]  
Sheet\_2\_4 PVA  
PM2\_ADR1

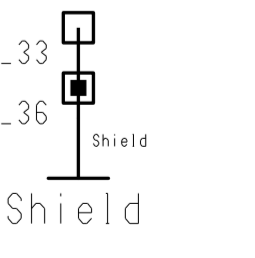
Connects to Battery Shunt Board  
I2C[2:0]  
Sheet\_3  
BTTR\_CRRNT\_SENSE[1:0]  
BOX\_TEMP  
PM\_ADR0  
PM\_ADR1  
**Battery Current Monitor**

I2C[2:0]  
Sheet\_5  
Sheet\_6  
Sheet\_7  
Sheet\_8  
PVA[6:0]  
PVA[6:0]  
**Isolated Amplifiers To Relay Box**

VBUS  
VBUS  
**Power Block**  
Sheet\_4

**I2C Address**

8



SCH-2976  
SPC-2977  
ASM-2978

Engineer:	M. Bogdan
Drawn by:	M. Bogdan
DATE:	1/19/2021

**The University of Chicago**  
5620 S. Ellis Ave. Chicago, IL 60637  
*HELIX Monitor Board*

REV. A	DRW.	<b>2976</b>	Sheet	1
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