

BOARD'S DRILL SCHEDULE

DRILL SYMBOL	DRILL SIZE	COUNT	PLATED	Tolerance	COMMENT
0	. 02	24	YES		
	. 041	5	YES		
0	.047244094	22	YES		
	.051181102	4	NO		
\ominus	.07480315	28	YES		
	. 15	1	NO		
\oplus	. 16535433	7	NO		
	. 16929134	8	YES		
	. 17716535	1	NO		
	. 23622047	4	NO		

2-Layer PCB - 85×75mm, 0.093" Thick

Board Characteristics

- 1. Material RF4
- 2. Minimum trace width: 0.2mm
- 3. 2 Oz Copper on all layers
- 4. Silkscreen on Component Side.
- 5. Soldermask on Top and Bottom, as per Gerbers

TOLERANCES ARE: FRACTIONS DECIMALS ANGLES	CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP					
. A A . X X X D O N O T S C A L E D R A W I N G	APPROVALS	DATE	TITLE HE		nt Board			
TREATMENT	DRAWN M.Bogdan	10/15/202		Specification Drawina				
FINISH	CHECKED M. Bogdan	10/15/202		1				
	ISSUED				299	95-0		
SIMILAR TO ACT. WT CALC WT			SCALE 1/			SHEET		