

BOARD'S DRILL SCHEDULE

DRILL SYMBOL	DRILL SIZE	COUNT	PLATED	Tolerance	COMMENT
0	.018	4	YES		
	.047244094	3	YES		
\bigcirc	.051181102	1	NO		
	.059055118	10	YES		
\ominus	.07480315	4	YES		
	. 12834646	4	NO		
\oplus	. 16535433	1	NO		

2-Layer PCB - 0.062" Thick

Board Characteristics

- 2. Minimum trace width: 0.6mm
- 3. 1 Oz Copper on all layers
- 4. Silkscreen on Component Side.
- 5. Soldermask on Top and Bottom, as per Gerbers

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES	CONTRACT NO.		UNIVERSITY OF CHICAGO Electronics development group		
. X X . X X X DO NOT SCALE DRAWING	APPROVALS	DATE	HELIX Relay SW		
TREATMENT	DRAWN M. Bogdan	7/13/2001	Specification Drawing		
FINISH	^{CHECKED} M. Bogdan	7/13/2001	SIZE FSCM NO. DWG.NO. REV.		
	I S S U E D		5001		

