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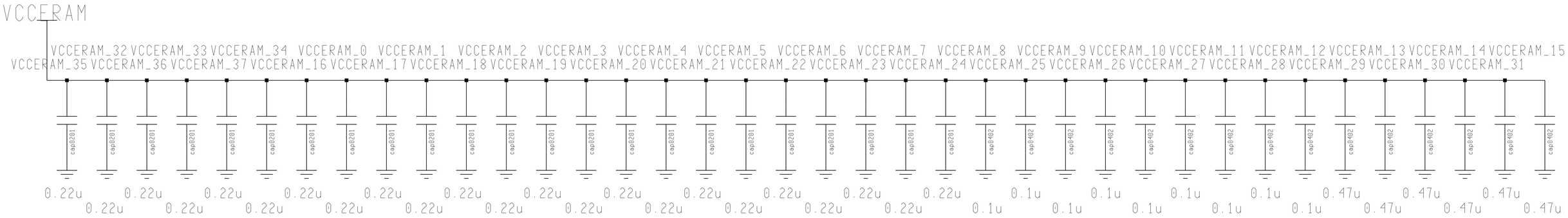

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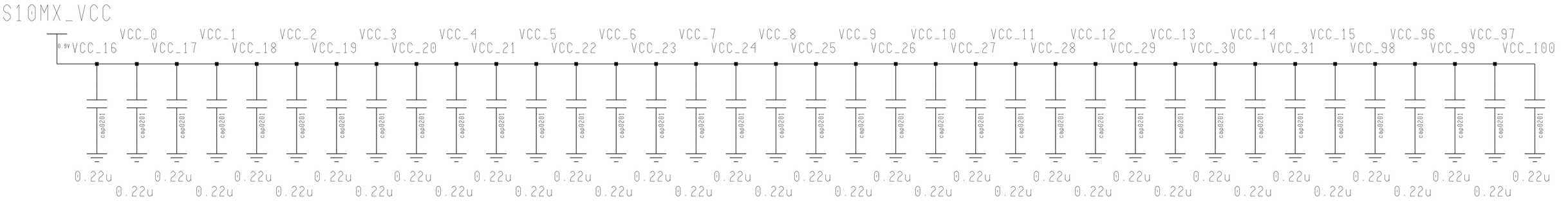

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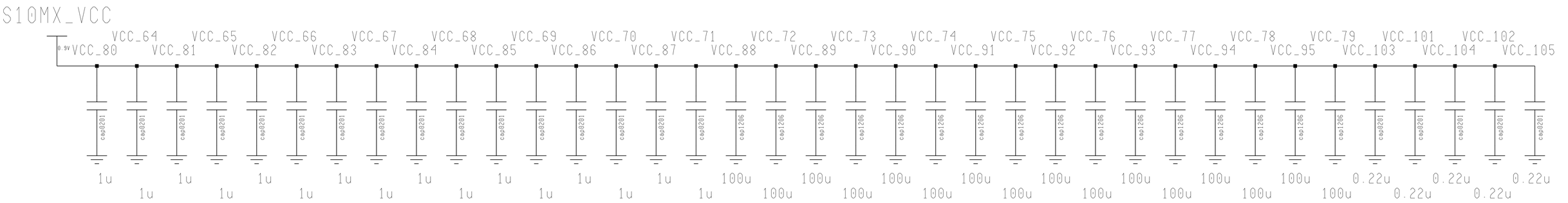

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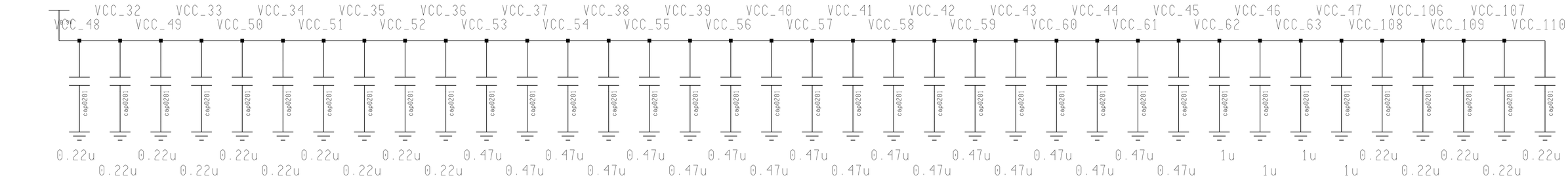

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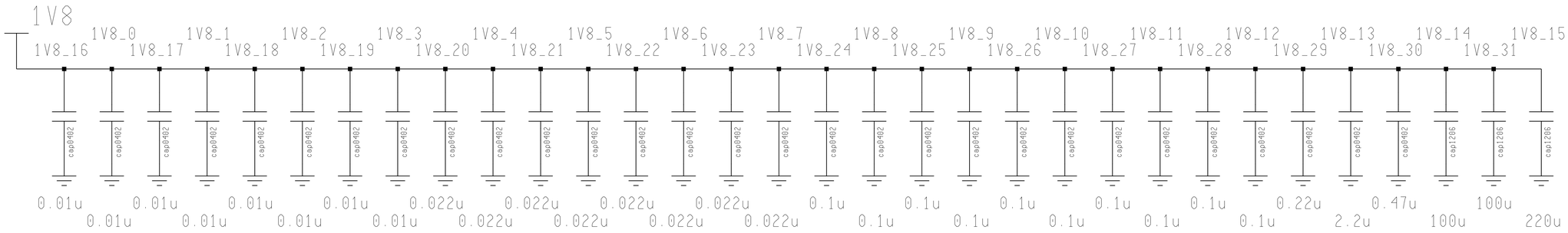

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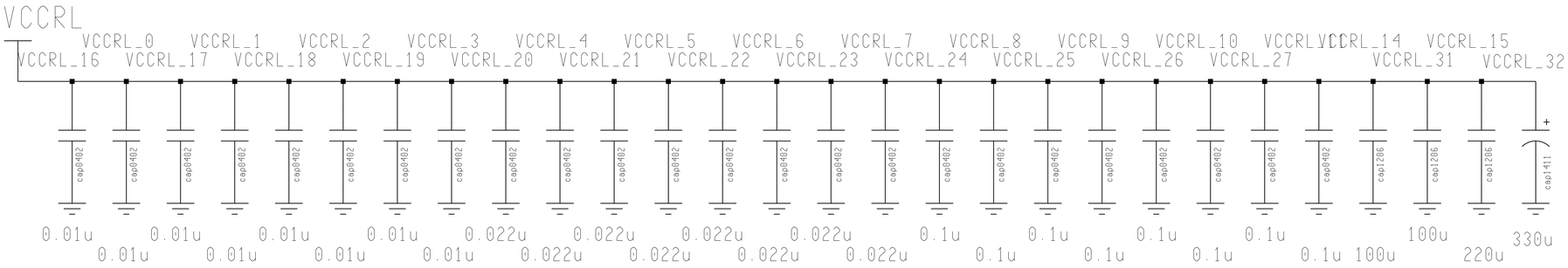

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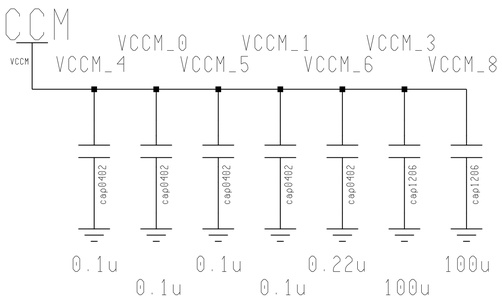

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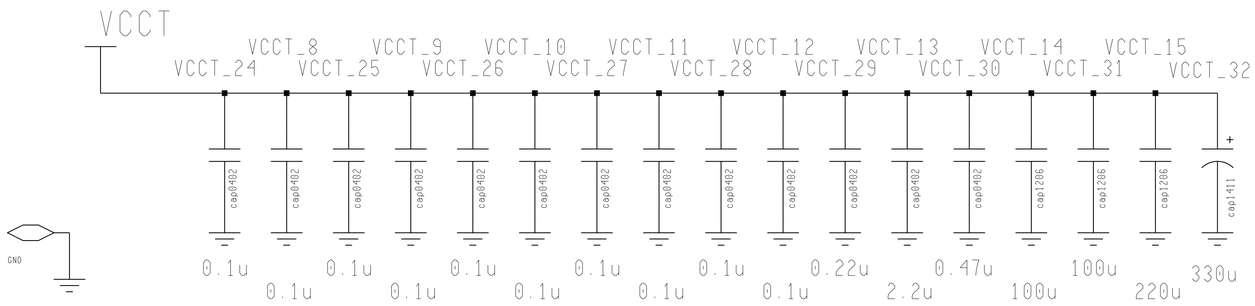

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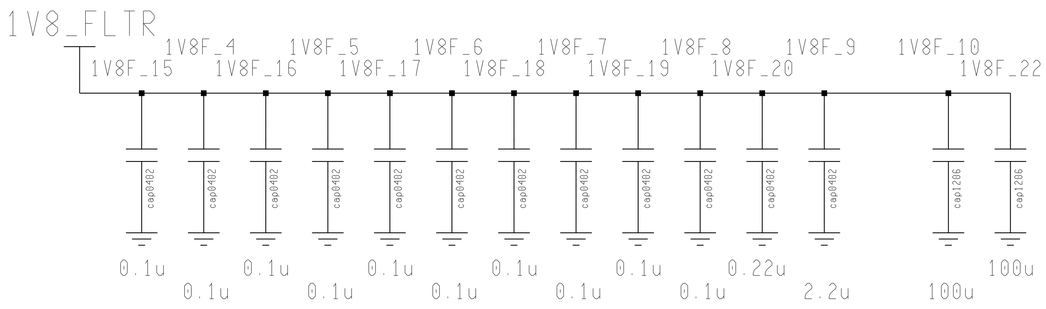

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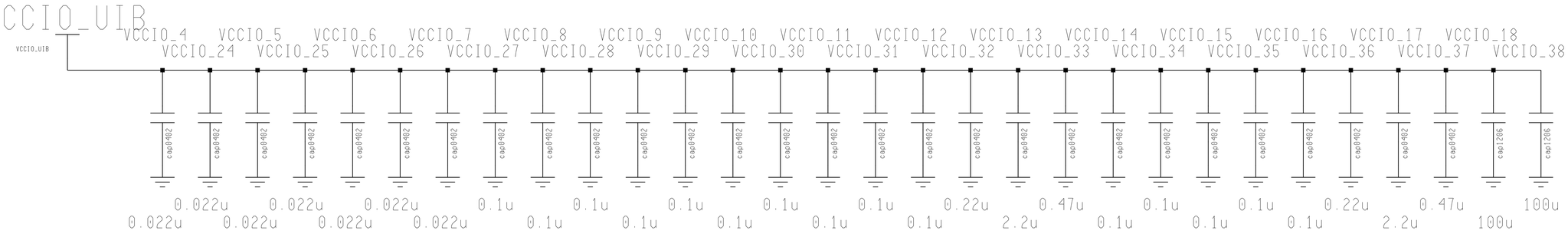

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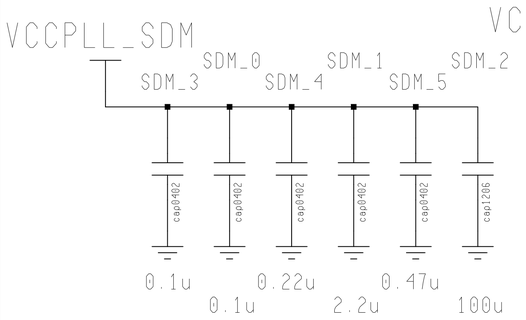

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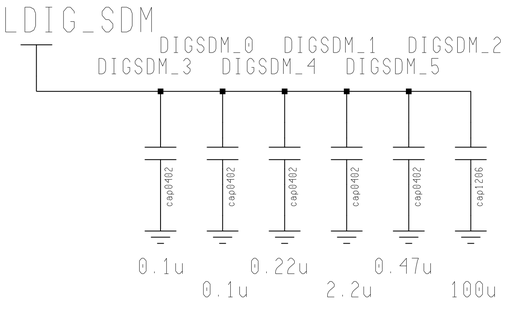

Engineer: M. Bogdan	*Track Fitter Mezzanine Card*		
Drawn by: M. Bogdan	**FPGA Decoupling**		
DATE: 9/5/2019	**The University of Chicago**		
SPC#2929 ASM#2930	REV. A	DRW. 2928	Sheet 12