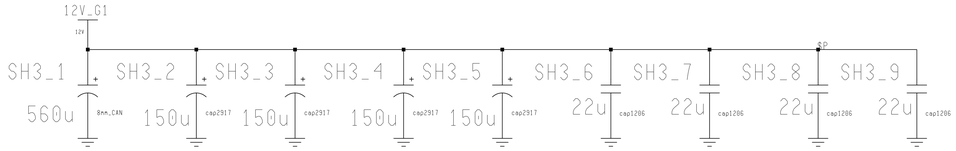
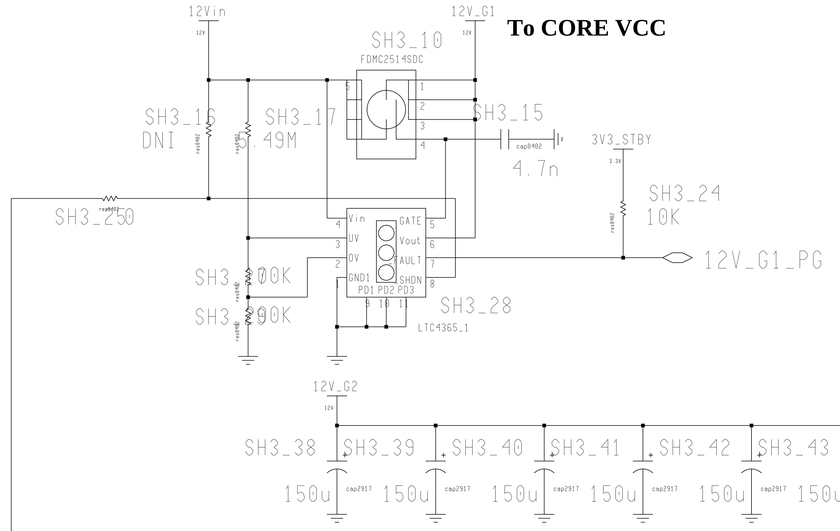


16SVPF560M



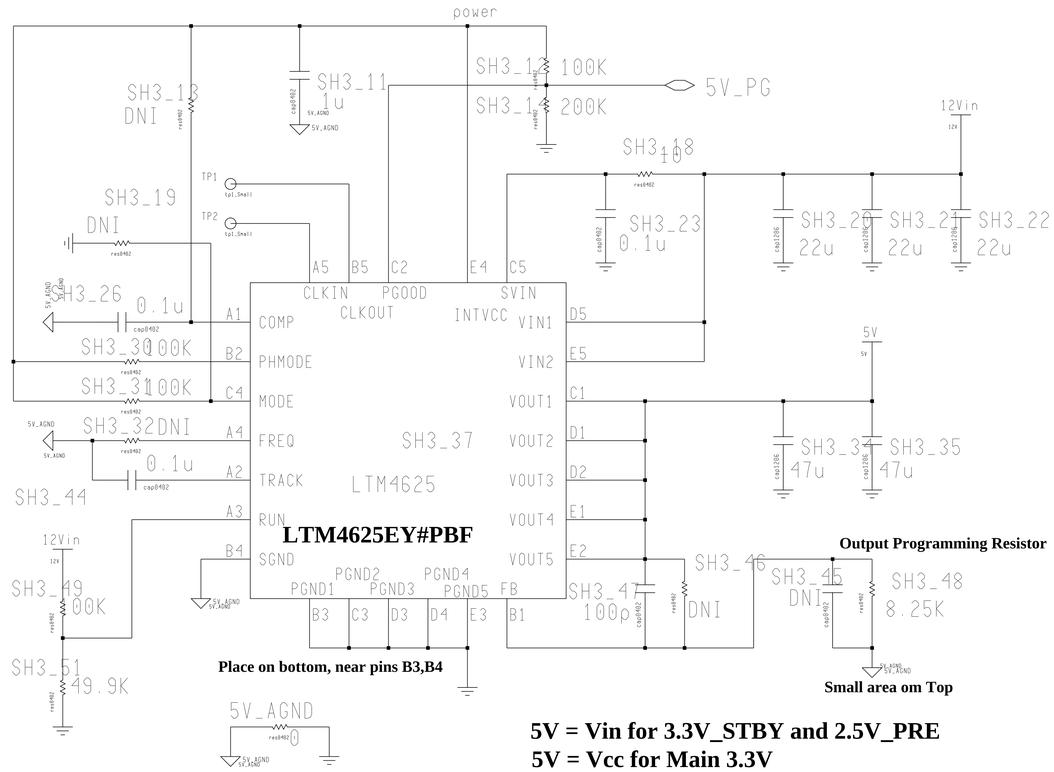
To CORE VCC

To Main 3.3V Supply

FDMC2514SDC
LTC4365HDDB-1#TRMPBF

Low when 12Vin < 10.5V
Low when 12Vin > 13.5V

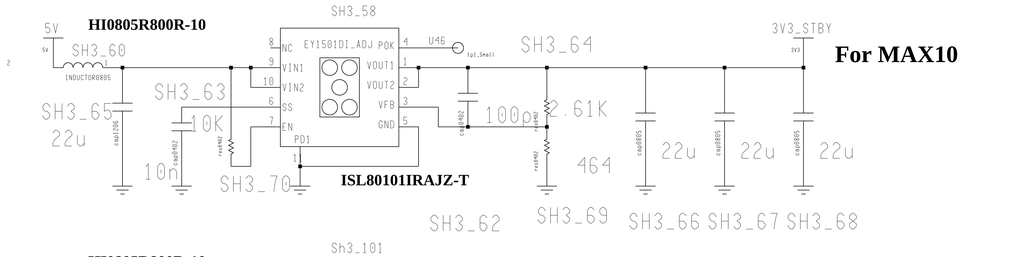
TPS3700DDCR



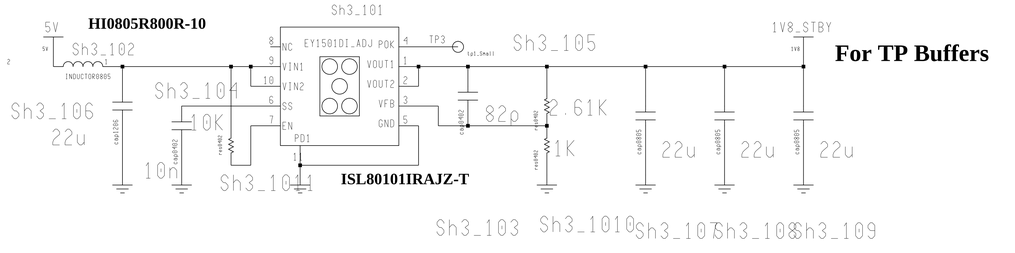
LTM4625EY#PBF

Output Programming Resistor

5V = Vin for 3.3V_STBY and 2.5V_PRE
5V = Vcc for Main 3.3V
5V = Vcc for VCCIO-UIB



For MAX10



For TP Buffers

This Block receives the 12Vin from Motherboard, and generates +5V, 3V3_STBY
The 5V, 3V3_STBY supplies will operate for 4V < 12Vin < 20V.

Engineer: M. Bogdan	*Track Fitter Mezzanine Card*		
Drawn by: M. Bogdan	**Power Input**		
DATE: 8/21/2019	**The University of Chicago**		
SPC#2929 ASM#2930	REV. A	DRW. 2928	Sheet 3