

CTRL_3V3_INT_H	→	ADC1IN1_D1	→	IO_J5	→	QSFP_INT_B	→	CLK2p_G9	→	IO_A3
ADC1IN2_C2	→	ADC1IN2_C2	→	IO_N5	→	QSFP_MODSEL_F	→	CLK2n_G10	→	IO_E6
CTRL_3V3_MODPRS_F	→	ADC1IN3_E3	→	IO_N6	→	QSFP_RST_B	→	CLK3p_F13	→	IO_B3
CTRL_3V3_INT_G	→	ADC1IN4_E4	→	IO_N7	→	QSFP_RST_F	→	CLK3n_E13	→	CRC_ERROR_D6
CTRL_3V3_MODPRS_D	→	ADC1IN5_C1	→	IO_M7	→	QSFP_MODPRS_E	→	IO_F12	→	IO_B4
CTRL_3V3_MODPRS_H	→	ADC1IN6_B1	→	IO_N8	→	QSFP_RST_J	→	IO_E12	→	nSTATUS_C4
CTRL_3V3_INT_F	→	ADC1IN7_F1	→	IO_J6	→		→	DPCLK3_F9	→	IO_A5
CTRL_3V3_INT_E	→	ADC1IN8_E1	→	IO_M8	→		→	VREFB6N0_D13	→	CONF_DONE_C5
	→	JTAGEN_E5	→	IO_K6	→		→	DPCLK2_F10	→	IO_A2
TMSq	→	TMS_G1	→	IO_M9	→		→	IO_C13	→	IO_B2
	→	VREFB1N0_H1	→	IO_J7	→		→	IO_F8	→	GND_N13
TCKq	→	TCK_G2	→	IO_K7	→	QSFP_INT_J	→	IO_B12	→	GND_N1
TDIq	→	TDI_F5	→	IO_M12	→	QSFP_INT_E	→	IO_B11	→	GND_M6
TD0q	→	TD0_F6	→	IO_N12	→	QSFP_INT_F	→	IO_E9	→	GND_L9
CTRL_3V3_LPMOD_A	→	IO_F4	→	IO_M13	→	QSFP_INT_D	→	IO_C12	→	GND_J4
CTRL_3V3_MODSEL_D	→	IO_G4	→	IO_N10	→	QSFP_MODPRS_H	→	IO_B13	→	GND_H12
CTRL_3V3_LPMOD_D	→	IO_H2	→	IO_M12	→	QSFP_MODPRS_D	→	IO_C11	→	GND_G7
CTRL_3V3_MODSEL_E	→	IO_H3	→	IO_N9	→	QSFP_MODPRS_F	→	IO_A12	→	GND_F3
QSFP_LPMOD_D	→	CLK0n_G5	→	CTRL_3V3_LPMOD_H	→	QSFP_MODPRS_G	→	IO_E10	→	GND_E11
QSFP_MODSEL_E	→	IO_J1	→	CTRL_3V3_LPMOD_J	→	QSFP_INT_G	→	IO_D9	→	GND_D5
QSFP_LPMOD_A	→	CLK0p_H6	→	CTRL_3V3_LPMOD_C	→	QSFP_INT_H	→	IO_D12	→	GND_C3
QSFP_MODSEL_D	→	IO_J2	→	CTRL_3V3_LPMOD_G	→	QSFP_RST_A	→	IO_D11	→	GND_B8
QSFP_RST_E	→	CLK1n_H5	→	CTRL_3V3_LPMOD_I	→	QSFP_MODPRS_J	→	CLK5p_C10	→	GND_A13
QSFP_MODSEL_C	→	IO_M1	→	CTRL_3V3_MODSEL_G	→	QSFP_MODSEL_G	→	IO_A8	→	GND_A1
QSFP_AUX_0	→	CLK1p_H4	→	CTRL_3V3_MODSEL_J	→	CTRL_3V3_INT_D	→	CLK5n_C9	→	REFGNDq
QSFP_MODSEL_B	→	IO_M2	→	CTRL_3V3_LPMOD_F	→	CTRL_3V3_INT_J	→	IO_A9	→	VCCIO1Aq
QSFP_MODSEL_A	→	DPCLK0_N2	→	QSFP_LPMOD_F	→	CTRL_3V3_RST_B	→	IO_B10	→	VCCIO1Bq
	→	VREFB2N0_L1	→	QSFP_RST_G	→	CTRL_3V3_MODPRS_G	→	IO_A10	→	VCCIO2q
QSFP_RST_D	→	DPCLK1_N3	→	QSFP_MODSEL_H	→	CTRL_3V3_MODSEL_F	→	DEV_CLRn_B9	→	VCCIO2q
QSFP_LPMOD_B	→	IO_L2	→	QSFP_LPMOD_H	→	CTRL_3V3_MODSEL_E	→	IO_A11	→	VCCIO3q
QSFP_RST_H	→	CLKOUTn_M3	→	QSFP_LPMOD_B	→	CTRL_3V3_INT_B	→	DEV_OE_D8	→	VCCIO3q
QSFP_INT_A	→	IO_K1	→	QSFP_MODPRS_A	→	CTRL_3V3_RST_J	→	IO_E8	→	VCCIO3q
QSFP_LPMOD_E	→	CLKOUTp_L3	→	QSFP_AUX_3	→	CTRL_3V3_RST_F	→	VREFB8N0_B7	→	VCCIO3q
QSFP_RST_C	→	IO_K2	→	QSFP_LPMOD_J	→	CTRL_3V3_MODPRS_C	→	CONFIG_SEL_D7	→	VCCIO5q
CTRL_3V3_RST_C	→	IO_L5	→	QSFP_AUX_2	→	CONFIG_SELq	→	IO_A7	→	VCCIO5q
CTRL_3V3_LPMOD_E	→	IO_M4	→	QSFP_AUX_1	→	CTRL_3V3_MODPRS_E	→	nCONFIG_E7	→	VCCIO6q
CTRL_3V3_RST_A	→	IO_L4	→	QSFP_INT_C	→	CTRL_3V3_INT_C	→	IO_A6	→	VCCIO6q
CTRL_3V3_RST_D	→	IO_M5	→	QSFP_MODPRS_C	→	CTRL_3V3_INT_J	→	IO_B6	→	VCCIO8q
CTRL_3V3_LPMOD_B	→	IO_K5	→	QSFP_LPMOD_C	→	IO_A4	→	IO_A4	→	VCCIO8q
CTRL_3V3_RST_H	→	IO_N4	→	IO_G12	→	IO_B5	→	IO_B5	→	VCCIO8q
										VCCA1q

