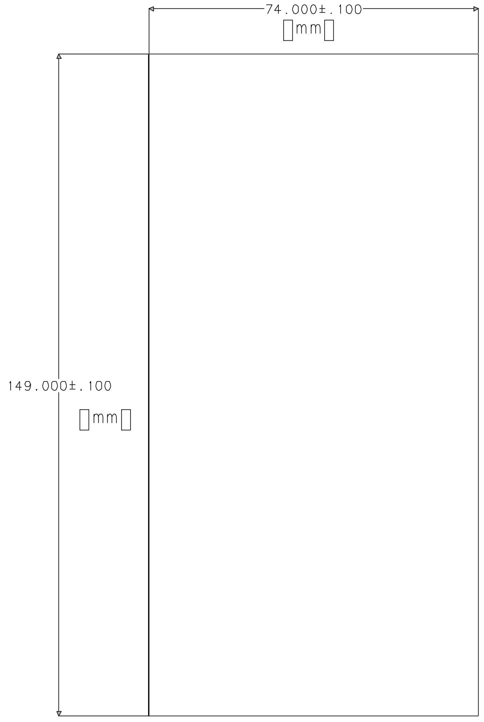
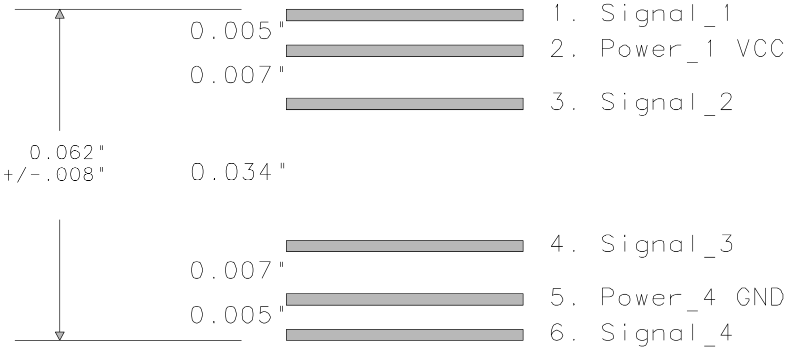
DWG. NO. B-2518 SH 1 REV. A



## Layer Order



## Board Characteristics

1. Dimensions are given in inches and mm;
2. Material FR4,
3. Min trace width: 0.006" on all layers;
4. Min. clearence: 0.006" on all layers;
5. 1 oz copper for top, bottom and power layers;
   1/2 oz copper for embedded signal layers;
6. Electroless Nickel/Immersion Gold plating,Apply soldermask;
7. FHS tolerances: 0.002";
8. Board Thickness and inter-layer spacing as specified;
9. Silkscreen on both sides;
10. Trace inpedance: 55 Ohm +/-5 Ohm
    for 0.006" traces on all layers;

BOARD's DRILL SCHEDULE

| FHS [inches] | COUNT | PLATED | COMMENT |
|---|---|---|---|
| .02 | 390 | YES | |
| .041 | 67 | YES | |
| .042 | 80 | YES | |
| .0551 | 4 | NO | |
| .057 | 2 | YES | |
| .1063 | 8 | NO | |

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ARE:
FRACTIONS DECIMALS ANGLES
.XX
.XXX
DO NOT SCALE DRAWING

TREATMENT

FINISH

SIMILAR TO

ACT. WT

CALC WT

CONTRACT NO.

| APPROVALS | | DATE |
|---|---|---|
| DRAWN | M.Bogdan | 11/12/02 |
| CHECKED | M.Bogdan | 11/12/02 |
| ISSUED | | |

The University of Chicago
Electronics Development Group

TITLE
CDF Level 2 TAXI-Tx Board

SIZE B
FSCM NO.
DWG. NO. B-2518
REV. A
SCALE
SHEET 1/1

THIS SHEET IS COMPUTER GENERATED