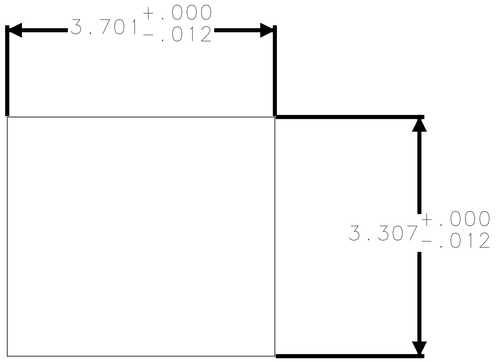

Board Characteristics

0. All dimensions are given in inches unless specified otherwise.

2. Minimum trace width: 0.006" and clearence: 0.006" on Signal_1,6 (Top and Bottom);

4. 1 oz copper for all power layers and for Signal_1,2 (Top and Bottom)

BOARD's DRILL SCHEDULE

DRILL SYMBOL	DRILL SIZE	COUNT	PLATED	Tolerance	COMMENT
○	.02	42	YES	---	
⊞	.041	47	YES	---	
⌀	.05	100	YES	---	
⊟	.12	4	YES	---	

8. Silkscreen on Component Side.

10. FHS tolerances: +/- 0.003

12. Zc=50 Ohm +/- 5 Ohm for all 6 mil traces
Perform TDR test for all signal layers.
Present TDR test results for all signal layers.

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ARE:
FRACTIONS DECIMALS ANGLES
.XX
.XXX
DO NOT SCALE DRAWING

TREATMENT

FINISH

SIMILAR TO | ACT. WT | CALC WT

CONTRACT NO.

APPROVALS	DATE
DRAWN M.Bogdan	6/16/14
CHECKED M.Bogdan	6/16/14
ISSUED	

UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE Video Inverter Board Specification Drawing

SIZE B | FSCM NO. | DWG.NO. 2833 | REV. A

SCALE 1/1 | SHEET

THIS SHEET IS COMPUTER GENERATED