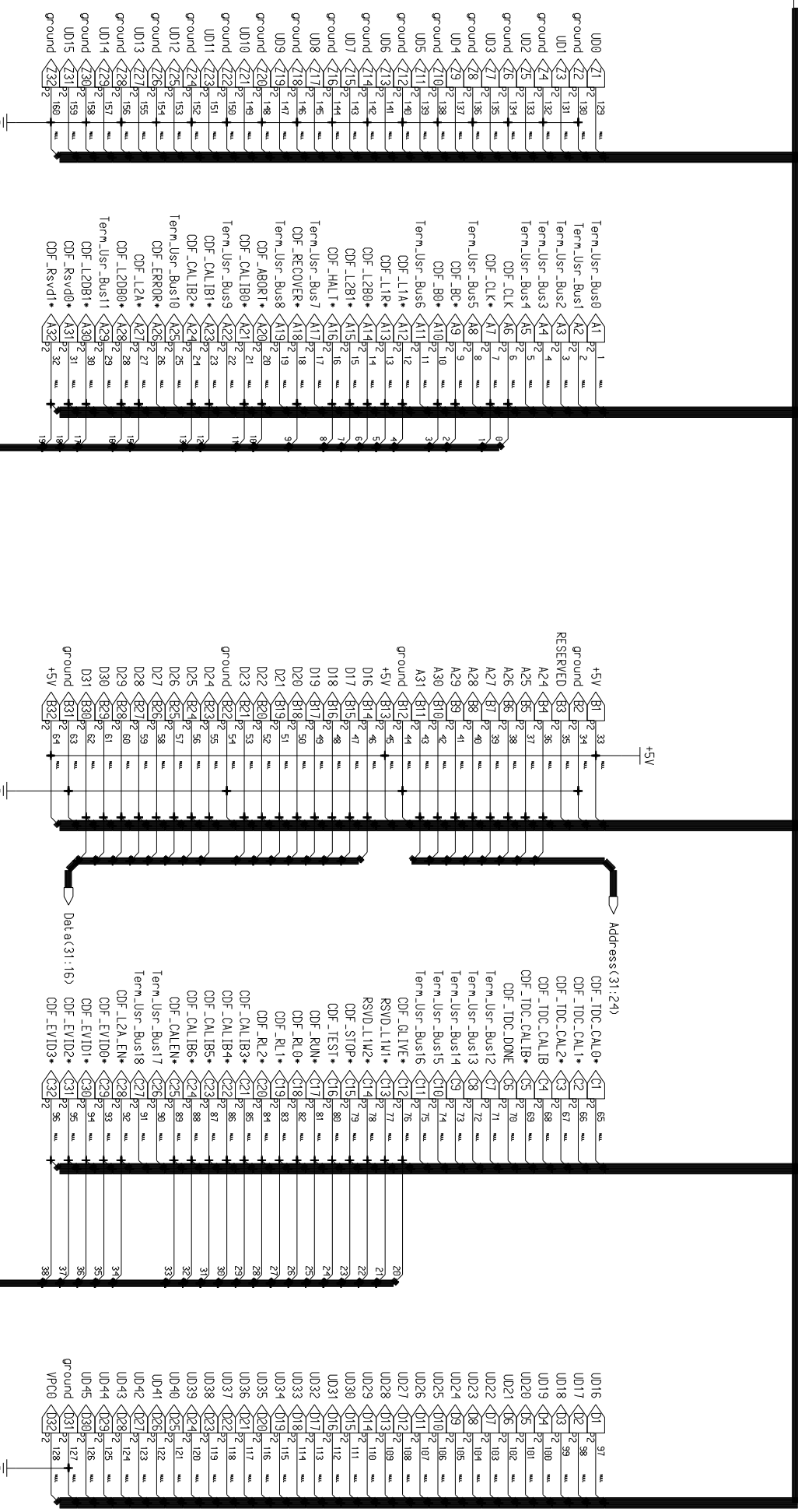


P2



Rows Z and D are in accordance with the VME64 Extensions standard. Note: the positioning of rows A, B and C should be still in accordance with the VME Standard.

All user-defined signals on P2 are totem-pole TTL excluding:
 a. Geographical Addresses are pulled-up/grounded wires
 b. 132ns Clock is differential PECL: CLK - CLK*
 c. ERROR* is open collector TTL

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		TITLE VMEbus_P2	
DATE 15 Dec 95	REVISION DESCRIPTION Dr: final design -- YF	SHEET 5 OF 13	C-2392
DATE 31 Aug 95	ADD row Z and D to conform to the VME64 Ext. Spec. Correction to conform to the VME P2 Spec.	DATE 18 Nov 1996	REV B
DATE 18 Nov 95	2nd Prototype - Sig Files	DRN	JM