



Engineer	M. Bogdan
Drawn by	M. Bogdan
R&D CHK	
DATE:	7/29/04
TIME:	2:00 pm
QA CHK	

The University of Chicago  
 5640 S. Ellis Ave.  
 Chicago, IL 60637

TITLE	<b>VME Chip</b> <i>Time to Digital Converter</i>	Size
		C
REV B	DRW C-2529/64	Sheet 4 of 16