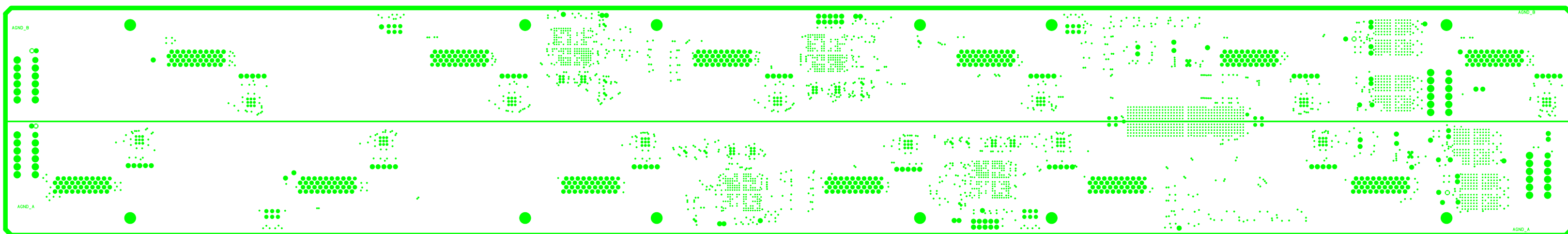
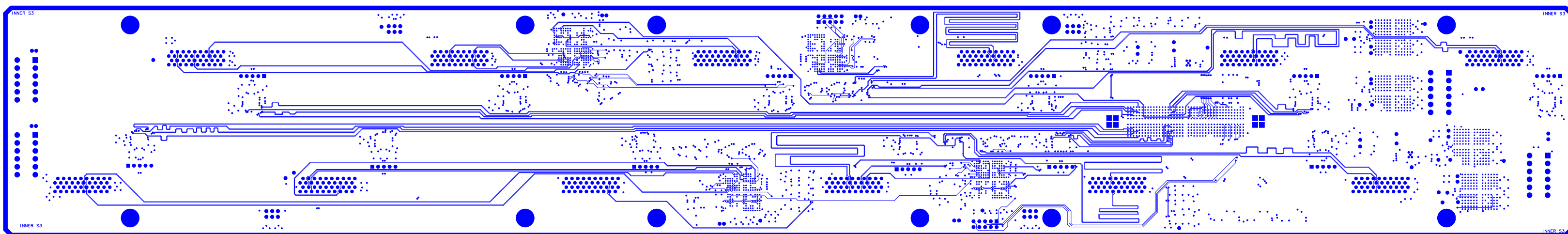


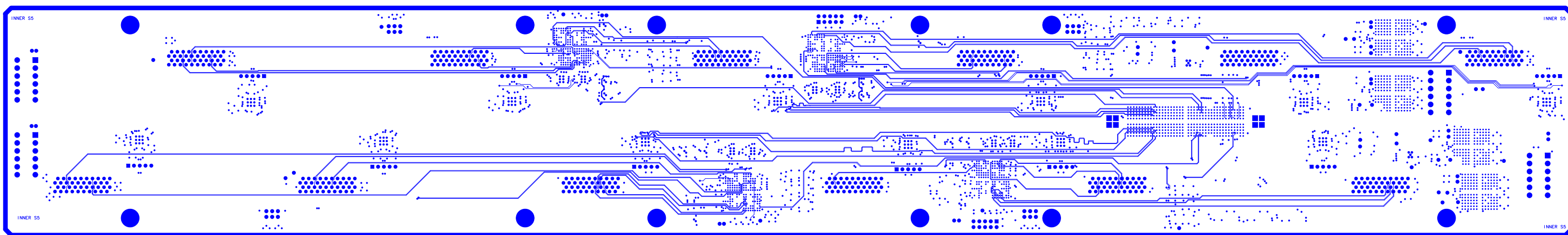
Artwork_1: Top Signal Layer



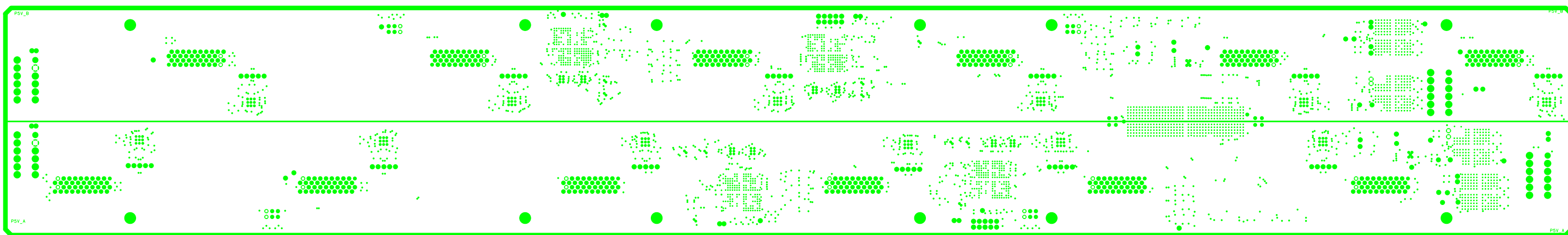
Artwork_2: POWER_13, POWER14 (AGND_A, AGND_B)



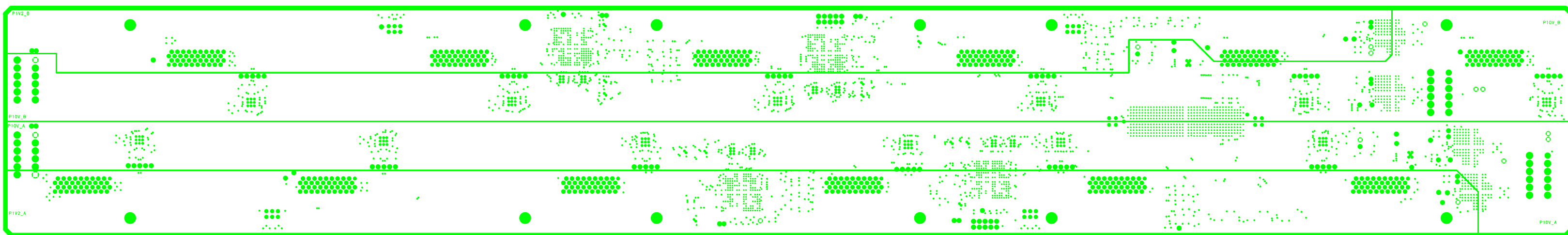
Artwork_3: Signal_3



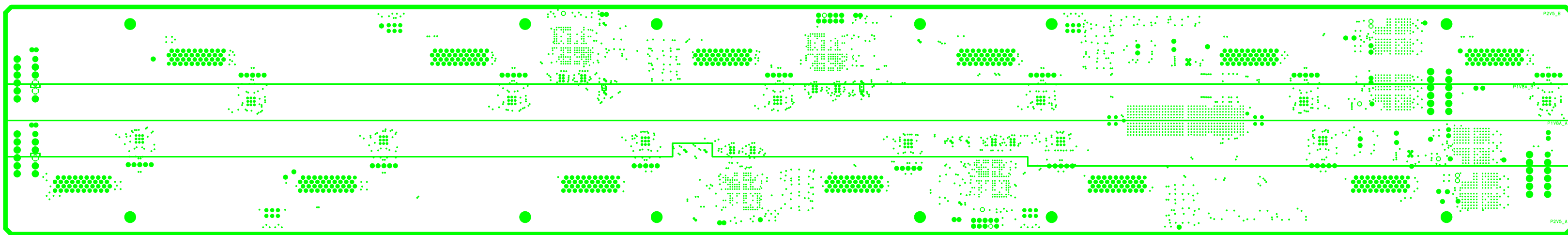
Artwork_4: Singnal_5



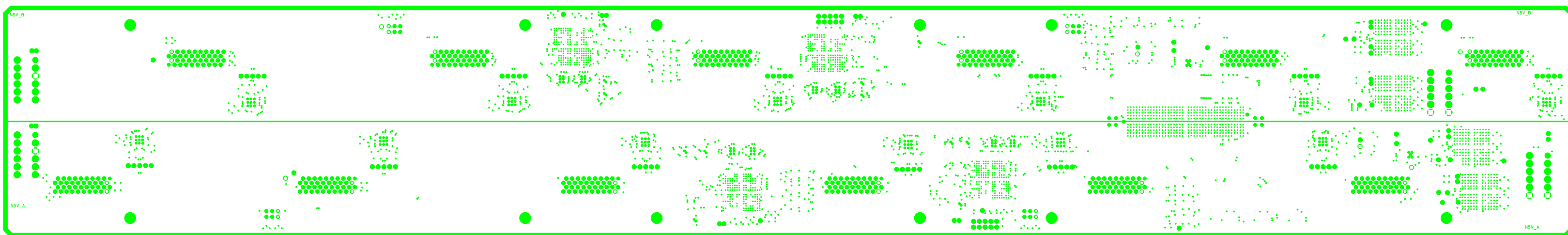
Artwork_5: POWER_3, POWER_4 (P5V_A, P5V_B)



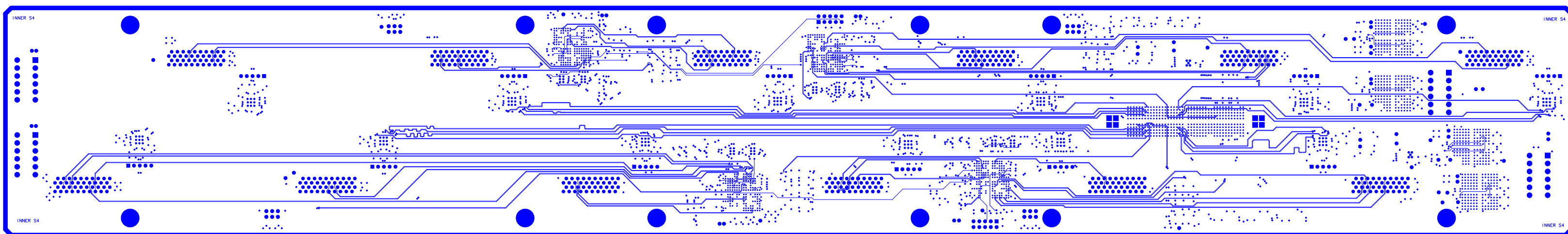
Artwork_6: POWER_1, POWER_2, POWER_11, POWER_12 (P10V_A, P10v_B, P1V2_A, P1V2_B)



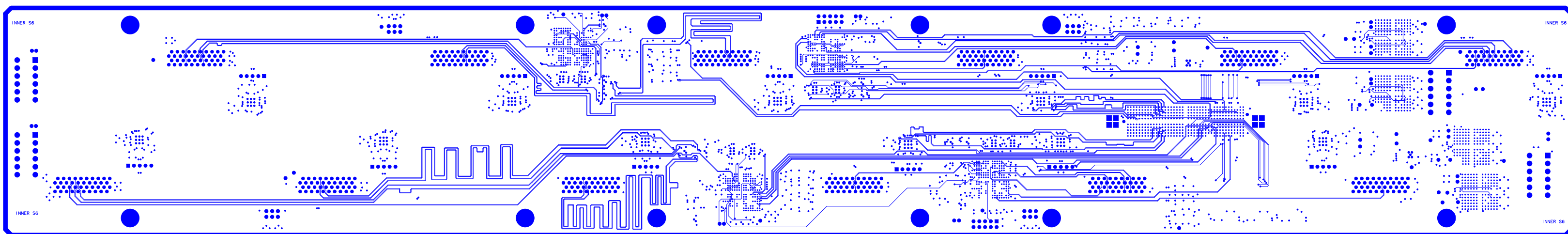
Artwork_7: POWER_7, POWER_8, POWER_9, POWER_10 (P2V5_A, P2V5_B, P1V8A_A, P1V8A_B)



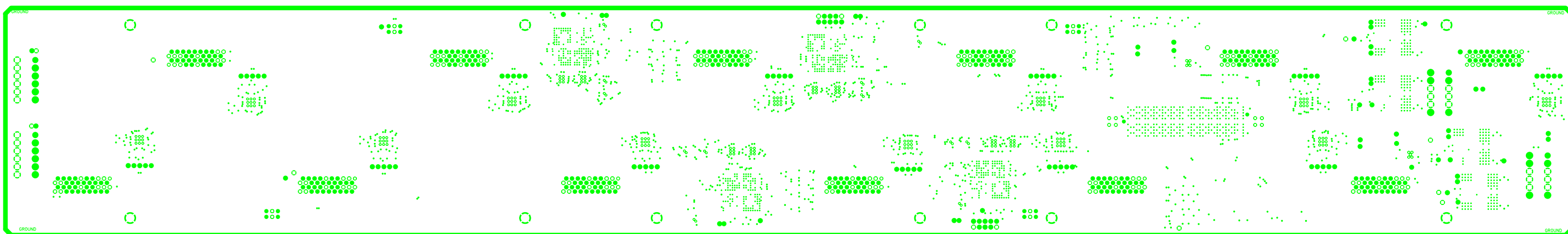
Artwor_8: POWER_5, POWER_6 (N5V_A, N5V_B)



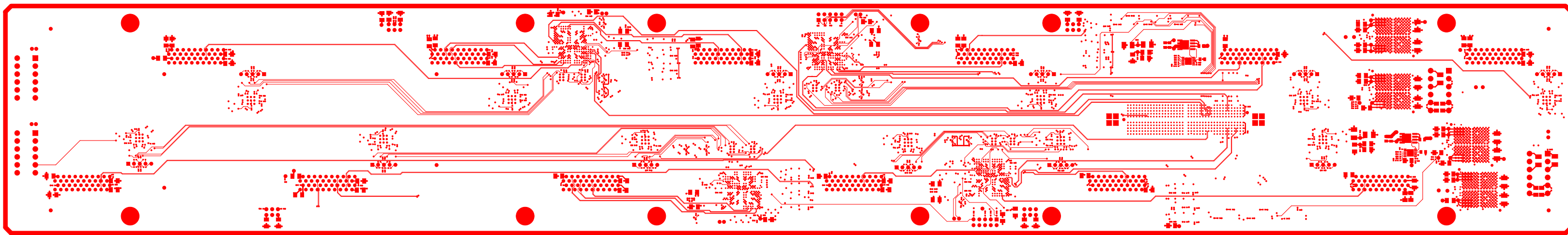
Artwork_9: SIGNAL_4



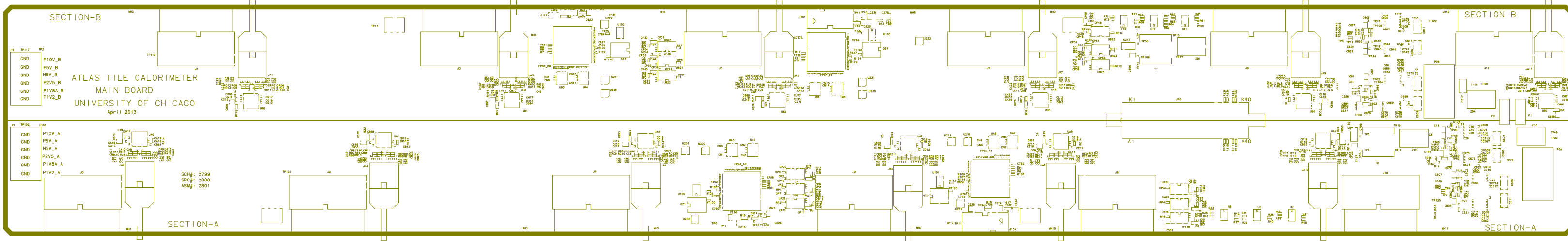
Artwork_10: Signal_6



Artwork_11: POWER_15 (GROUND)

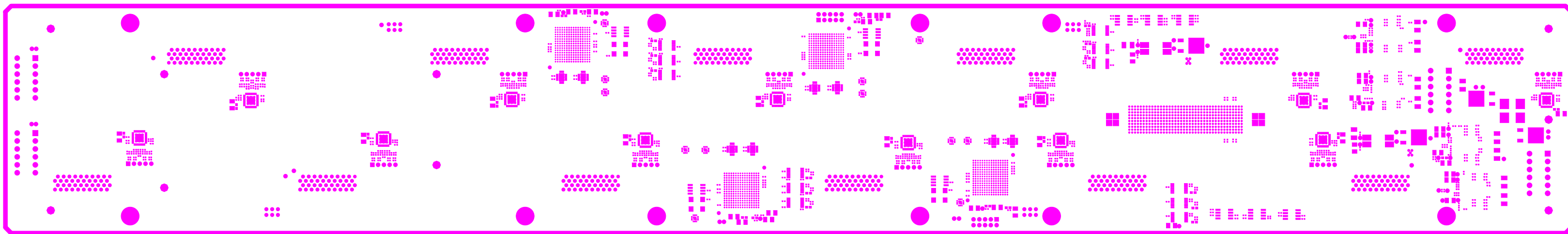


Artwork_12: Bottom Signal Layer

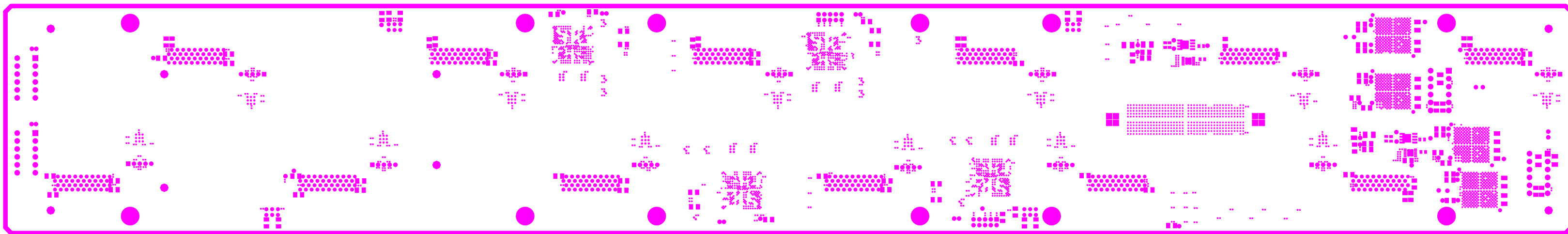


Artwork_13/Top Silkscreen

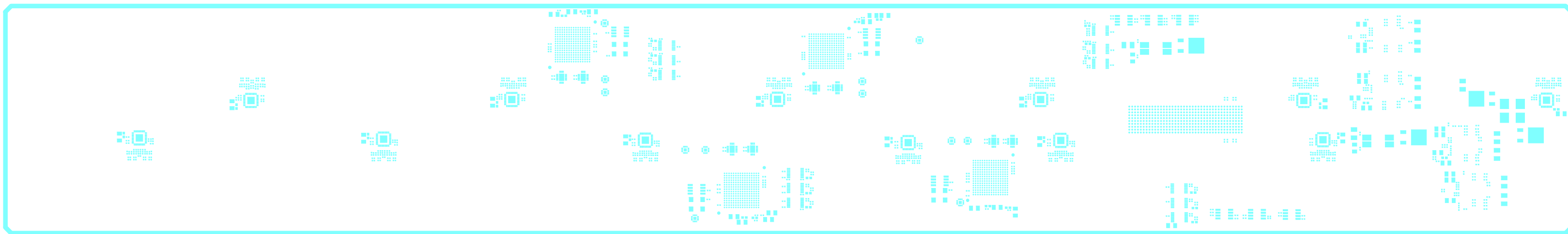




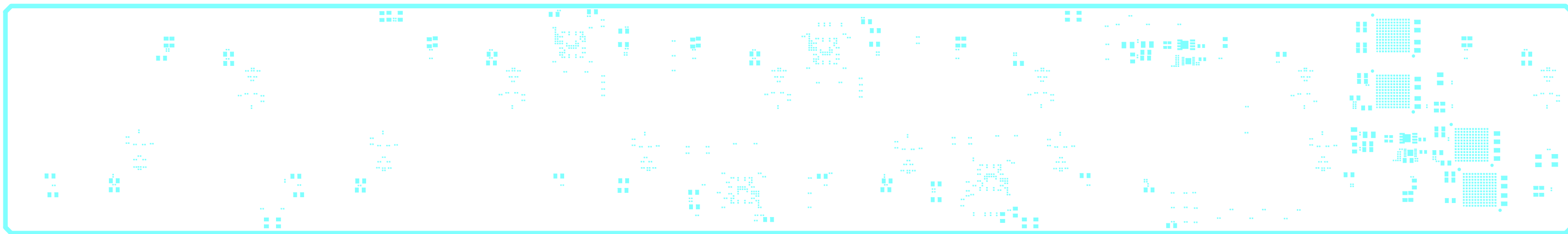
Artwork_15/Top Solder_Mask



Artwork_16\Bot 2older_Mask



Artwork_17/Top_Paste_Mask



Artwork_18\Bot Paste_Mask

