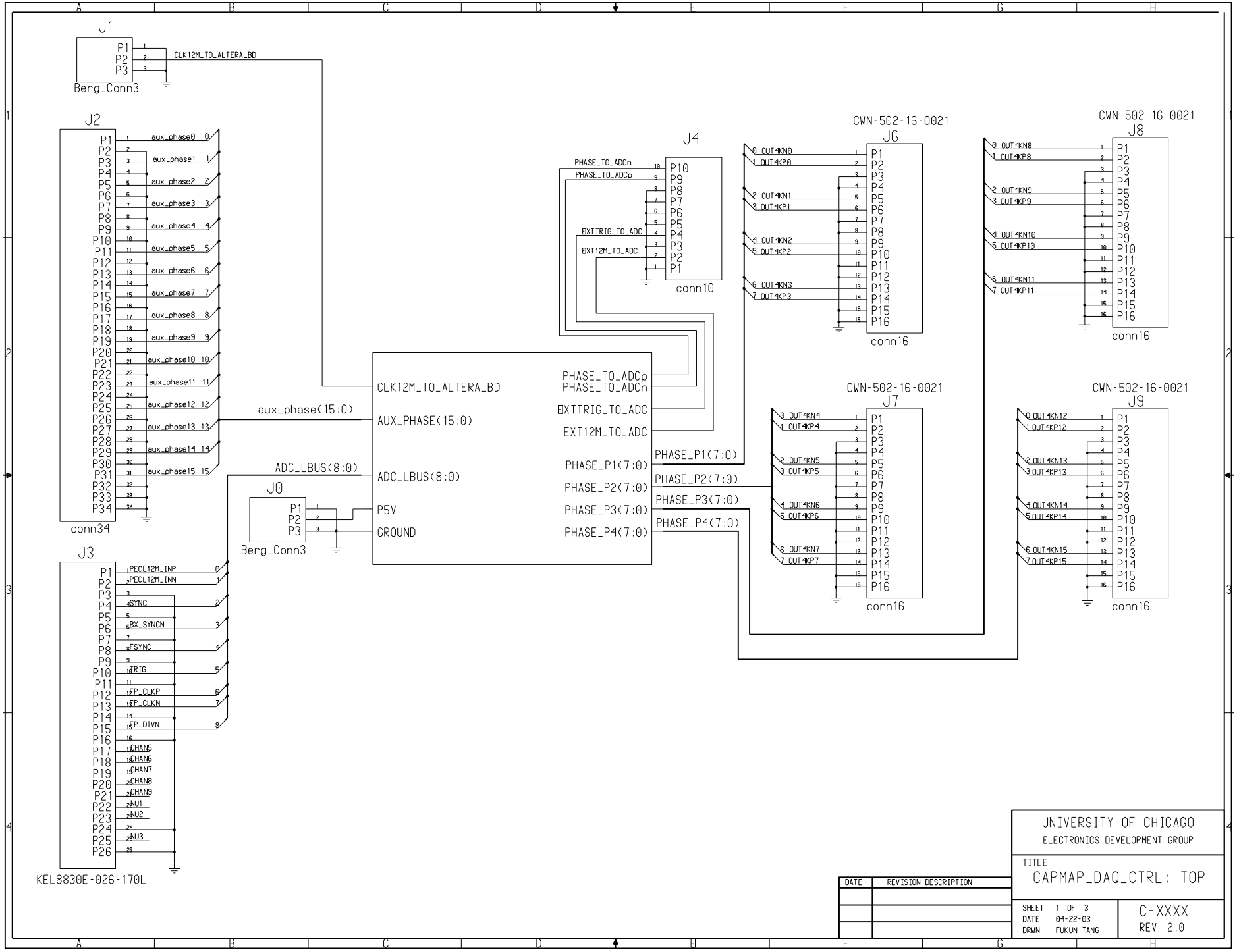
J1
Berg_Conn3
CLK12M_TO_ALTERA_BD
J2
conn34
aux_phase0
aux_phase1
aux_phase2
aux_phase3
aux_phase4
aux_phase5
aux_phase6
aux_phase7
aux_phase8
aux_phase9
aux_phase10
aux_phase11
aux_phase12
aux_phase13
aux_phase14
aux_phase15
aux_phase(15:0)
ADC_LBUS(8:0)
J0
Berg_Conn3
J3
KEL8830E-026-170L
PECL12M_INP
PECL12M_INN
SYNC
BX_SYNCN
FSYNC
TRIG
FP_CLKP
FP_CLKN
FP_DIVN
CHAN5
CHAN6
CHAN7
CHAN8
CHAN9
NU1
NU2
NU3
CLK12M_TO_ALTERA_BD
AUX_PHASE(15:0)
ADC_LBUS(8:0)
P5V
GROUND
PHASE_TO_ADCp
PHASE_TO_ADCn
EXTTRIG_TO_ADC
EXT12M_TO_ADC
PHASE_P1(7:0)
PHASE_P2(7:0)
PHASE_P3(7:0)
PHASE_P4(7:0)
J4
conn10
CWN-502-16-0021
J6
J7
J8
J9
conn16
OUT4KN0
OUT4KP0
OUT4KN1
OUT4KP1
OUT4KN2
OUT4KP2
OUT4KN3
OUT4KP3
OUT4KN4
OUT4KP4
OUT4KN5
OUT4KP5
OUT4KN6
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OUT4KP11
OUT4KN12
OUT4KP12
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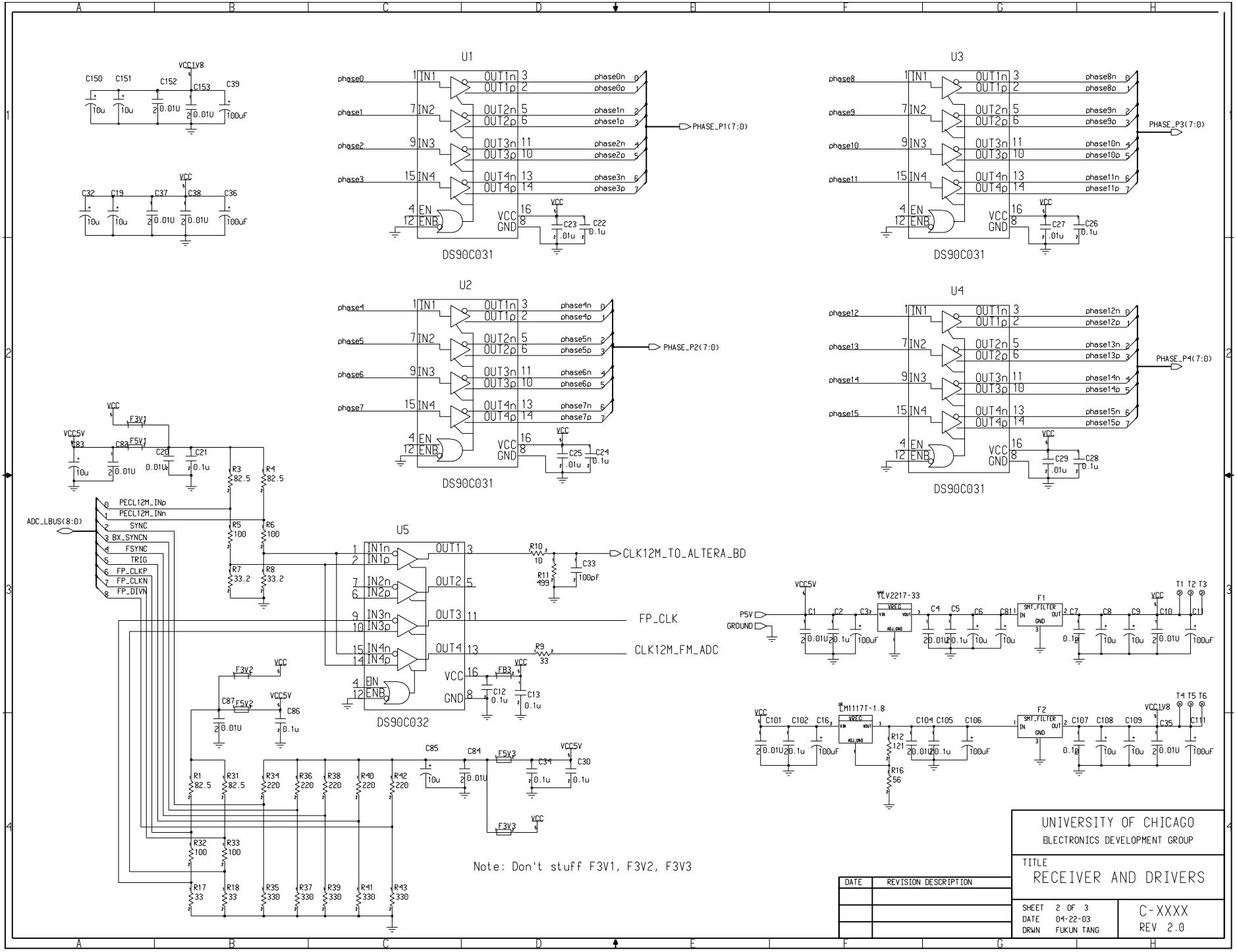
OUT4KP13
OUT4KN14
OUT4KP14
OUT4KN15
OUT4KP15
UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP
TITLE
CAPMAP_DAQ_CTRL: TOP
SHEET 1 OF 3
DATE 04-22-03
DRWN FUKUN TANG
C-XXXX
REV 2.0
DATE
REVISION DESCRIPTION



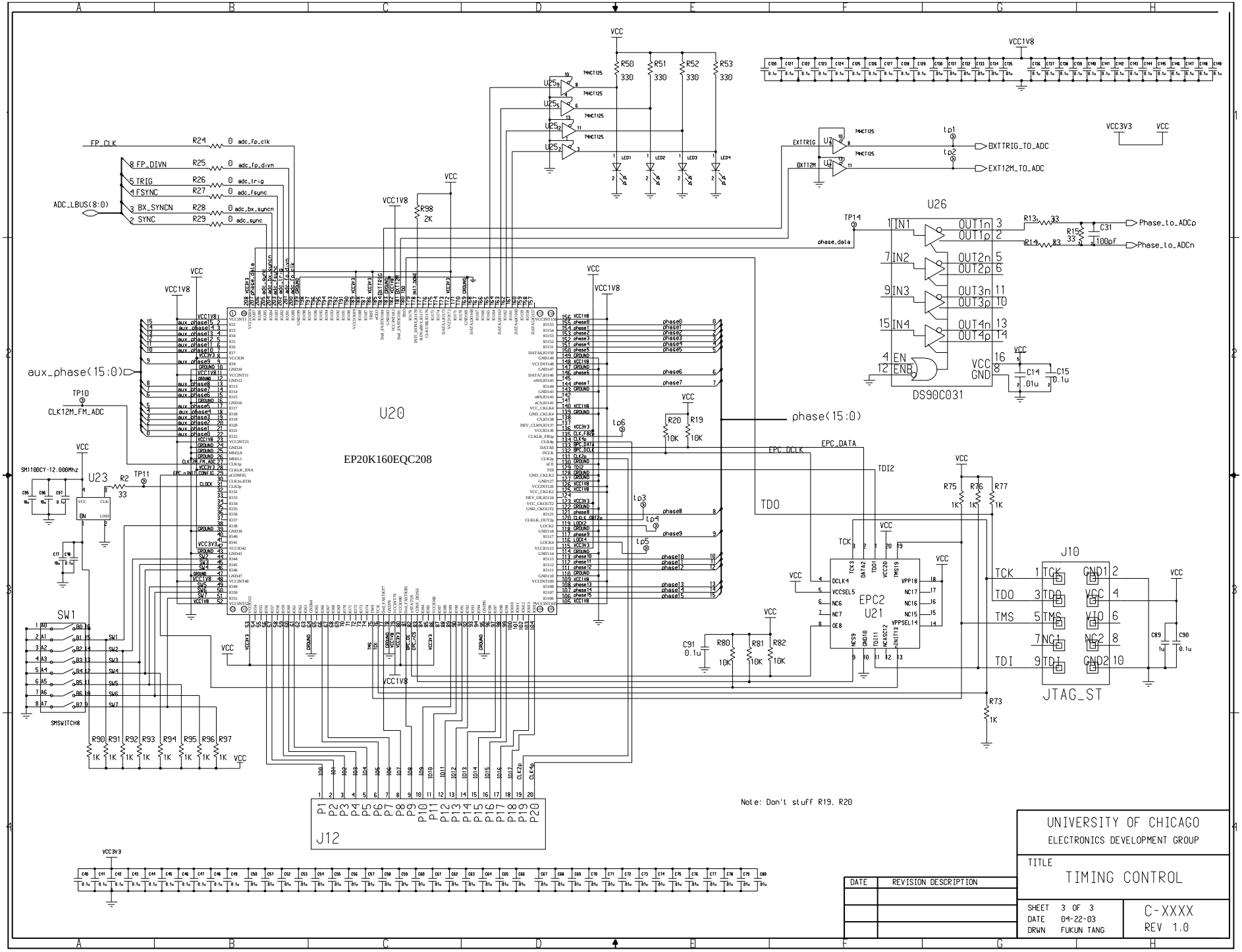
UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE
RECEIVER AND DRIVERS

DATE	REVISION DESCRIPTION

SHEET 2 OF 3
DATE 04-22-03
DRWN FUKUN TANG

C-XXXX
REV 2.0



UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE
TIMING CONTROL

DATE	REVISION DESCRIPTION

SHEET 3 OF 3
DATE 04-22-03
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REV 1.0